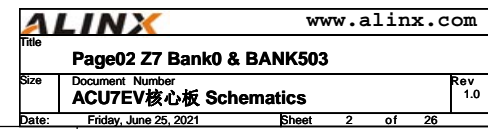
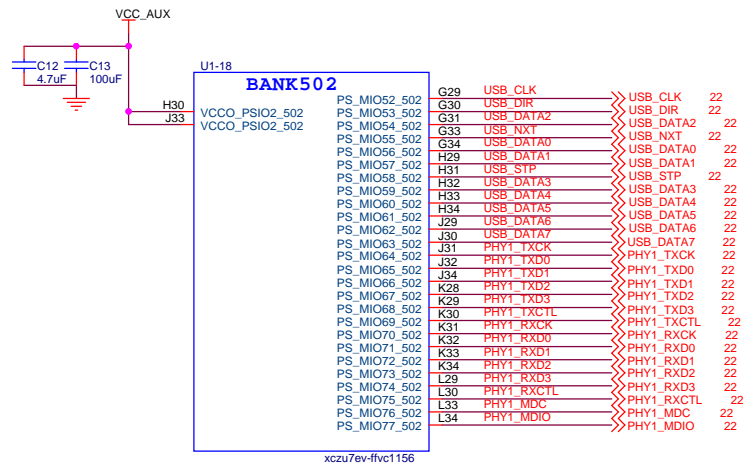
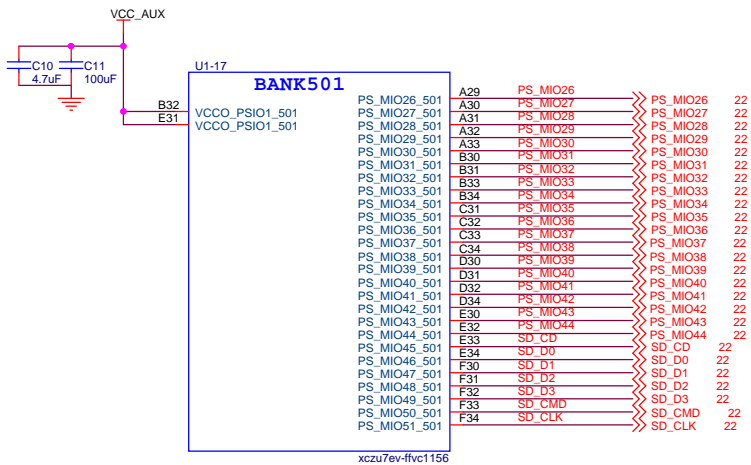
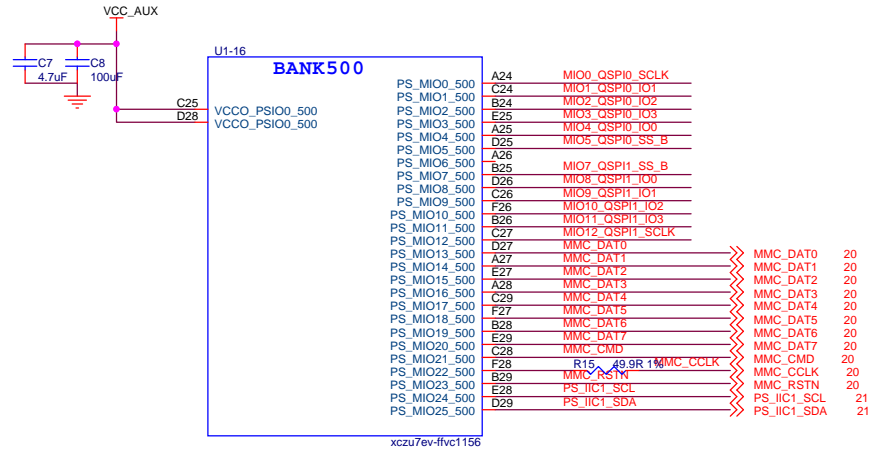
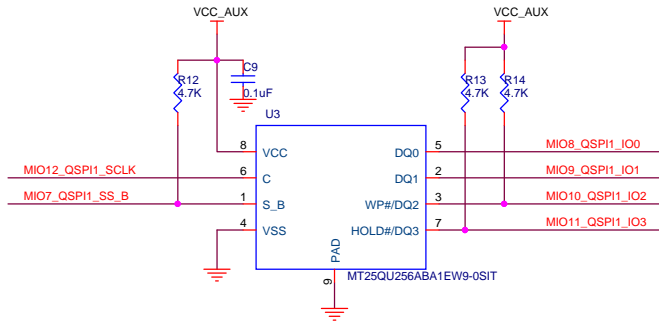
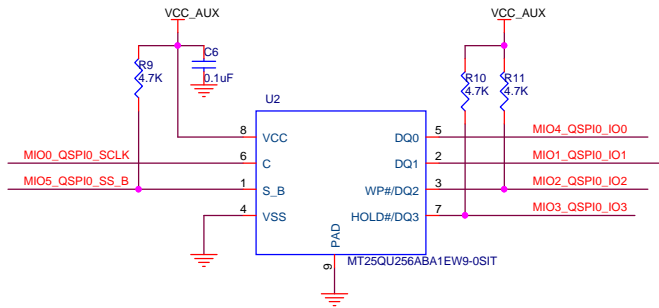
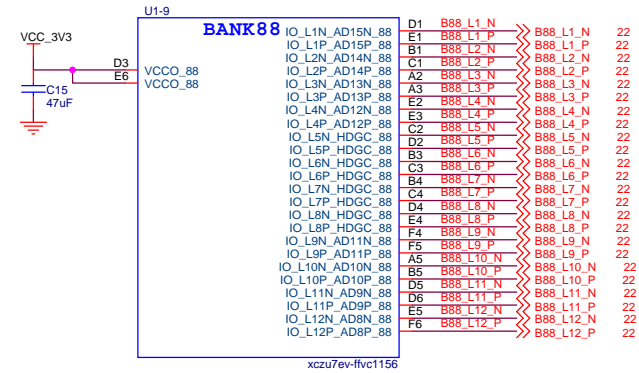
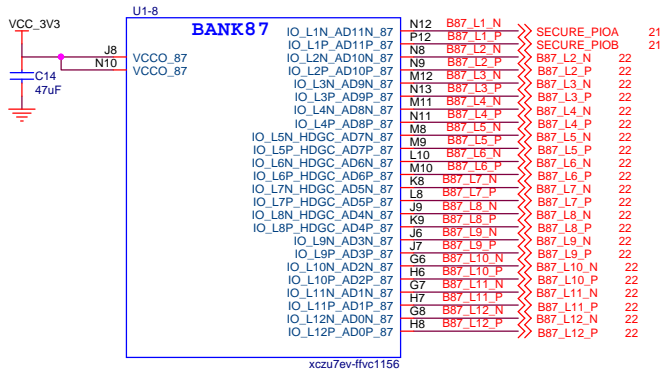




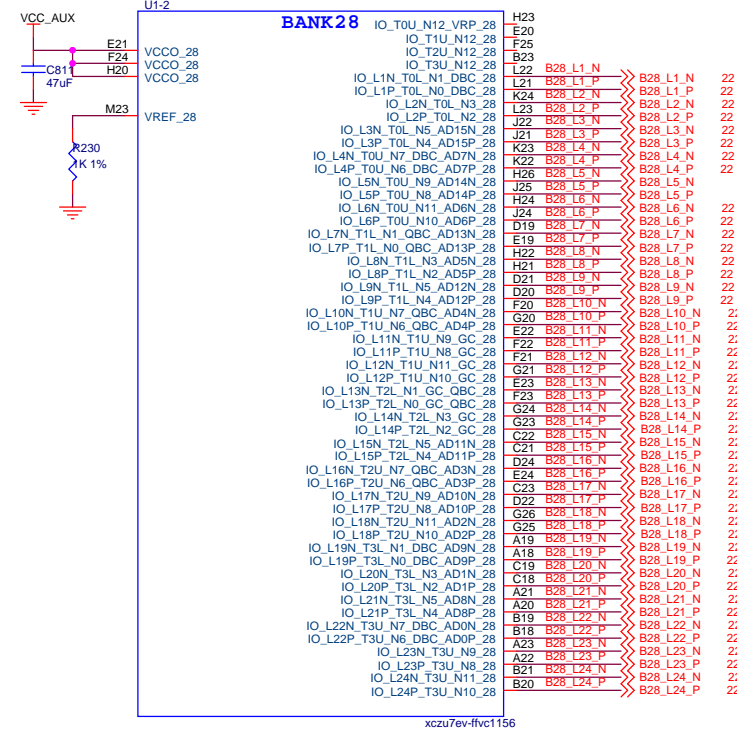
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1110	SD1 LS(3.0)	SD 3.0

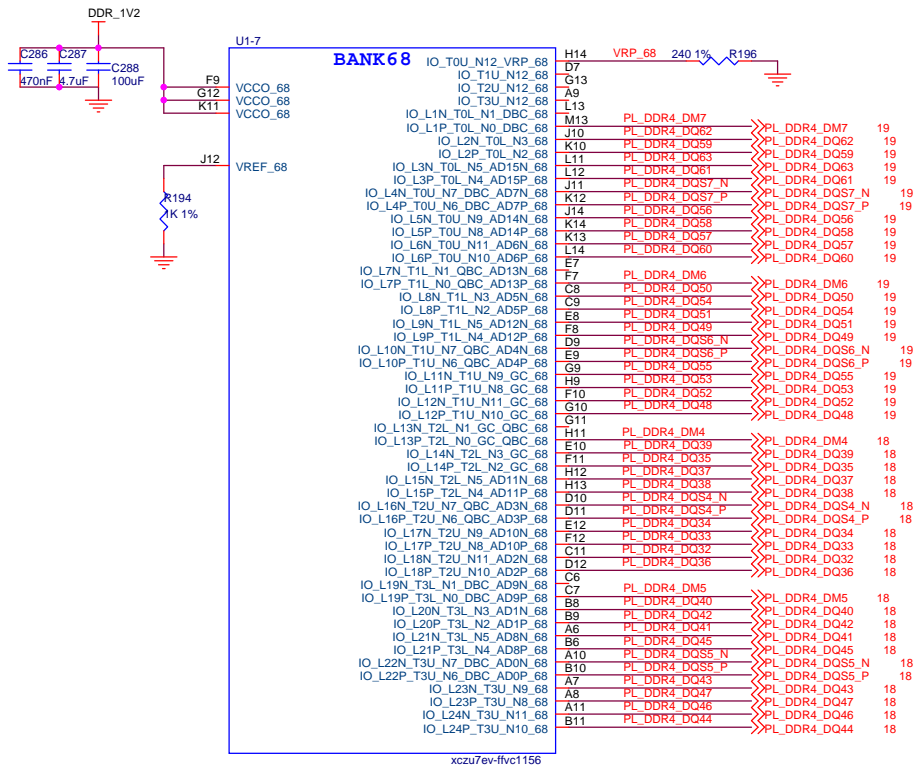
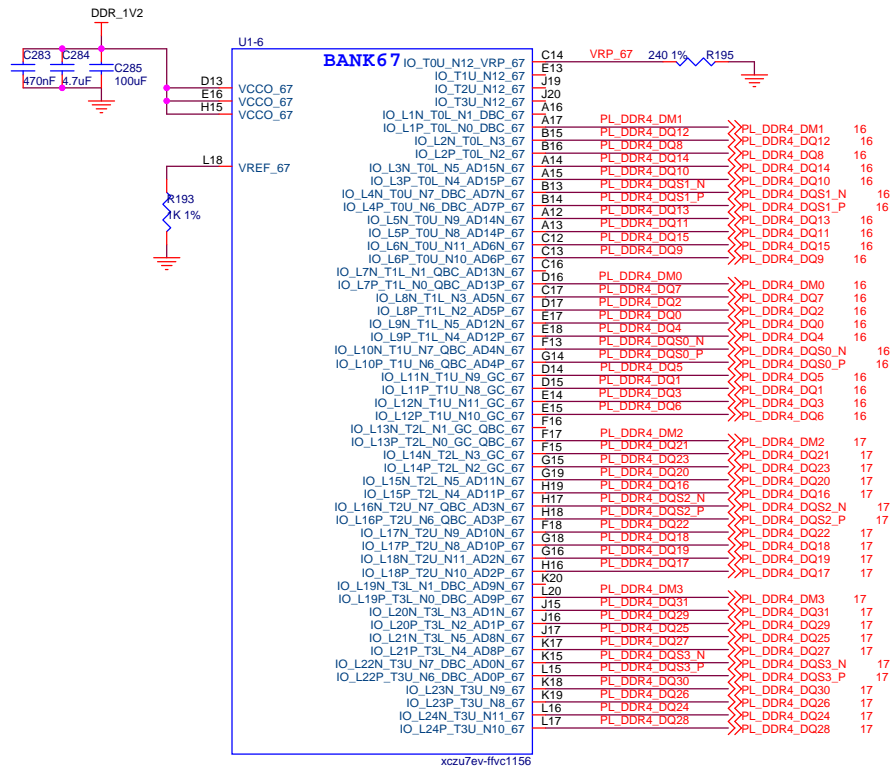




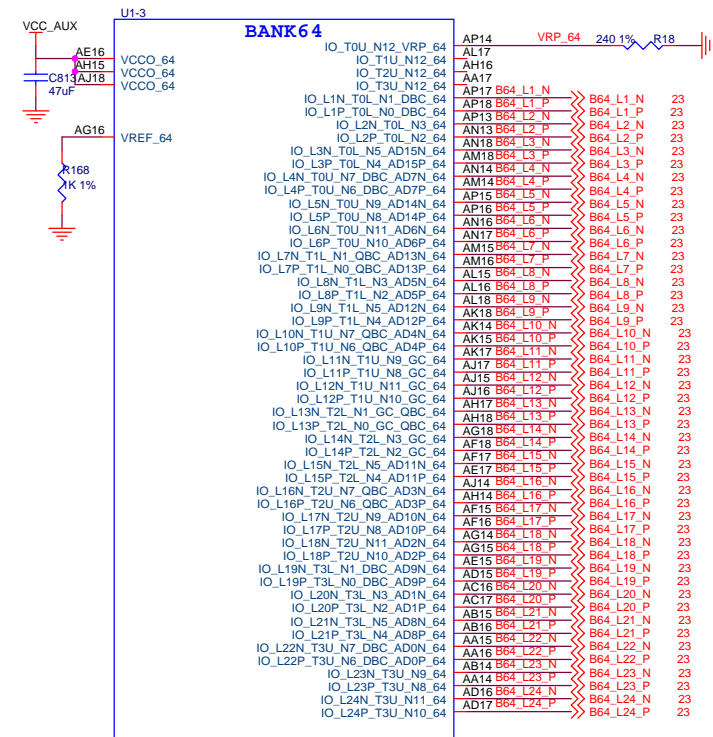


+1.8V

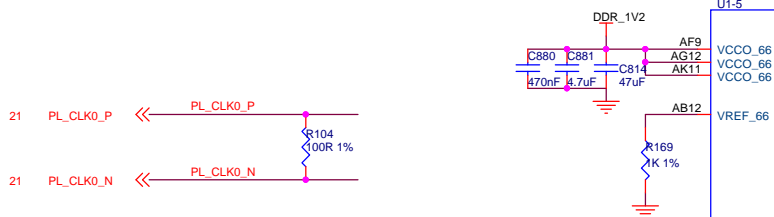
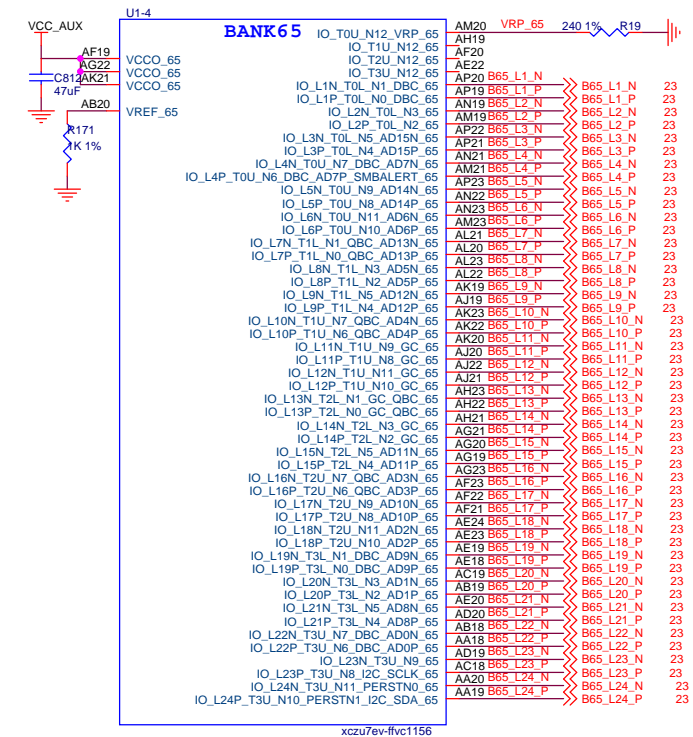




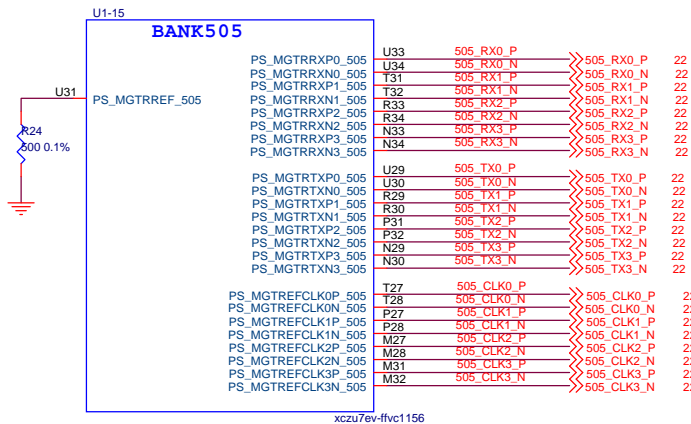
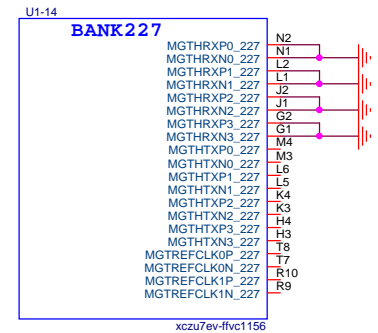
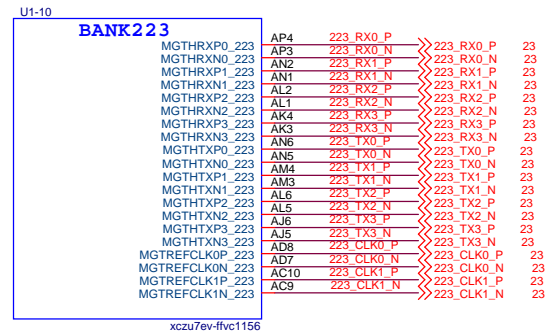
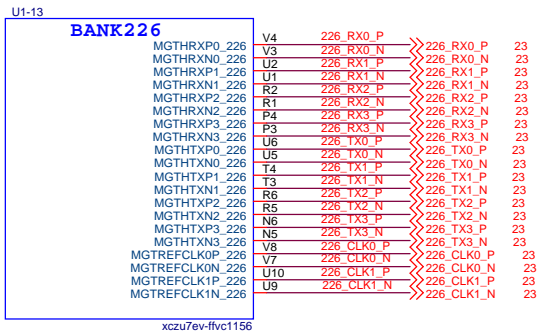
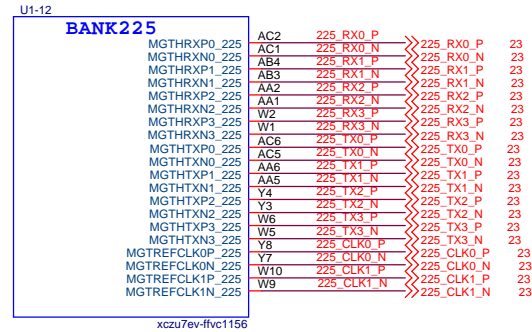
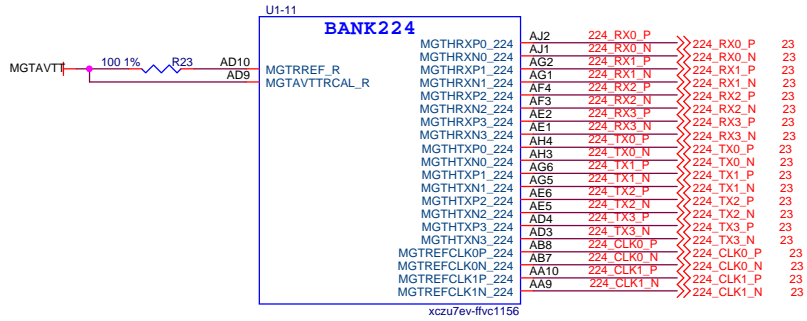
+1.8V



+1.8V



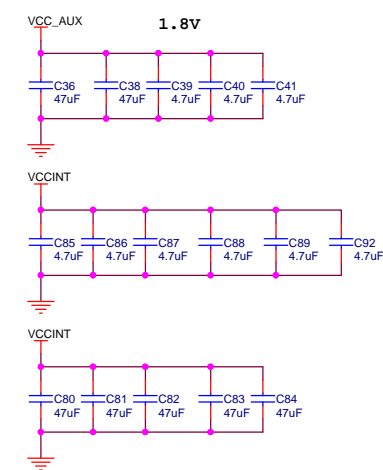
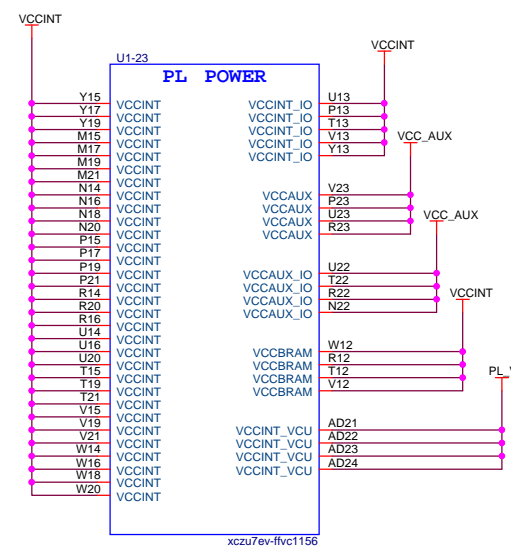
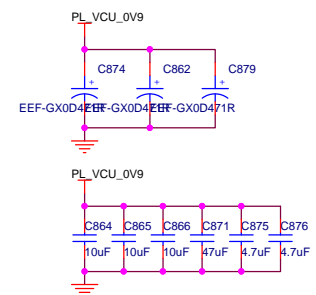
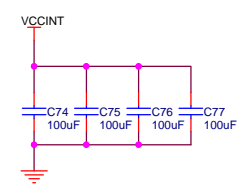
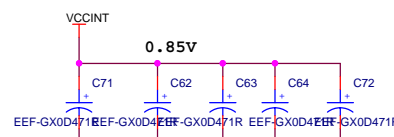
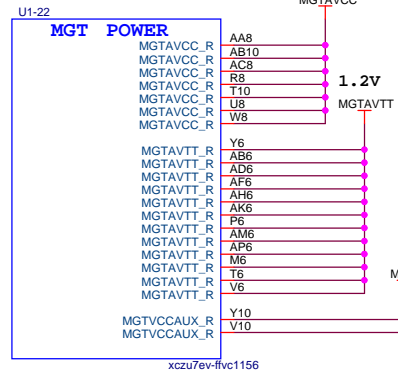
ALINX Confidential








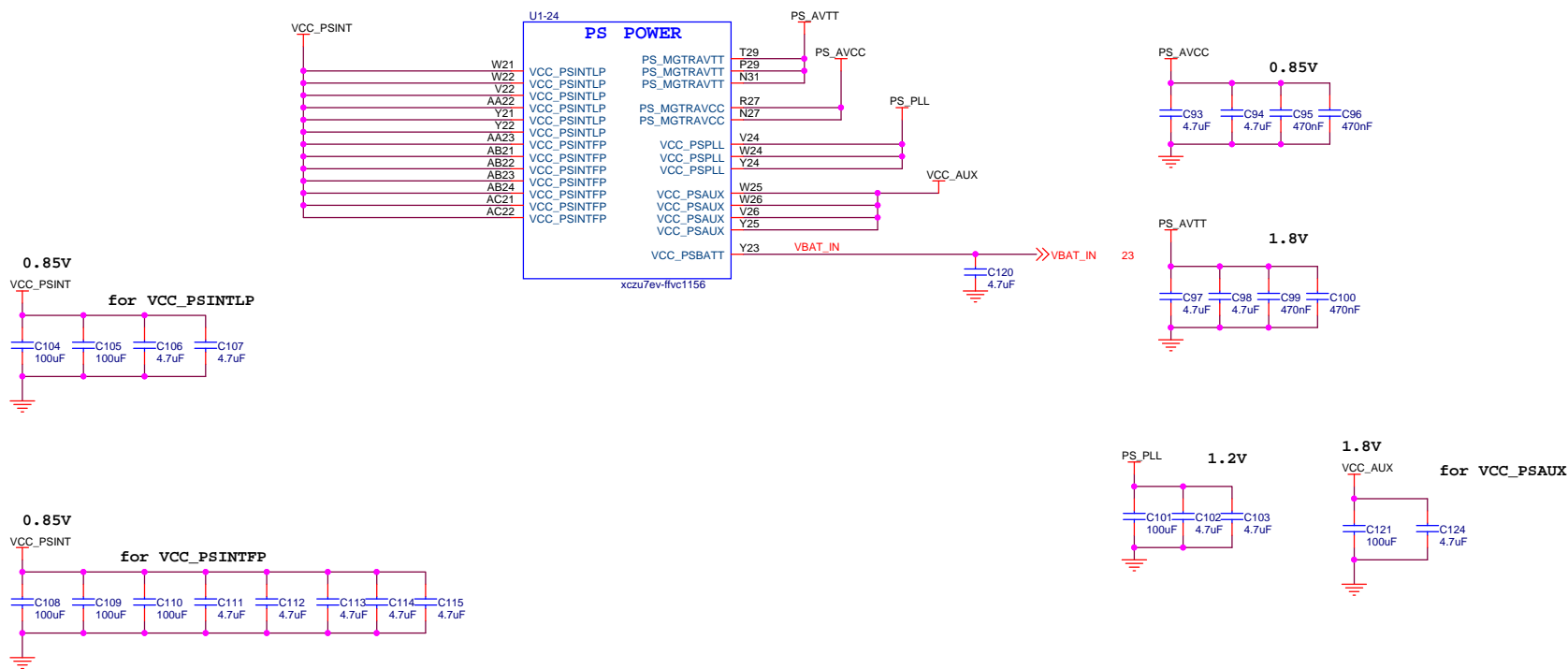




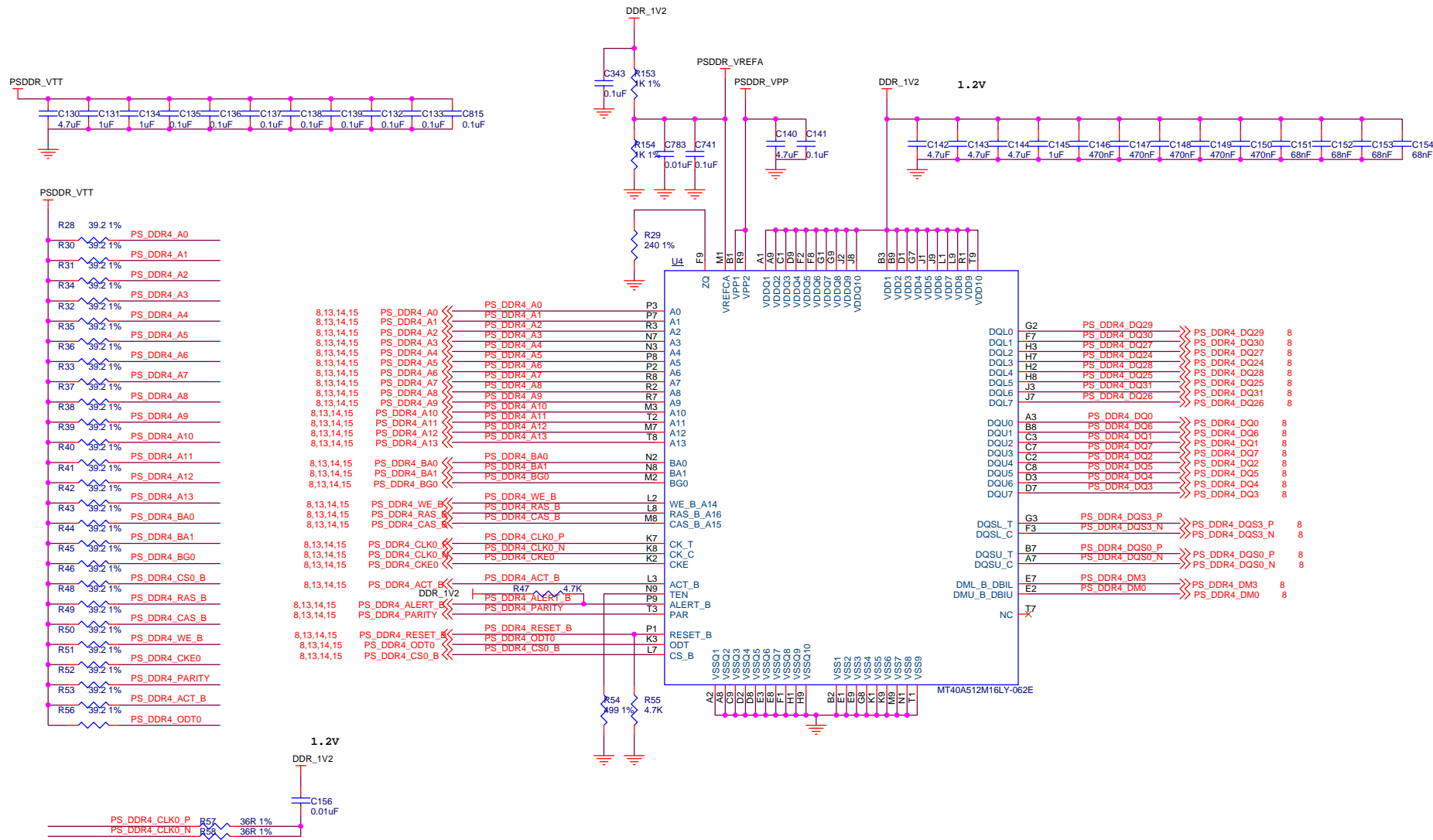
		<a href="http://www.alinx.com">www.alinx.com</a>	
Title		PAGE09 Z7 PL Power1	
Size	Document Number	ACU7EV核心板 Schematics	Rev 1.0
Date:	Friday, June 25, 2021	Sheet	9 of 26

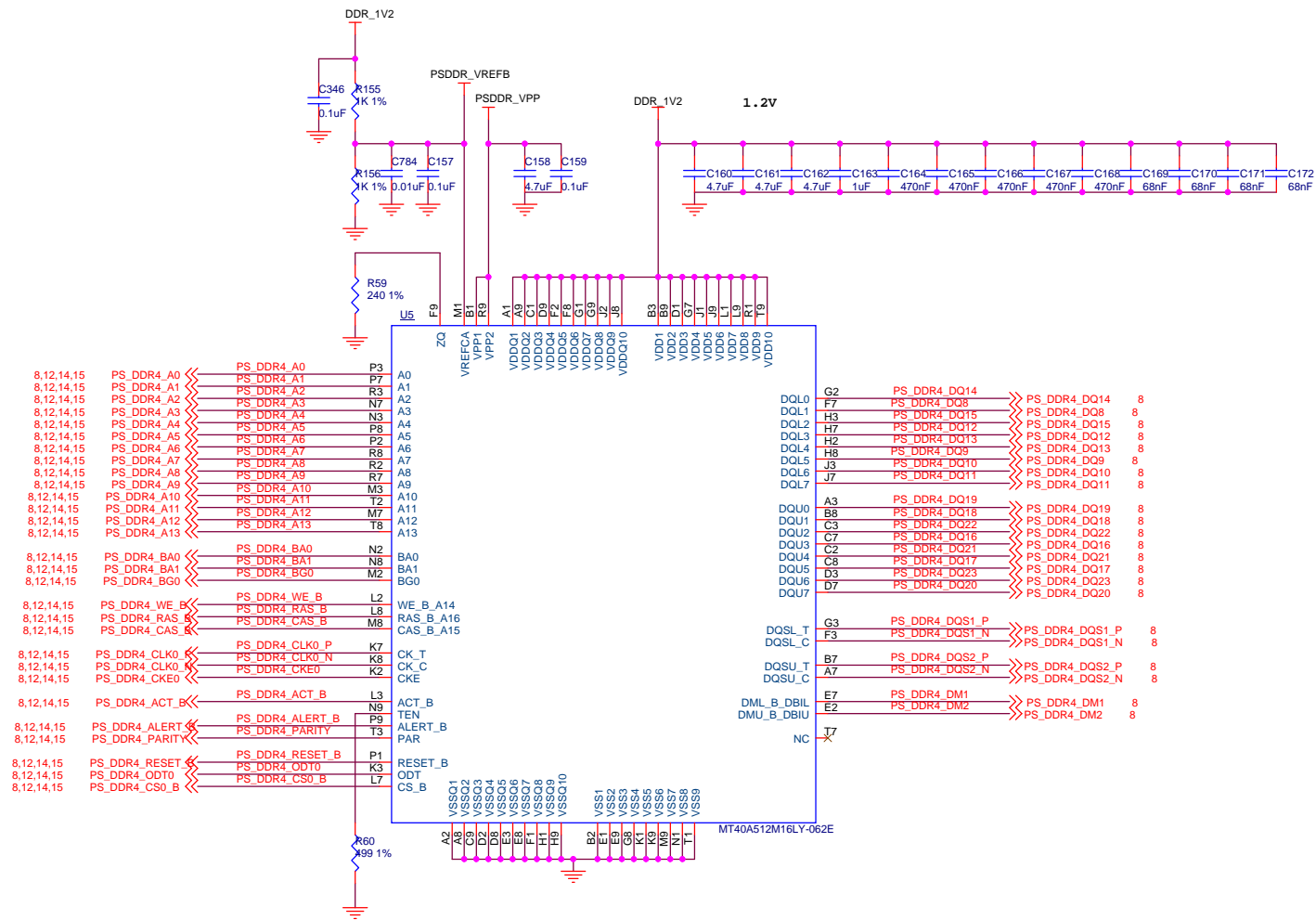
POWER ON: VCC\_PSINTFP/VCC\_PSINTFP\_DDR(+0.85V)->VPS\_MGTRAVCC(+0.9V),VCC\_PSDDR\_PLL(+1.8V)->VPS\_MGTRAVTT(+1.8V),VCCO\_PSDDR( )

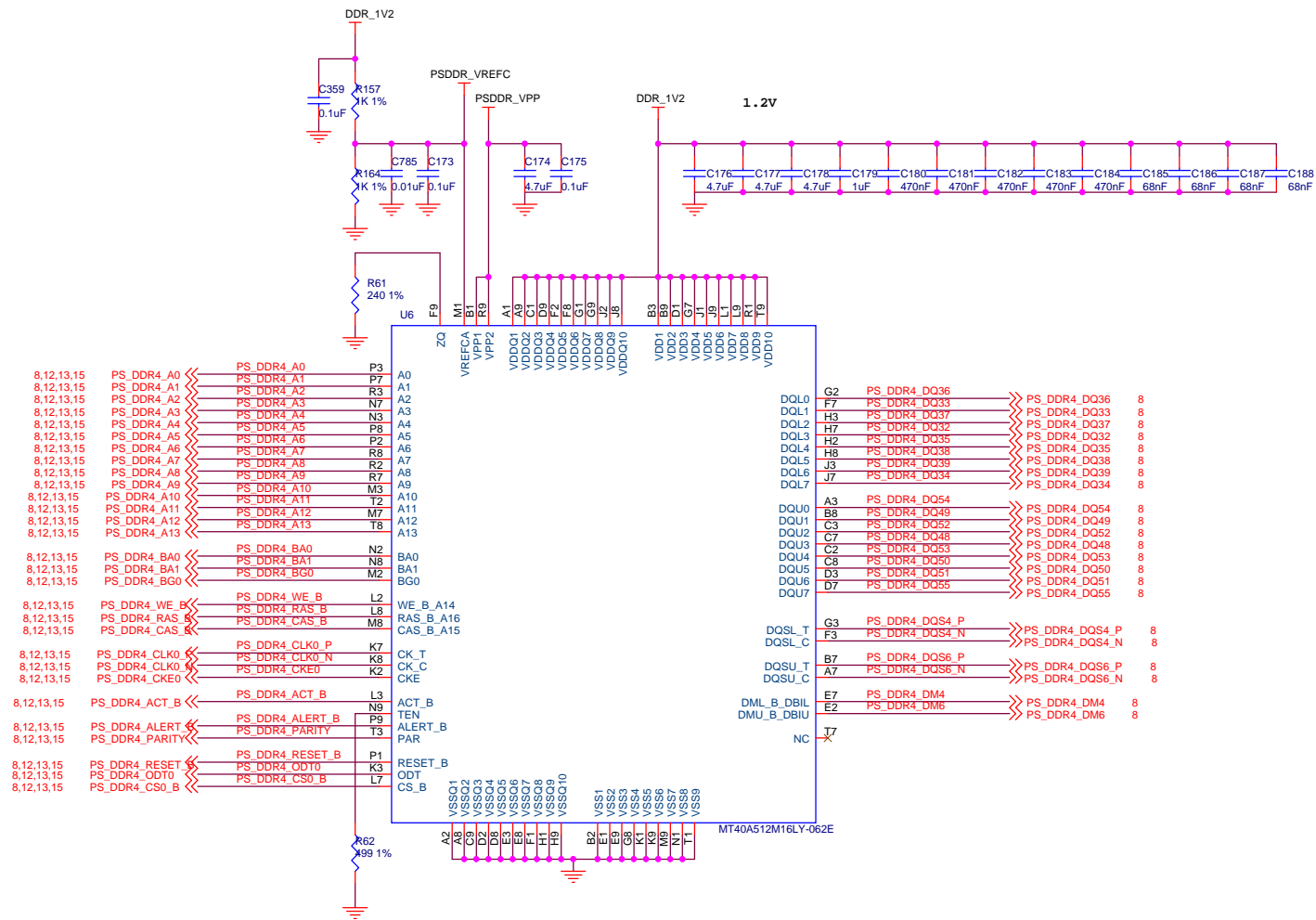
POWER ON: VCC\_PSINTLP(+0.85V)->VCC\_PSAUX(+1.8V),VCC\_PSADC(+1.8V),VCC\_PSPLL(+1.2V)->VCCO\_PSIO(+1.8V)



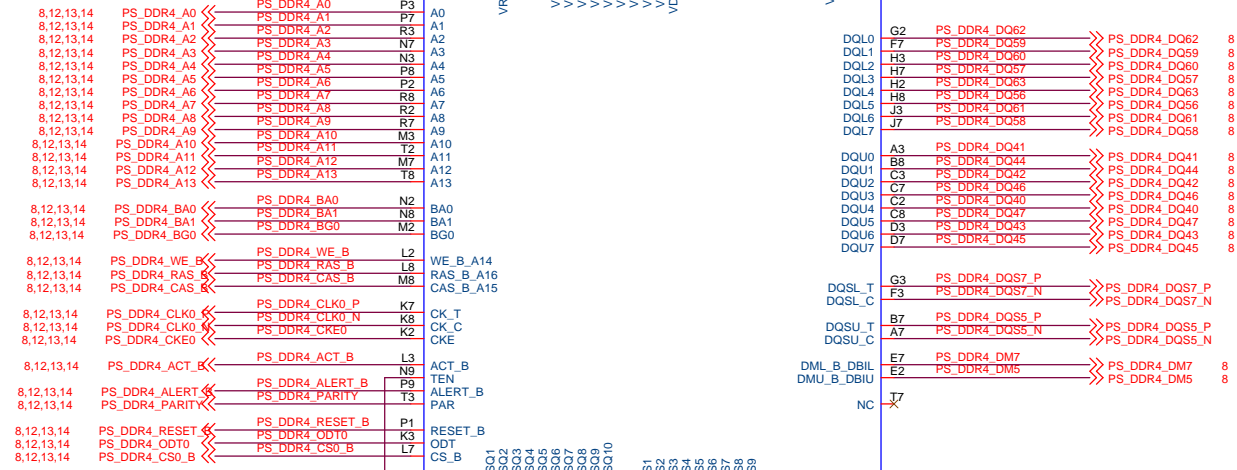




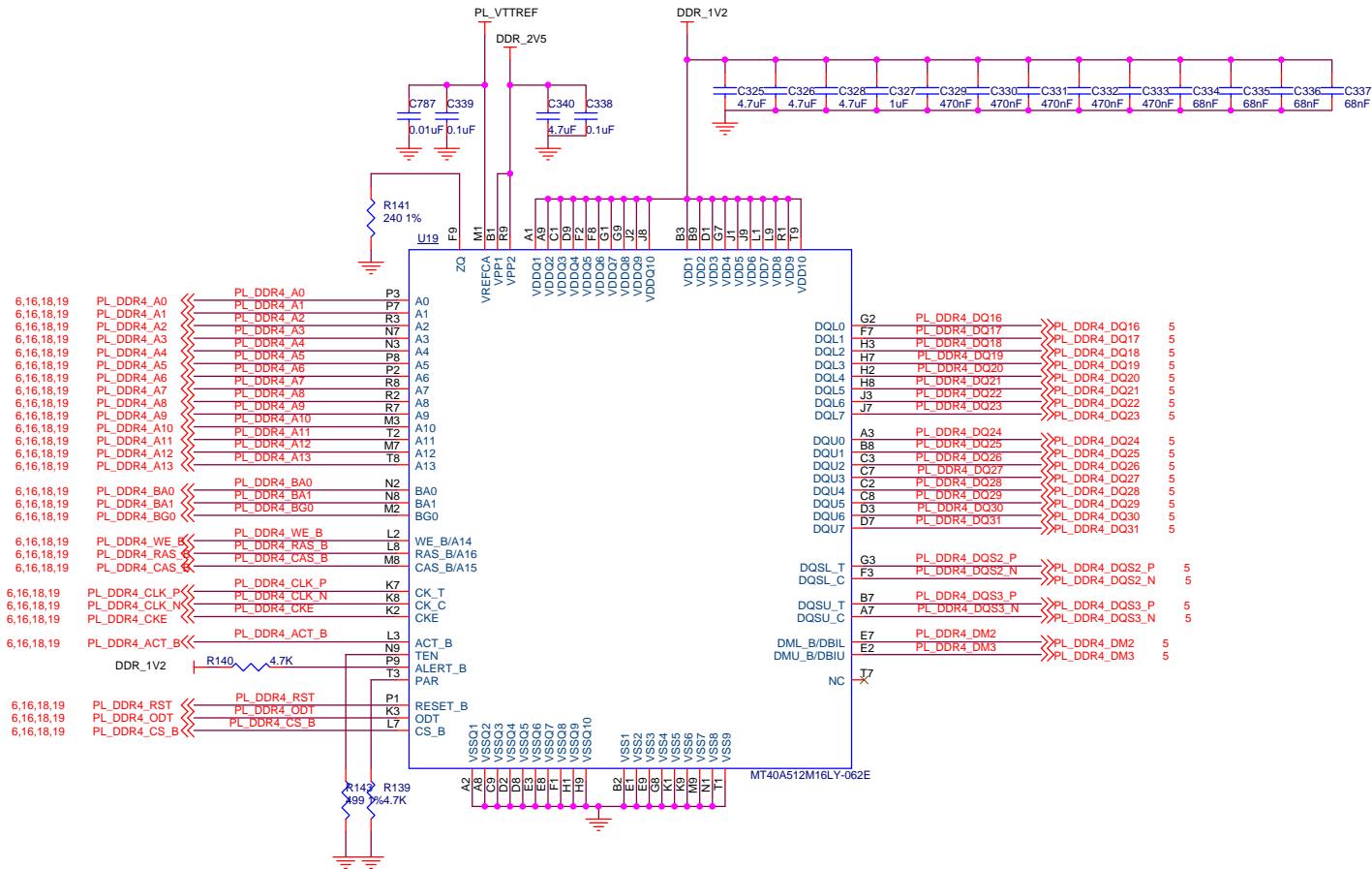


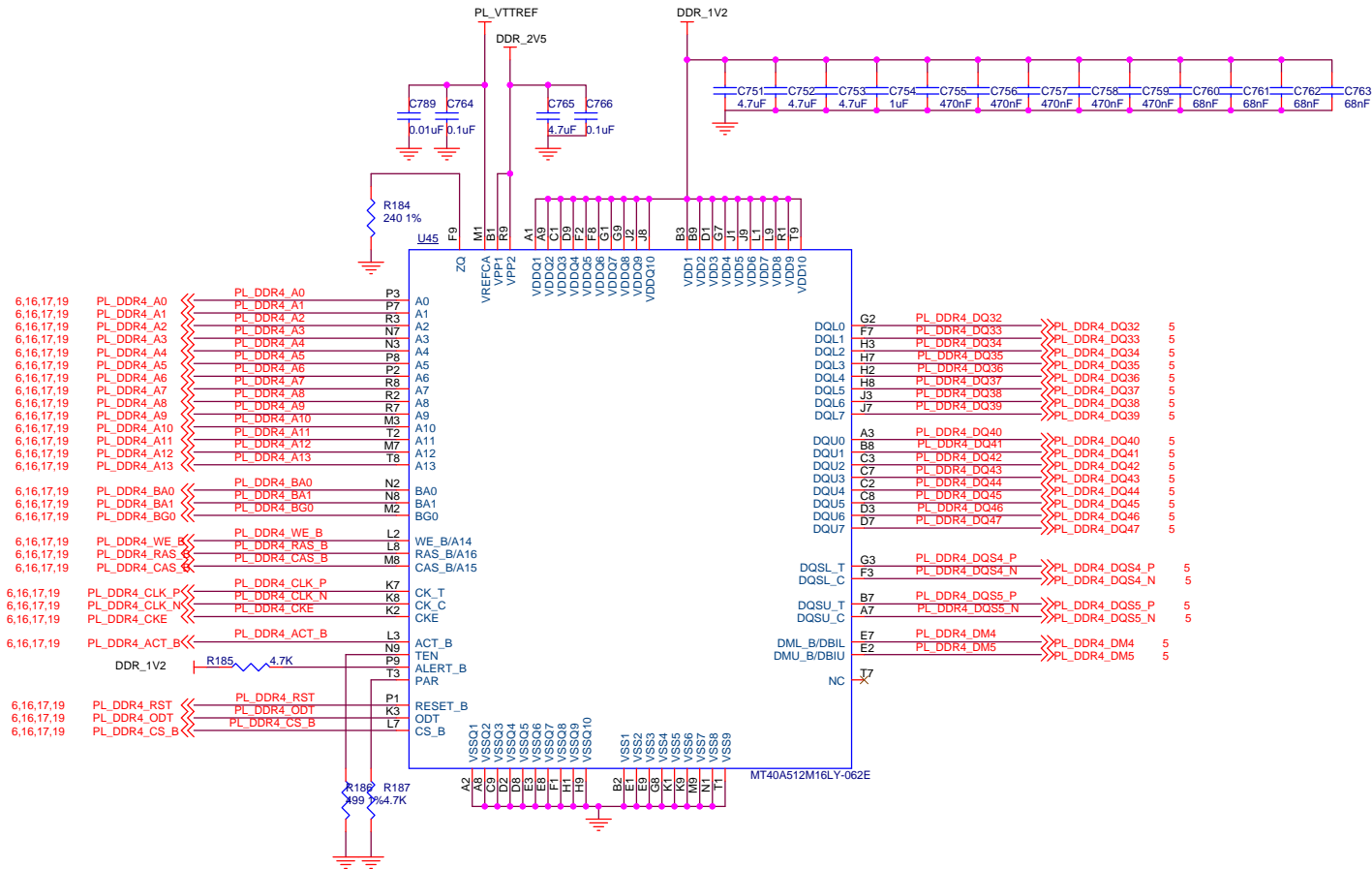


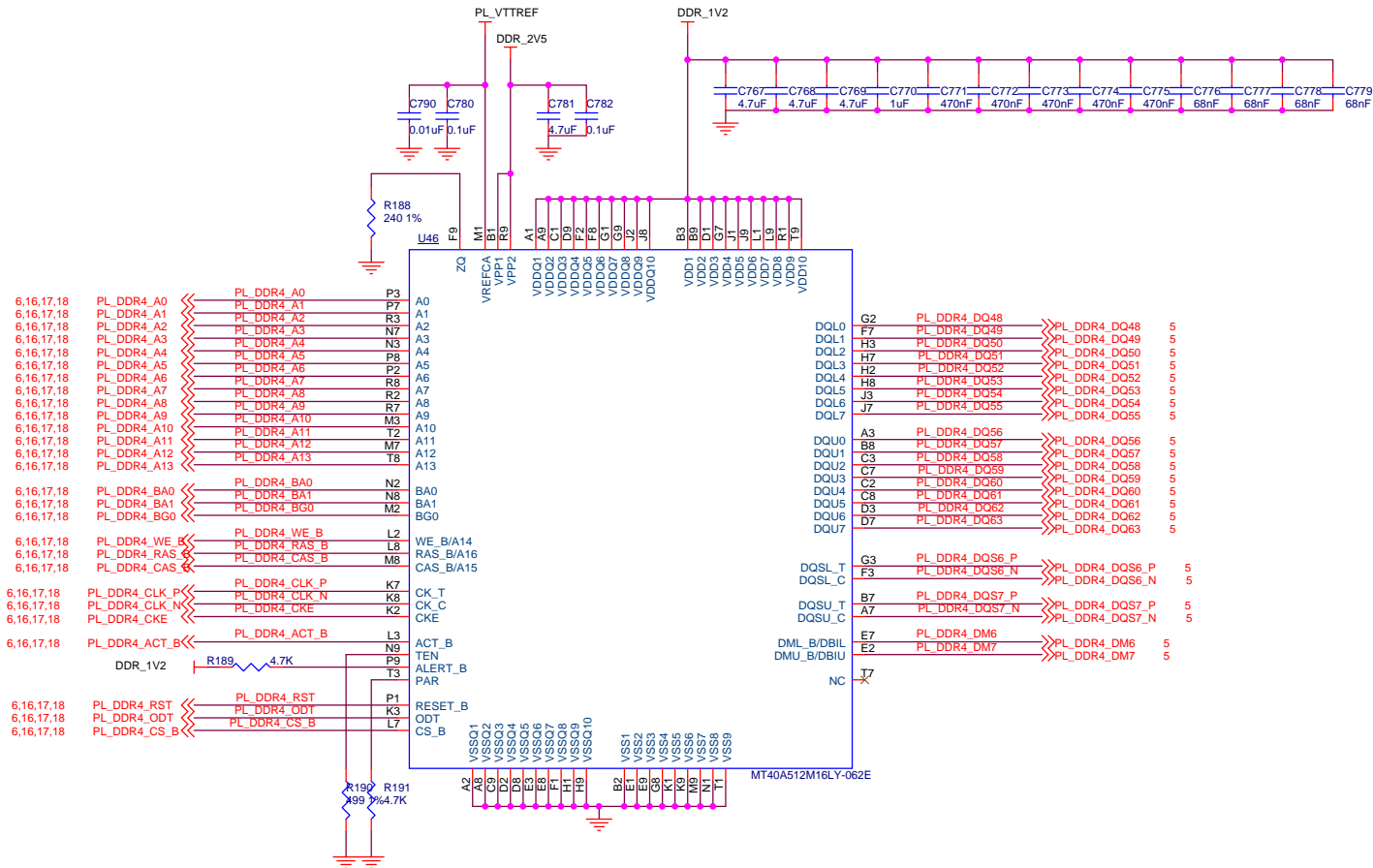


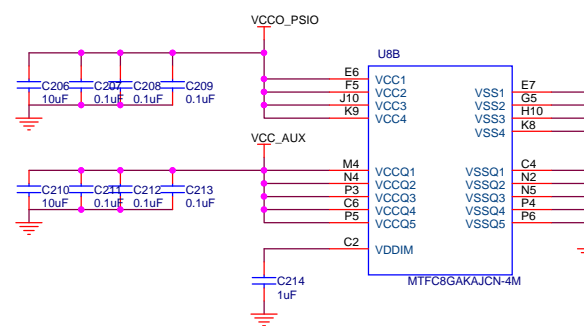
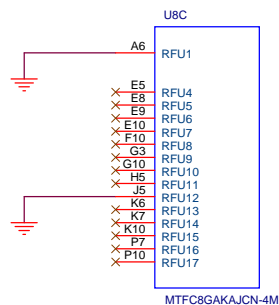
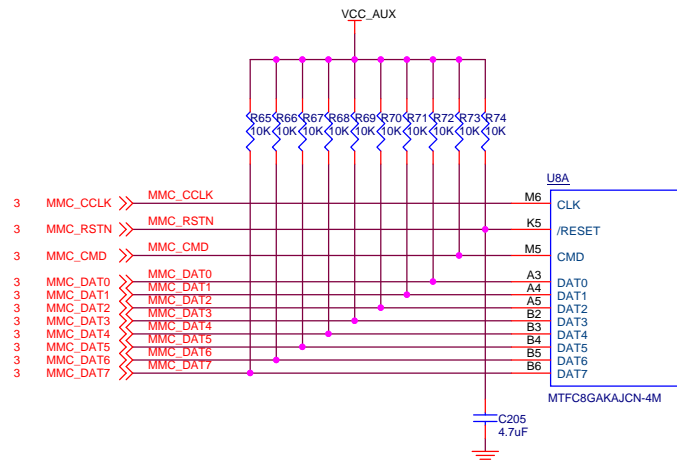
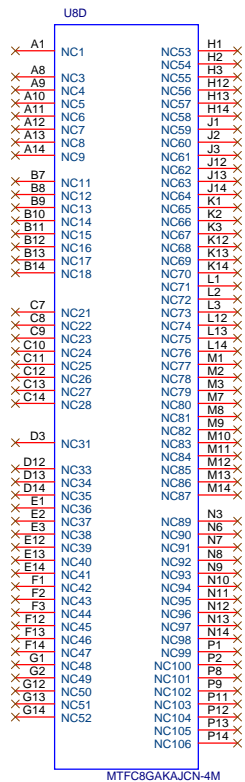






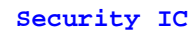


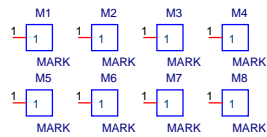






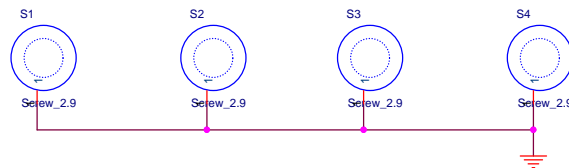
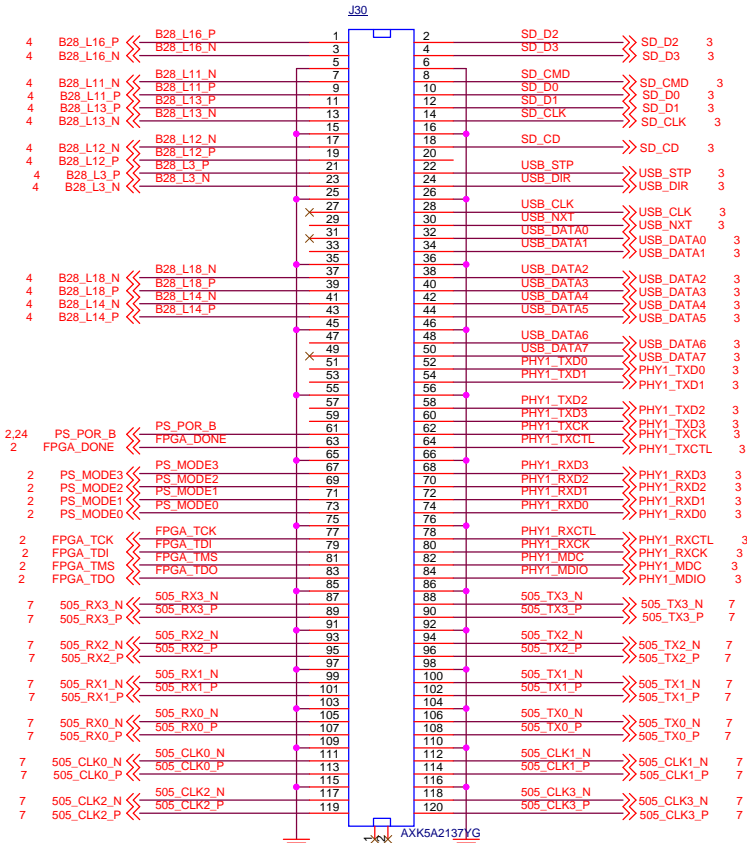
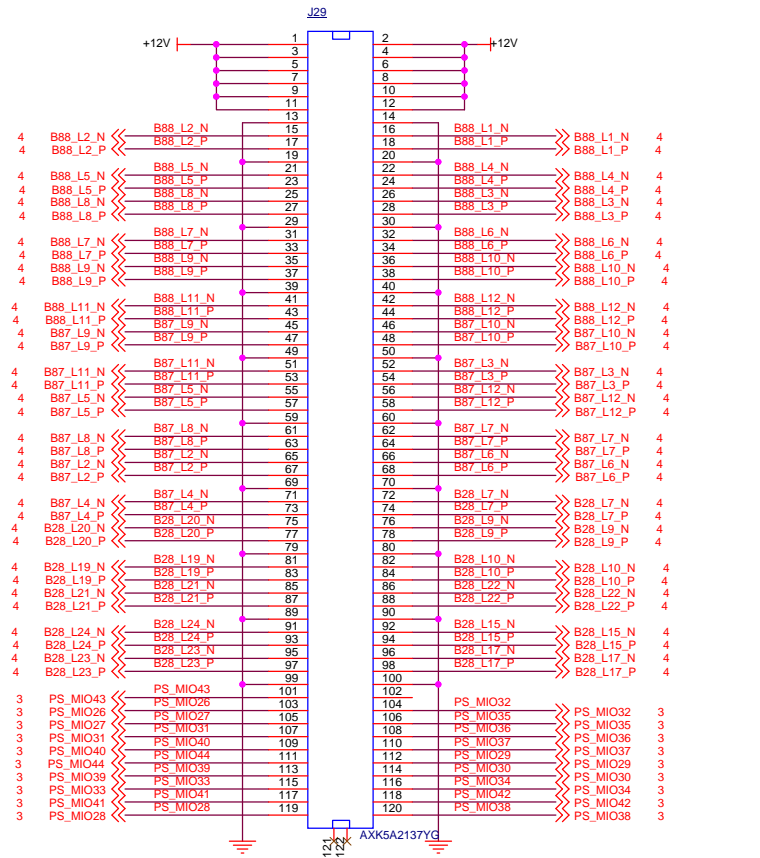
200MHz



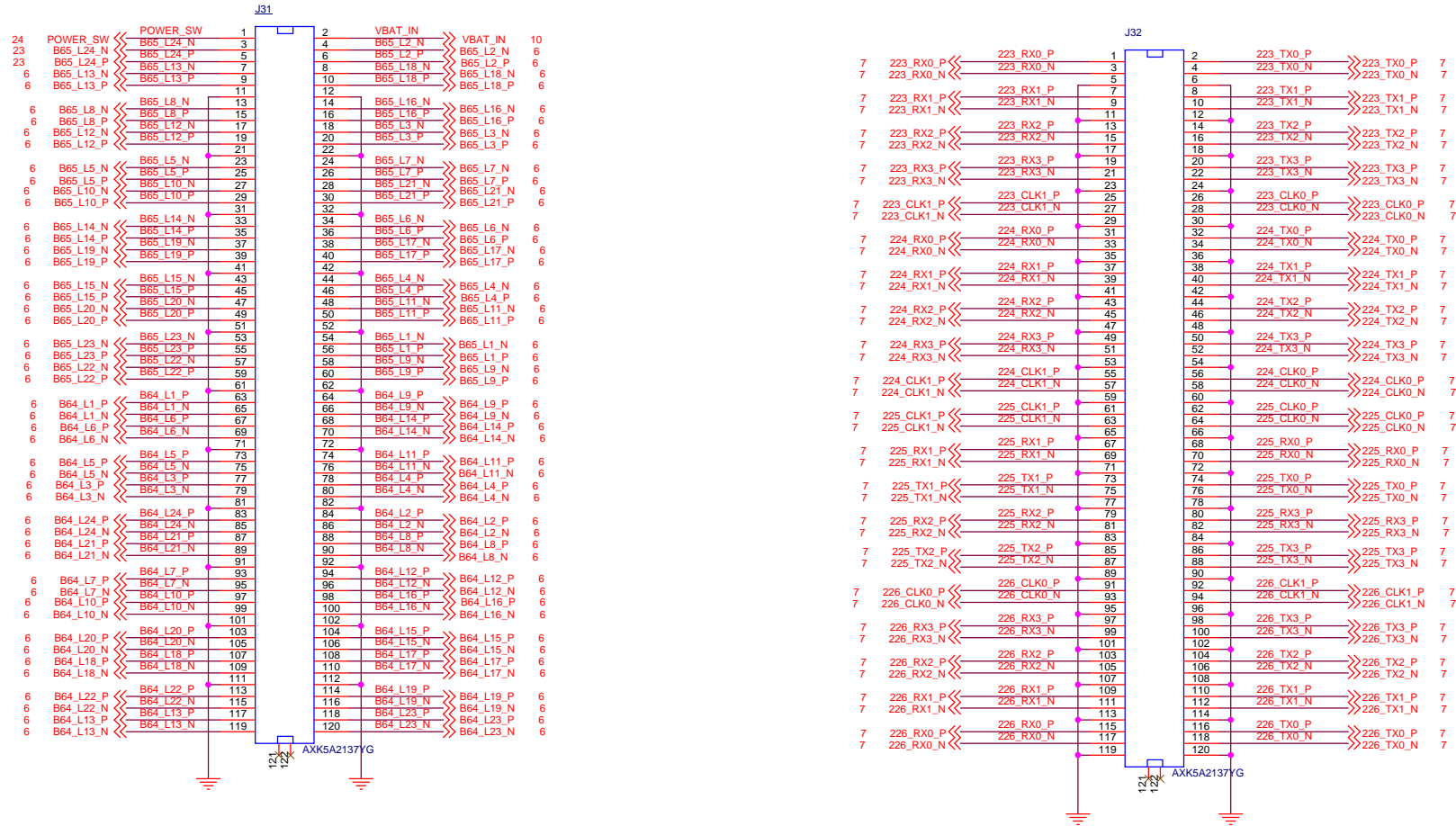


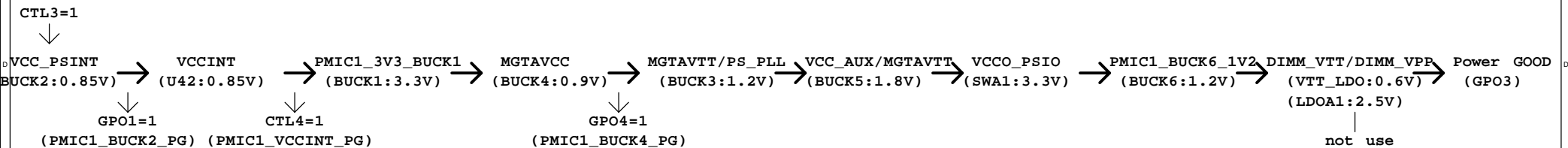
BANK28 IO Voltage is 1.8V Standard  
BANK87,88 IO Voltage is 3.3V Standard

MIO/SD/USB/ETH IO Voltage is 1.8V Standard



BANK65,66 IO Voltage is 1.8V Standard





GPO1-----BUCK2\_PG  
GPO4-----BUCK4\_PG  
GPO3-----Power\_PG

CTL1=1&CTL2=0-----BUCK6=1.2V  
CTL3-----PS\_FP\_PWR\_EN\_LS  
CTL4-----enable BUCK1  
CTL5=1&CTL2=0-----VTT\_LDO=0.6V  
CTL6=0-----BUCK2=0.85V

