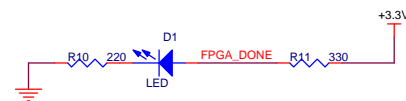
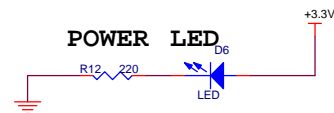
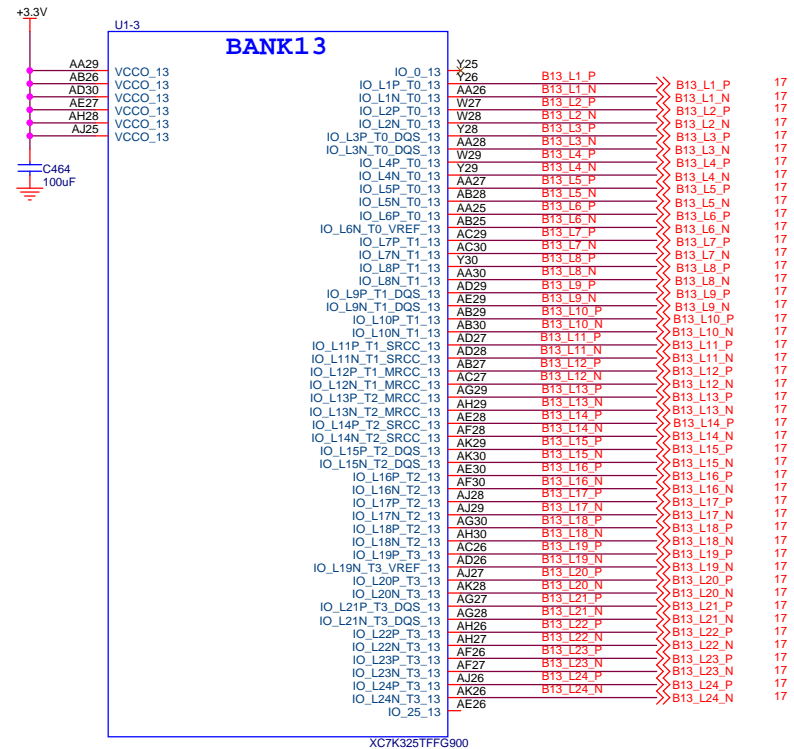
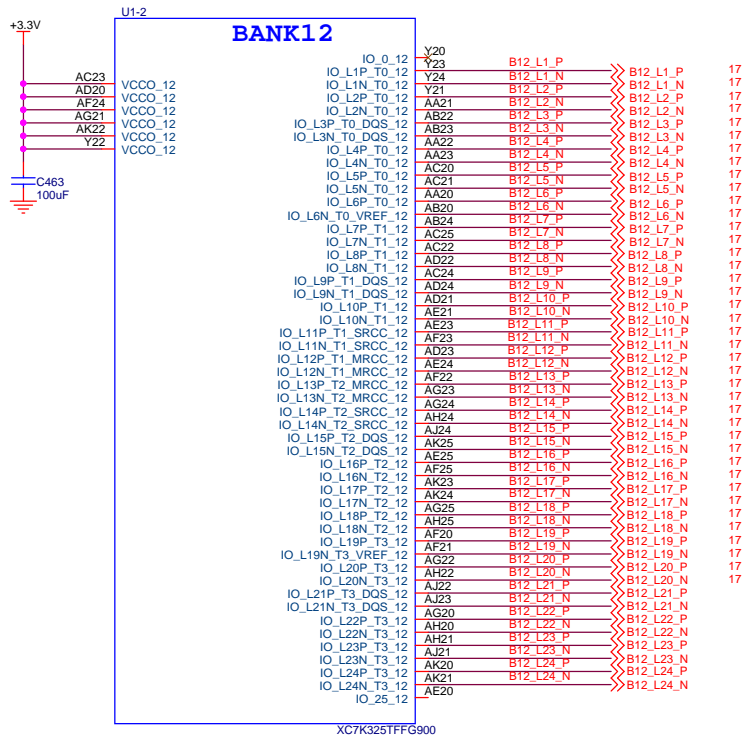


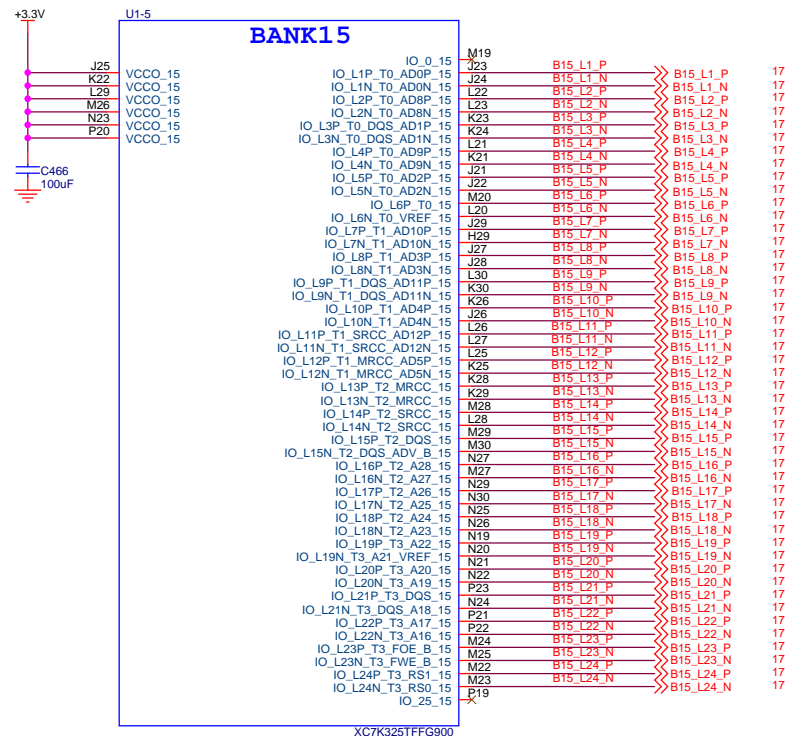
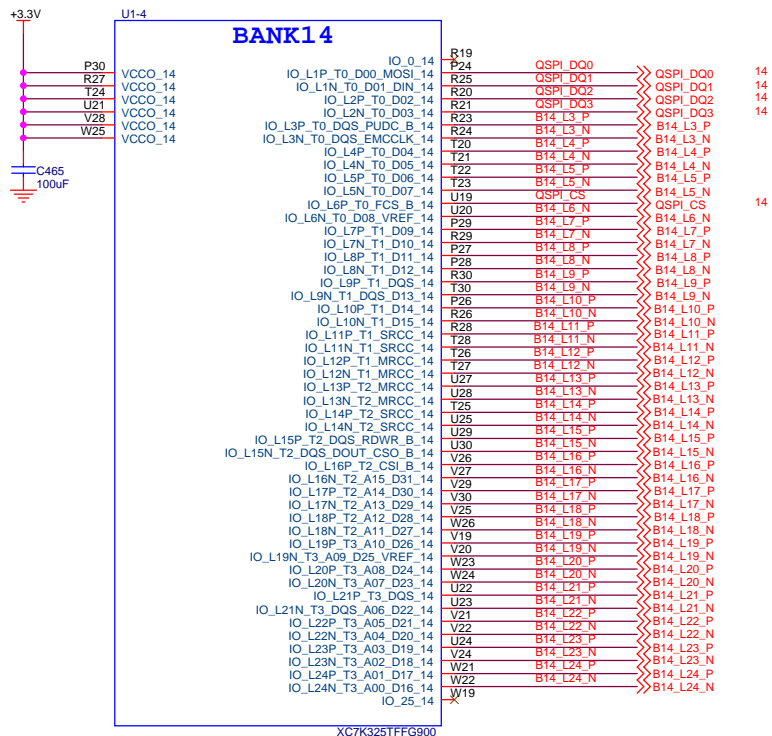
FPGA DONE LED



POWER LED

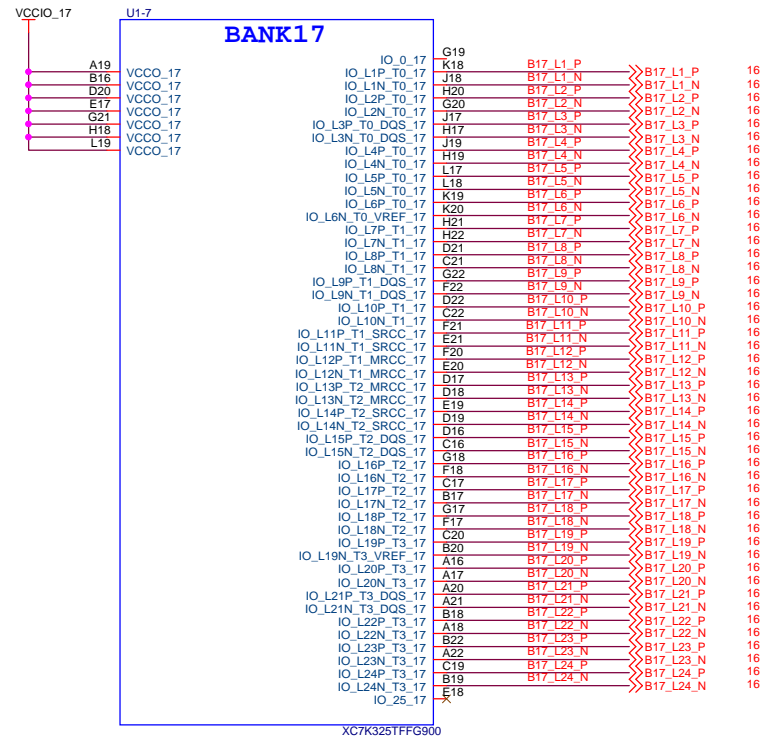
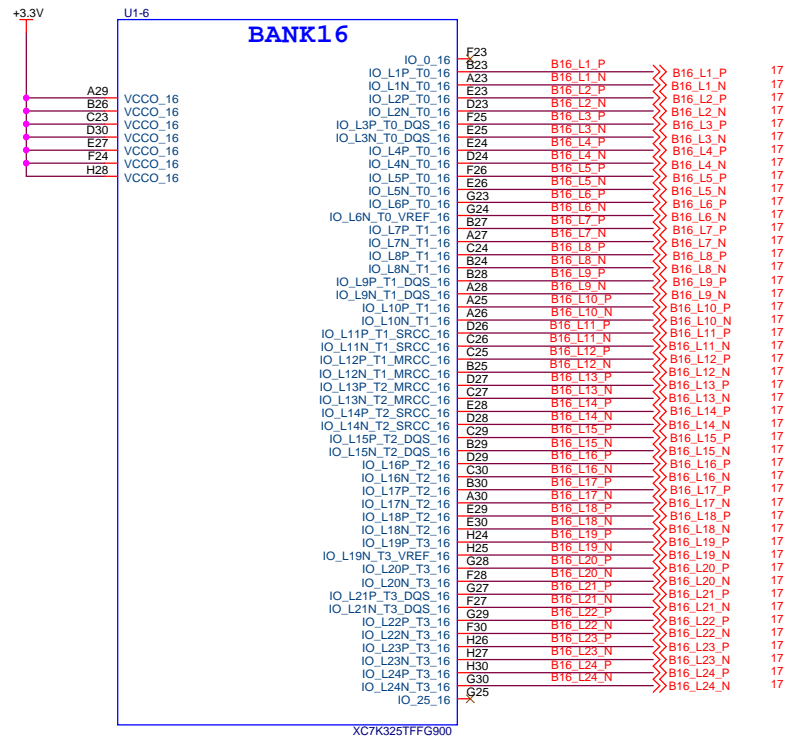


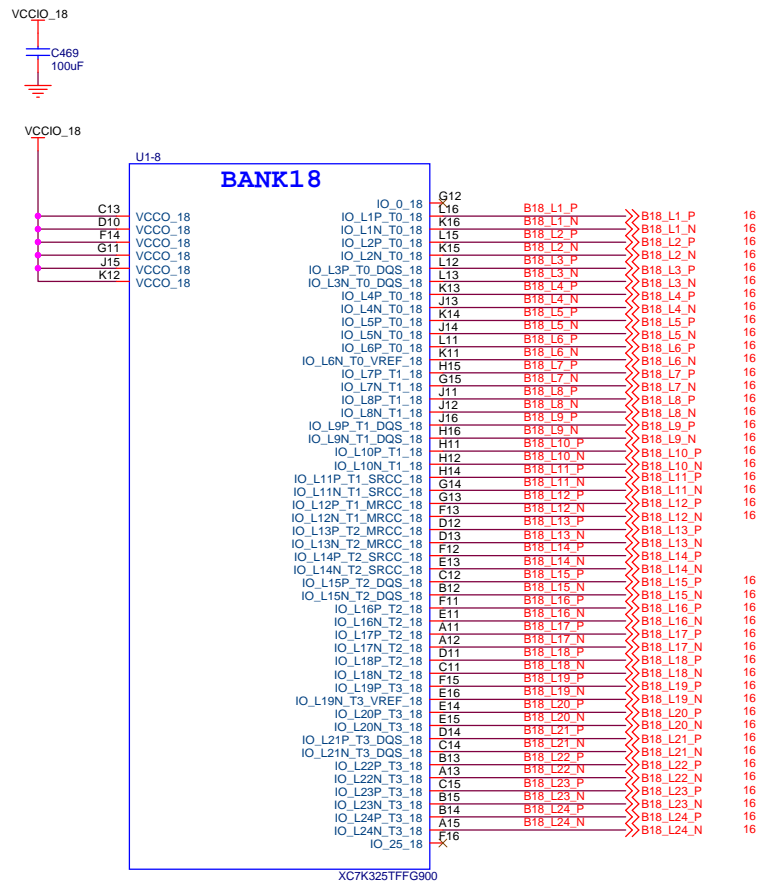


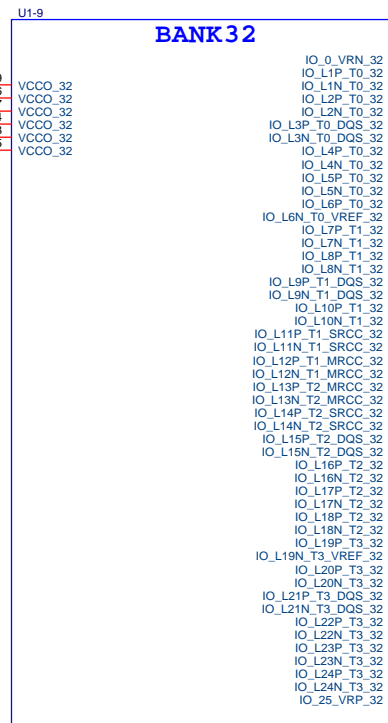
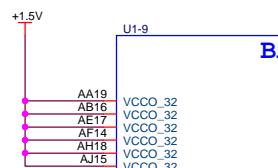
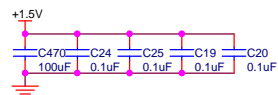


PUDC_B=1: Deactive internal Pull up Resister

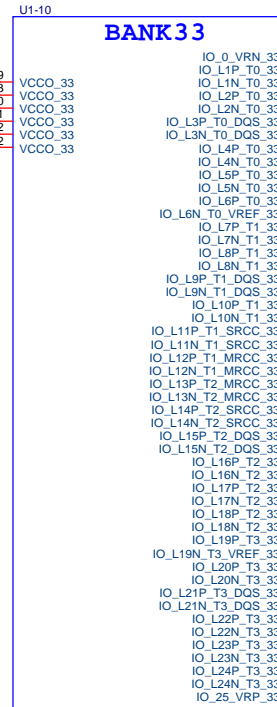
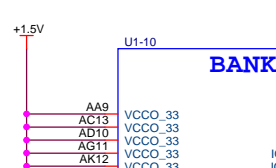
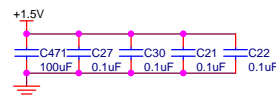




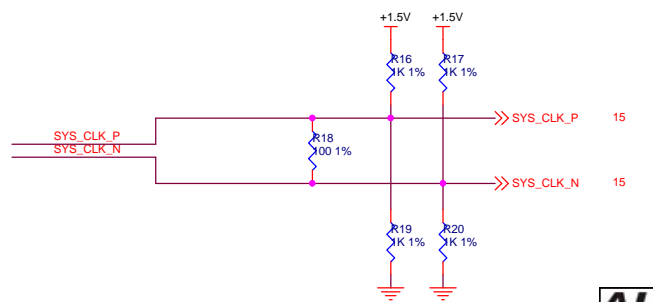


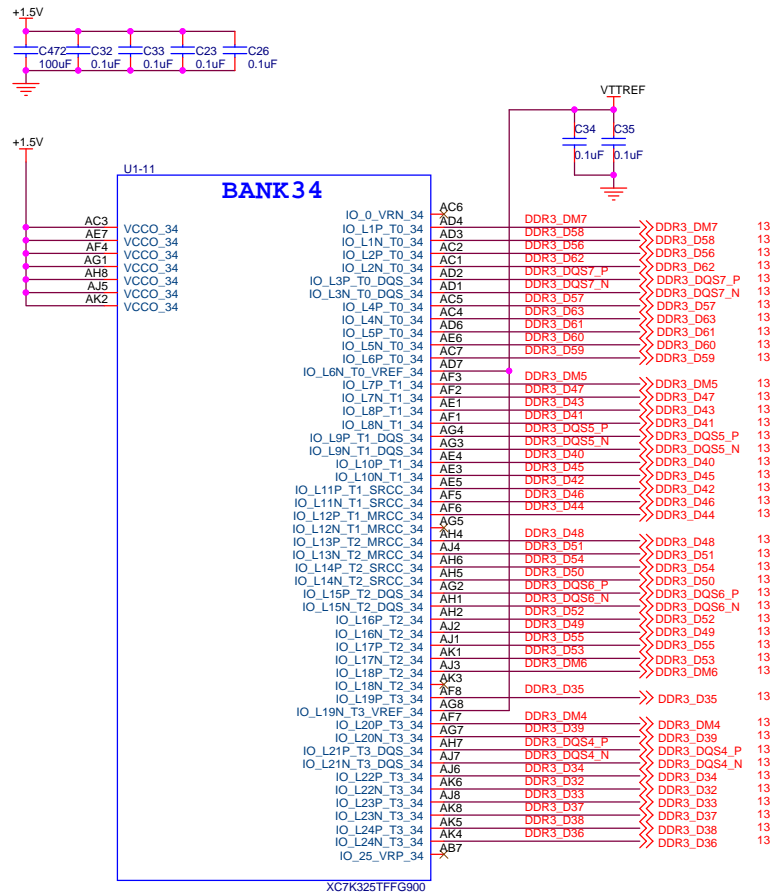


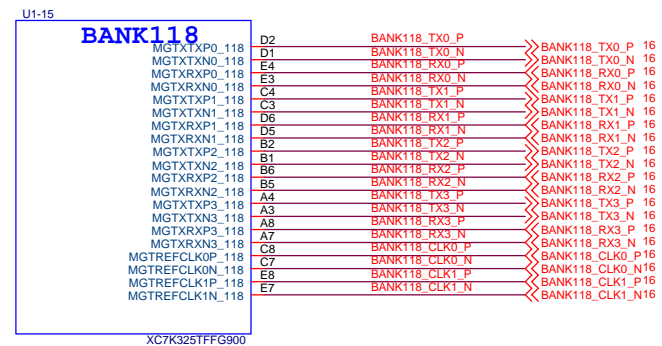
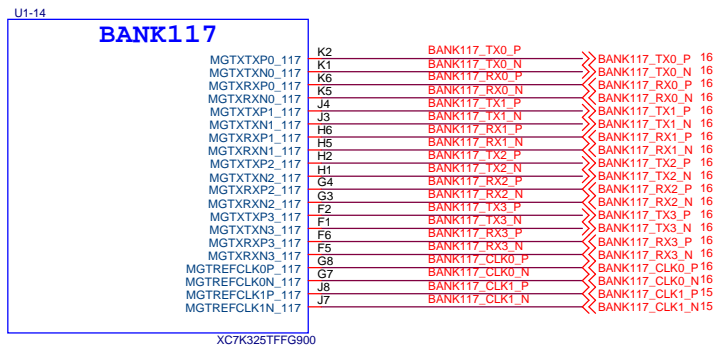
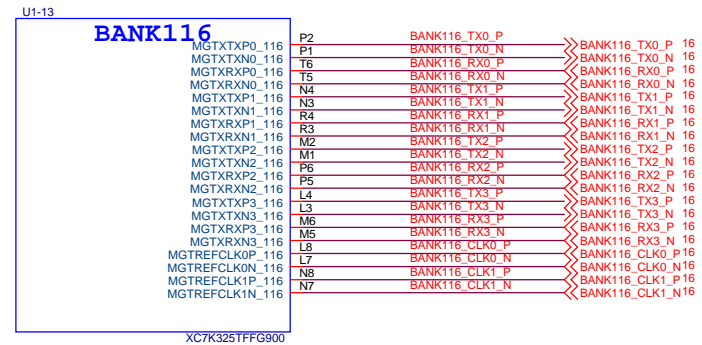
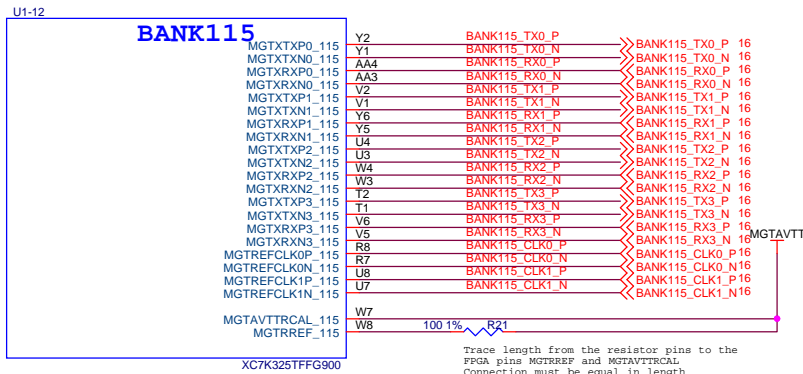
XC7K325TFFG900

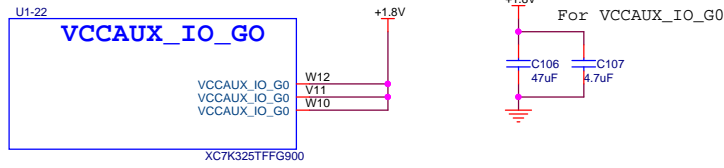
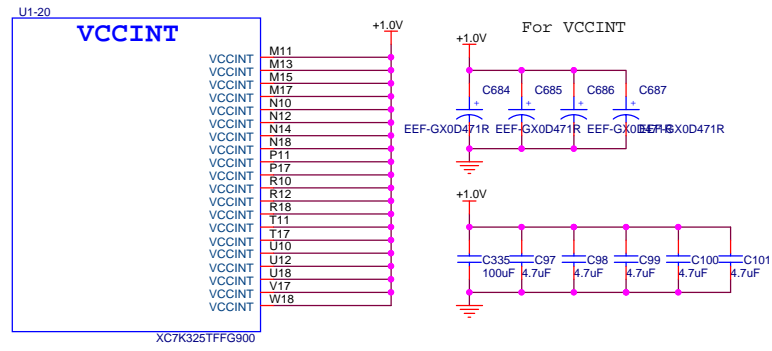
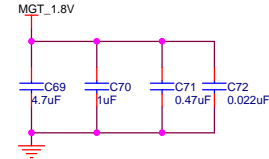
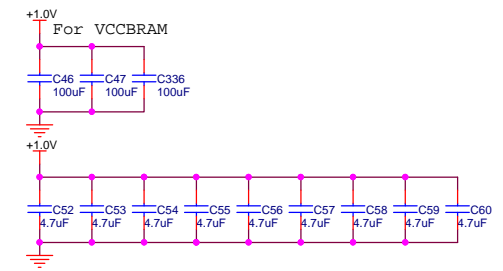


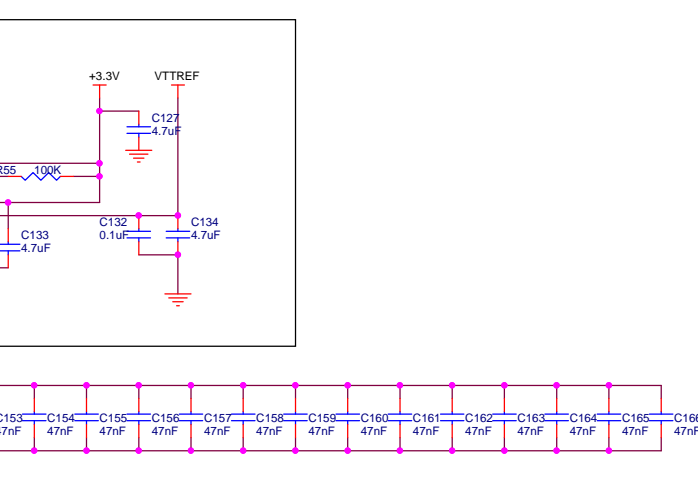
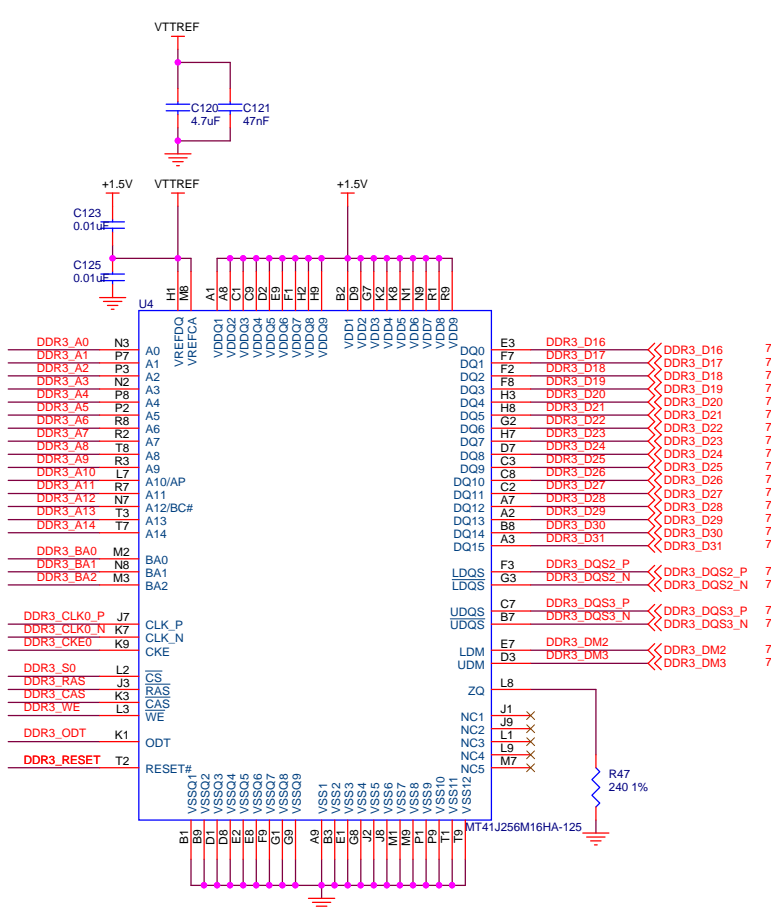
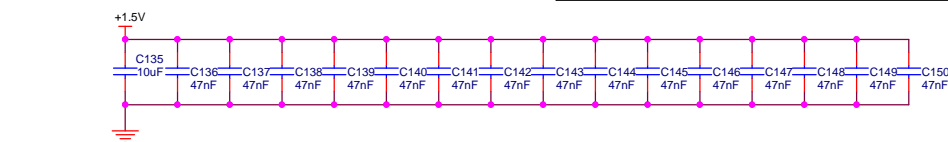
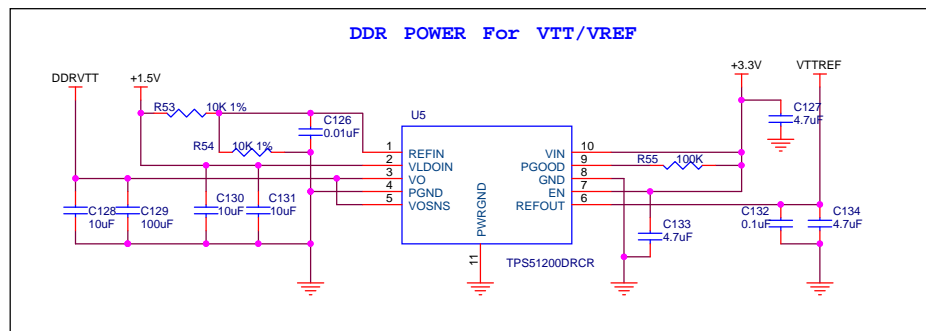
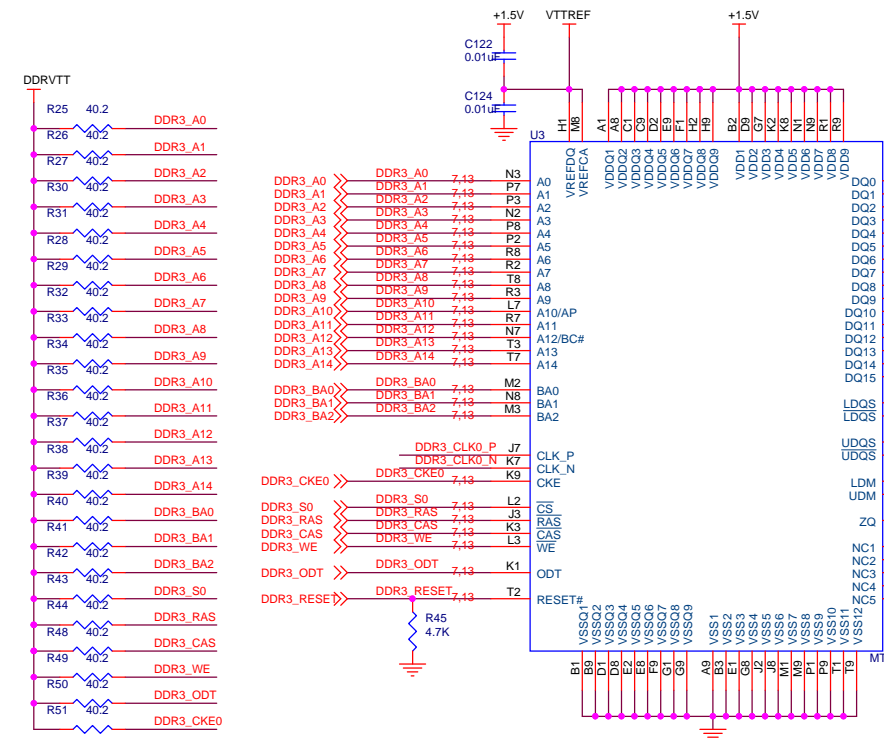
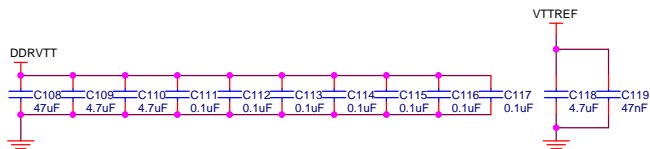
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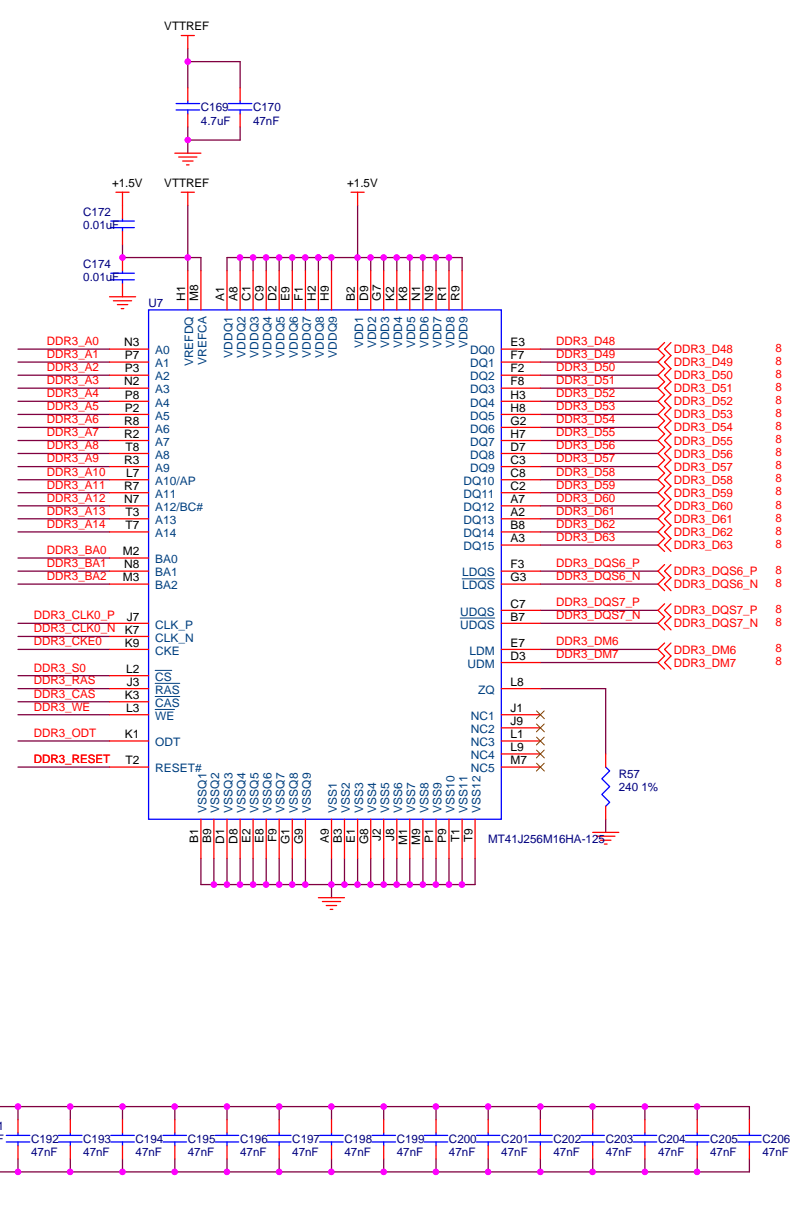
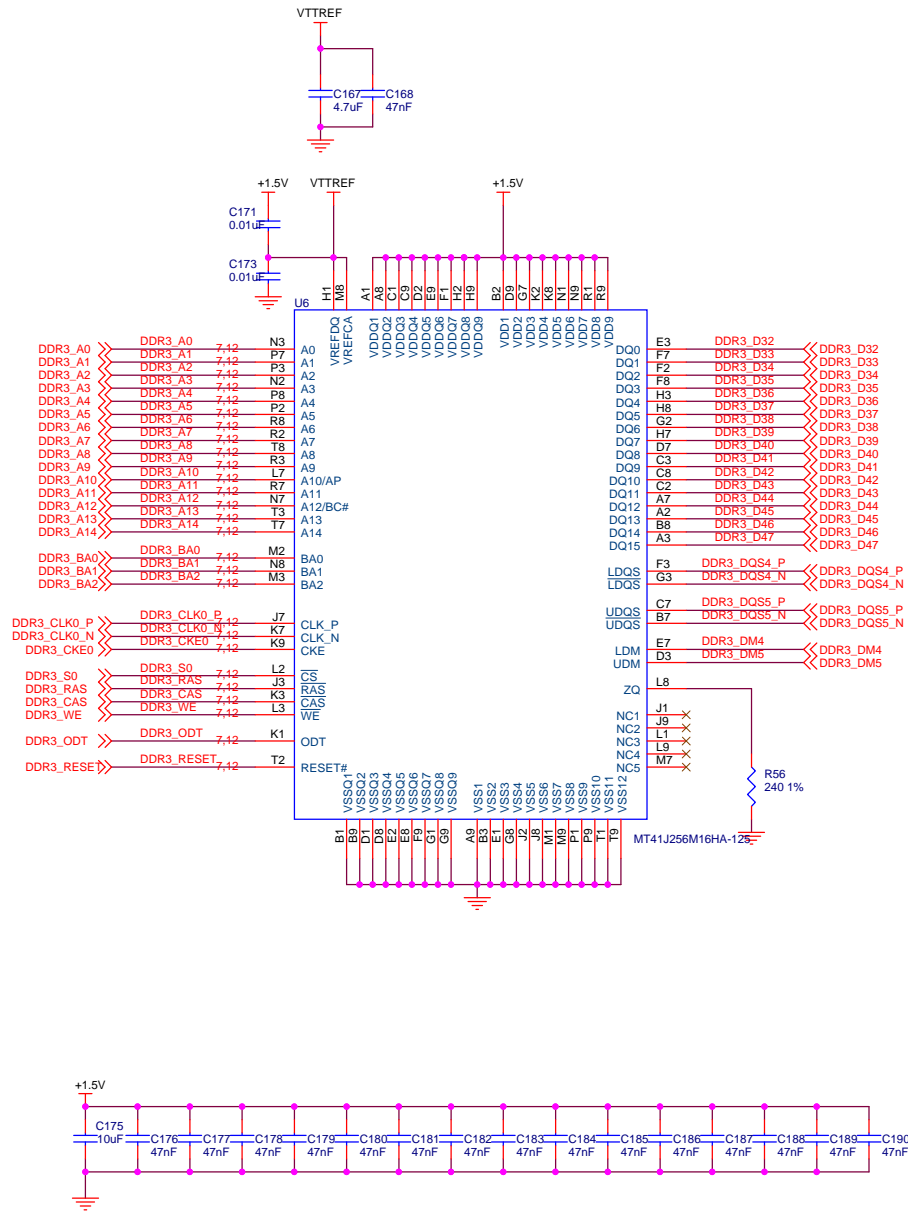




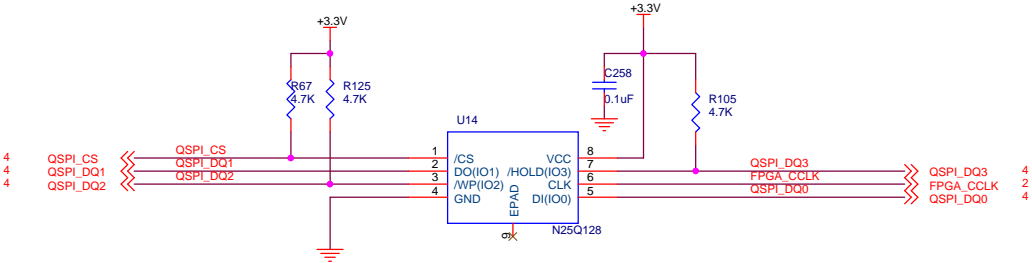






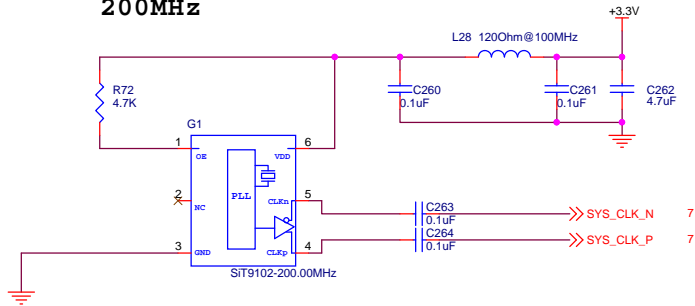


FPGA CONFIG SPI



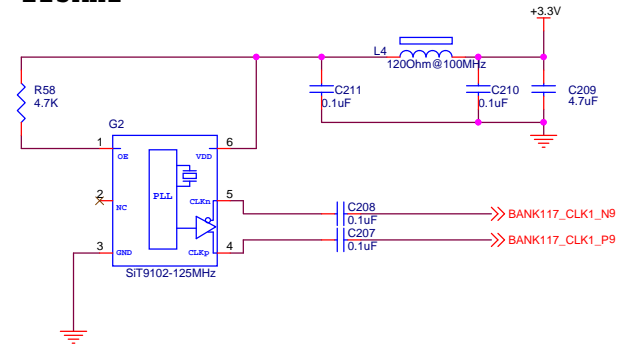
SYSTEM CLOCK

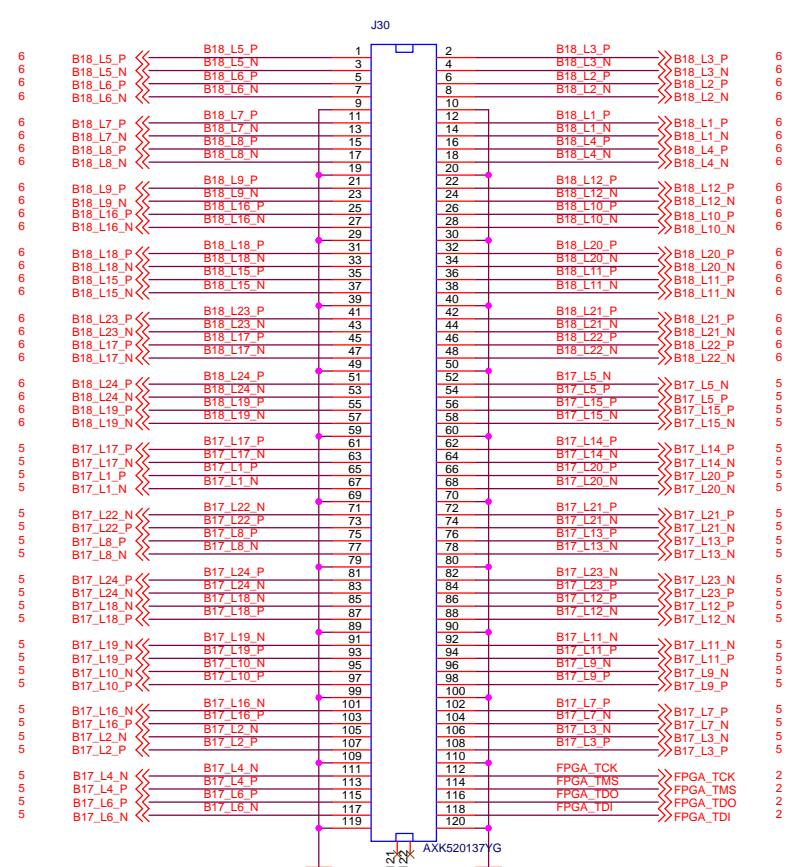
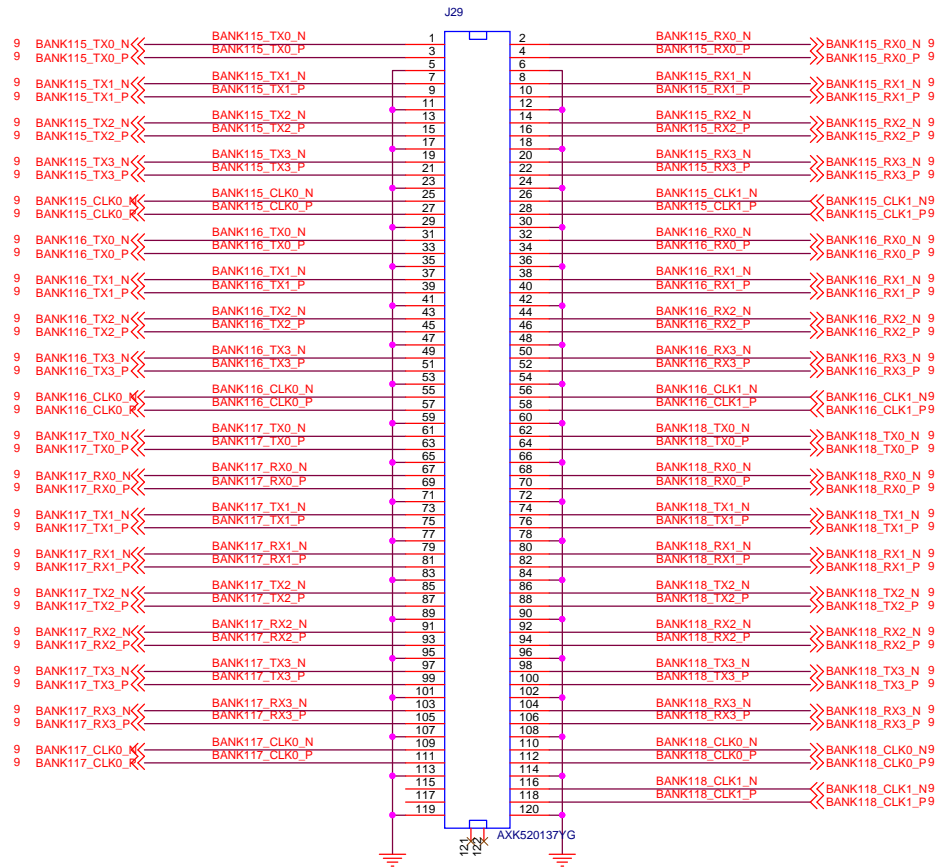
200MHz



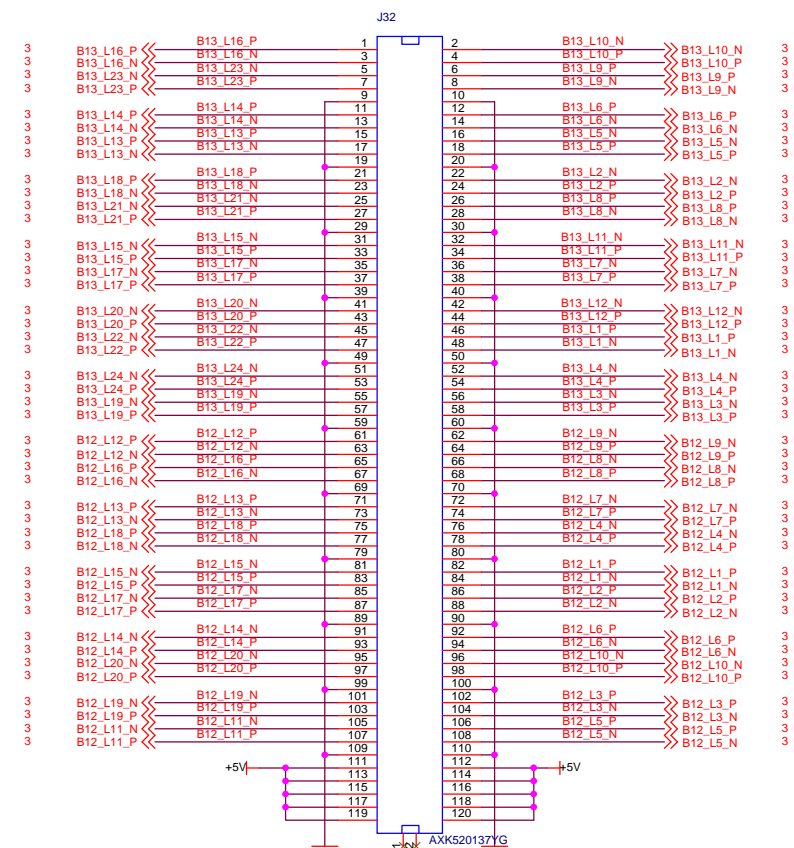
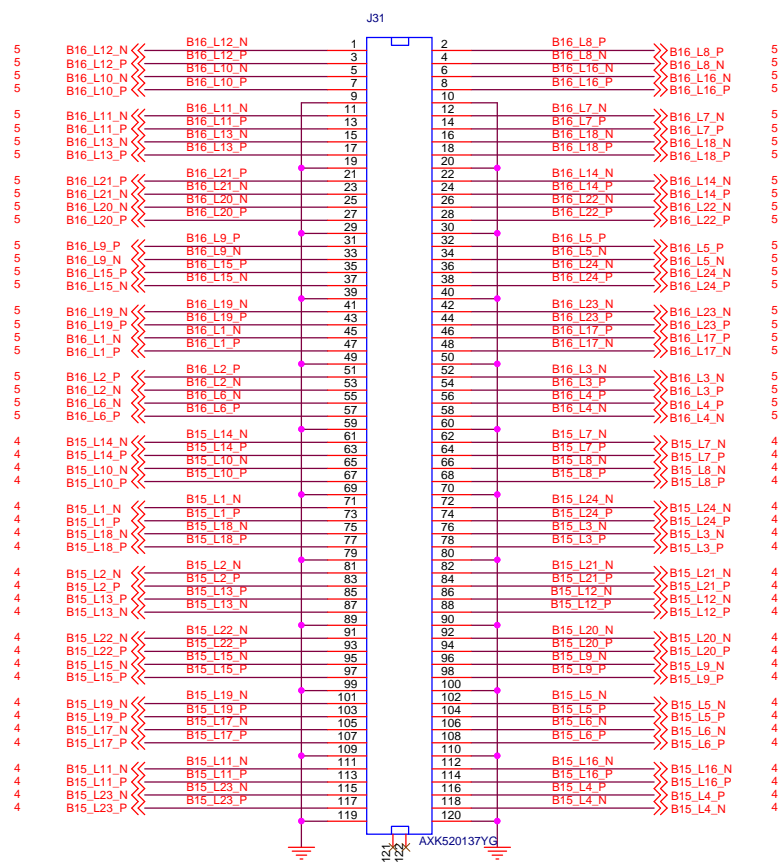
GTX CLOCK

125MHz





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