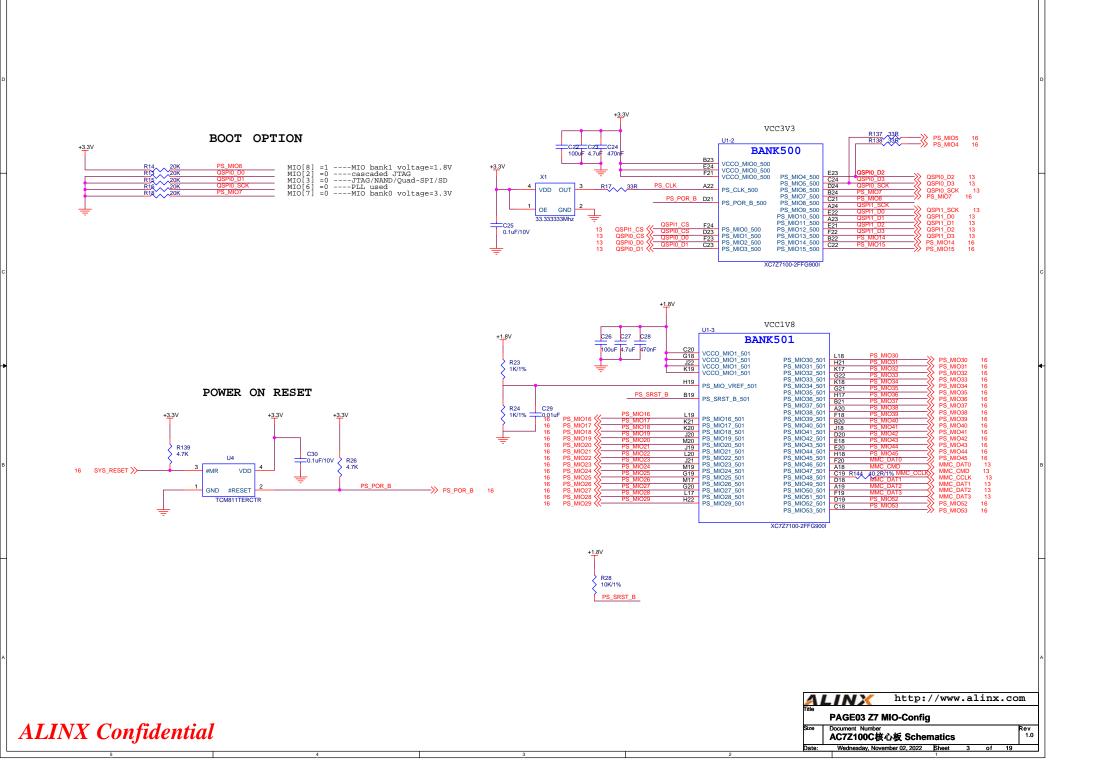
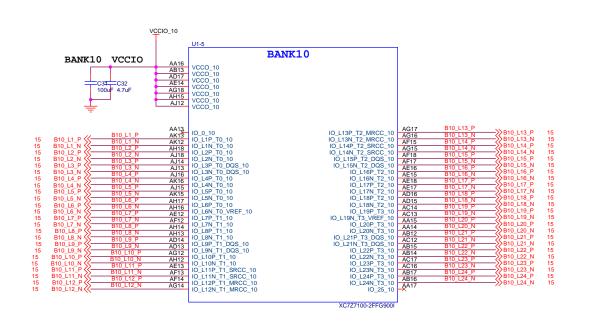
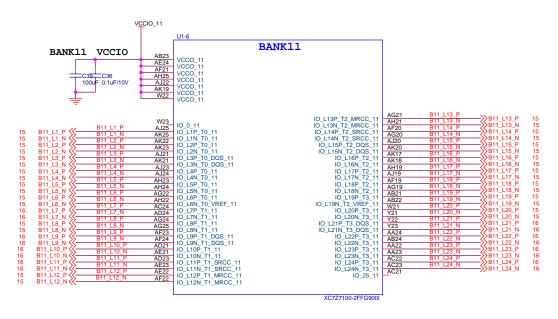


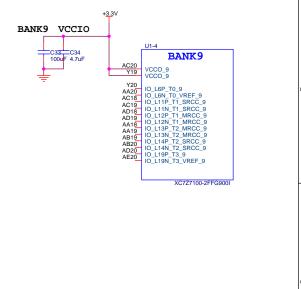


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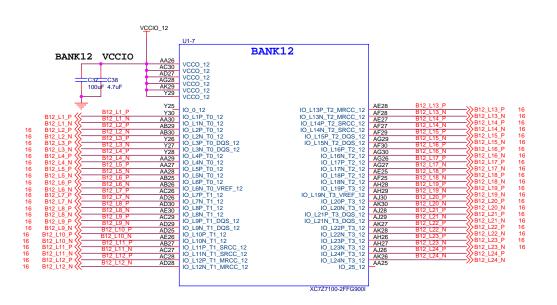


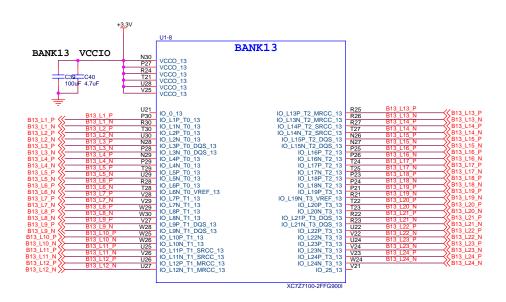
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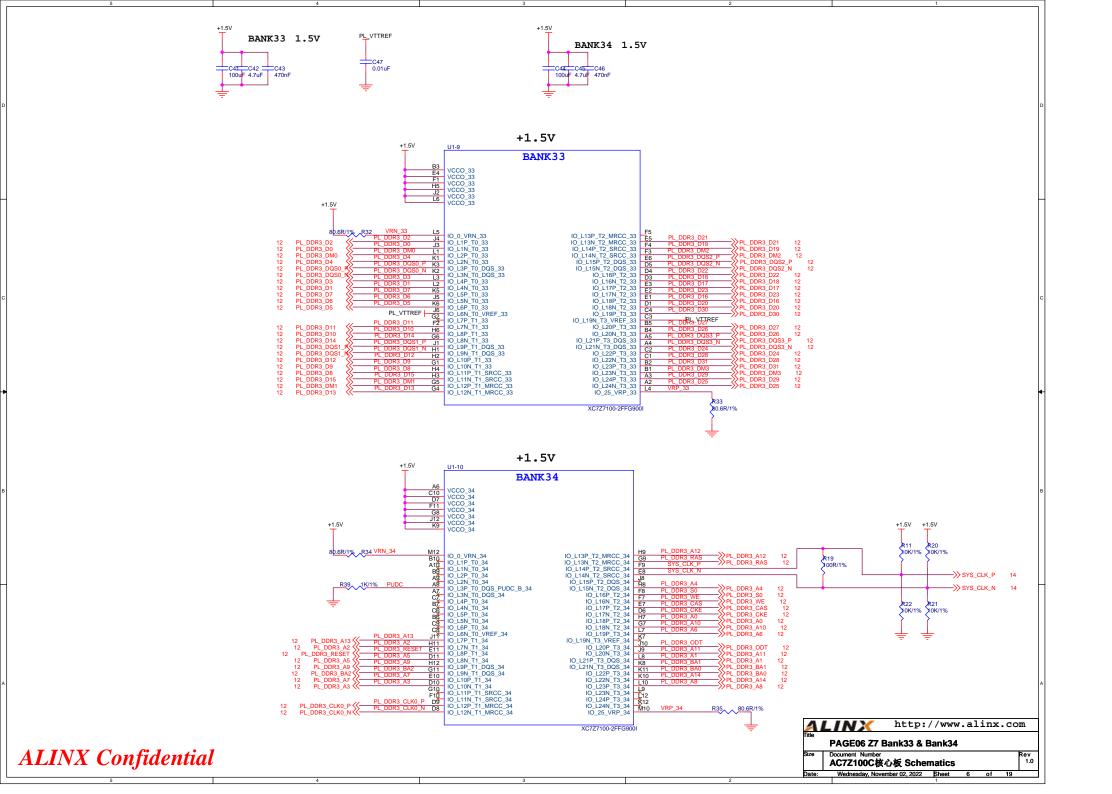
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AC7Z100C核心板 Schematics
Date: Wednesday, November 02, 2022 Sheet 4 of 19

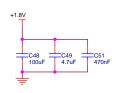
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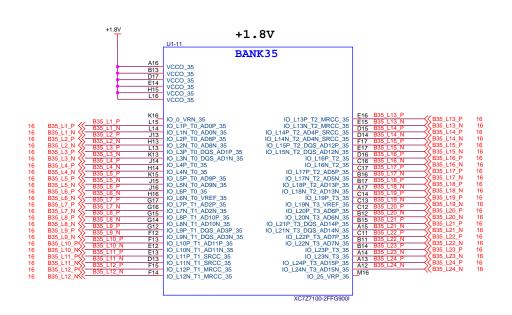




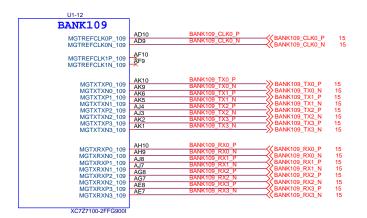


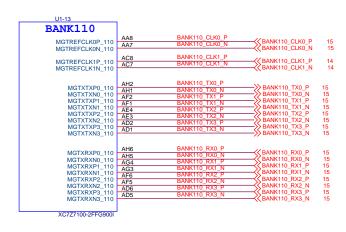


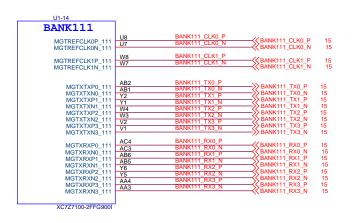


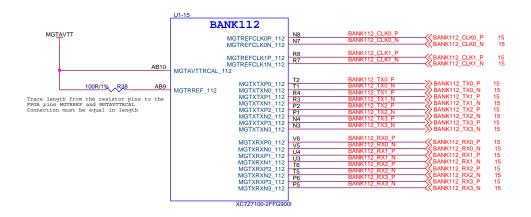


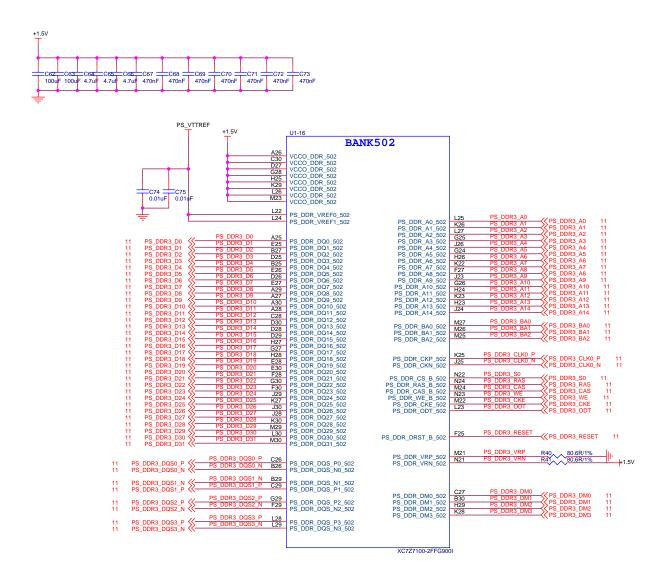


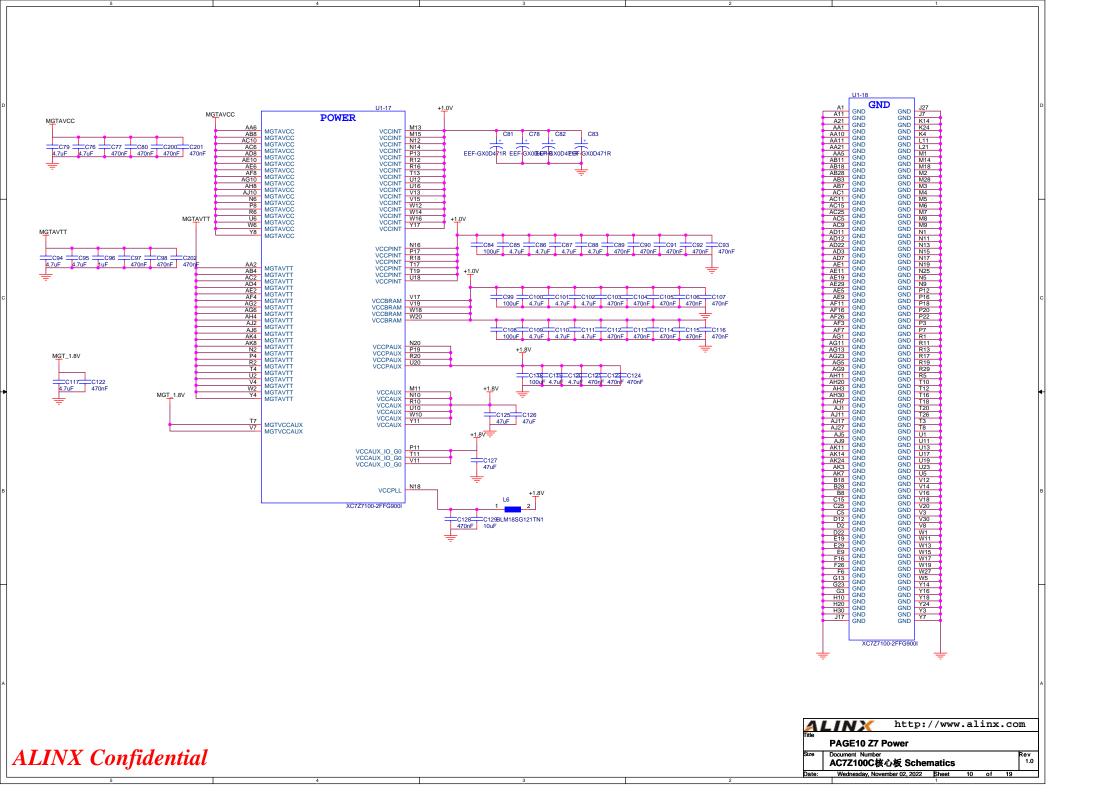


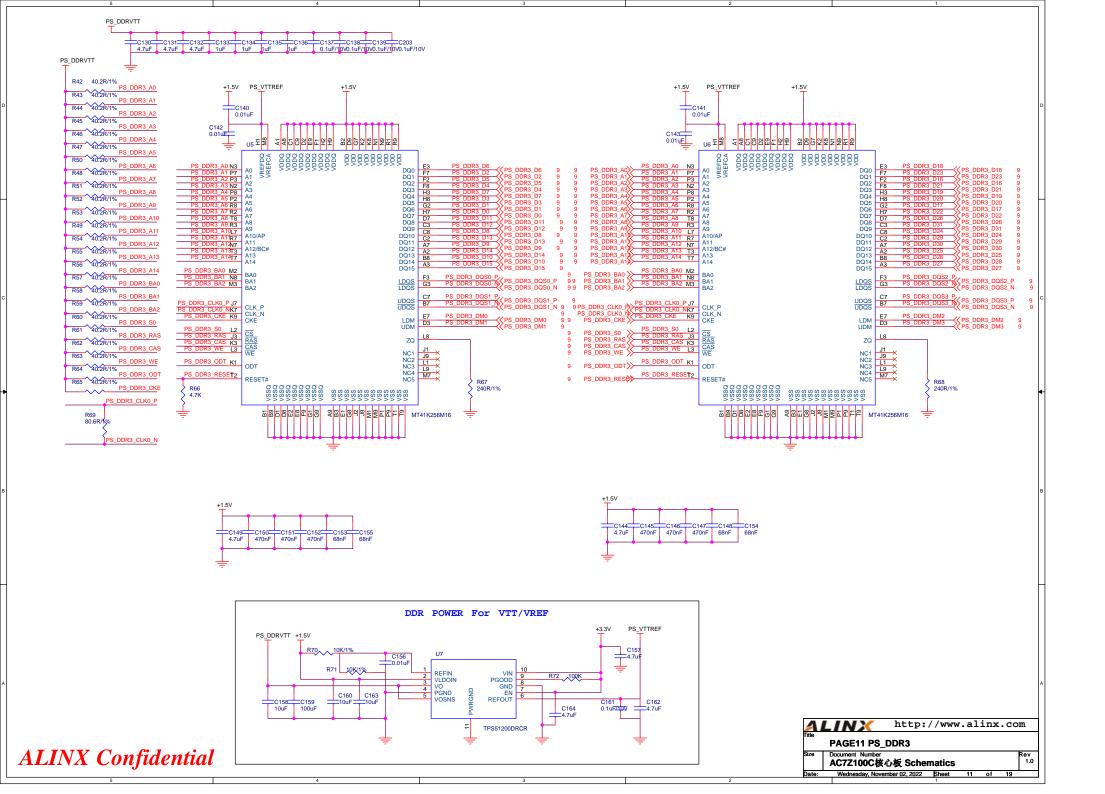


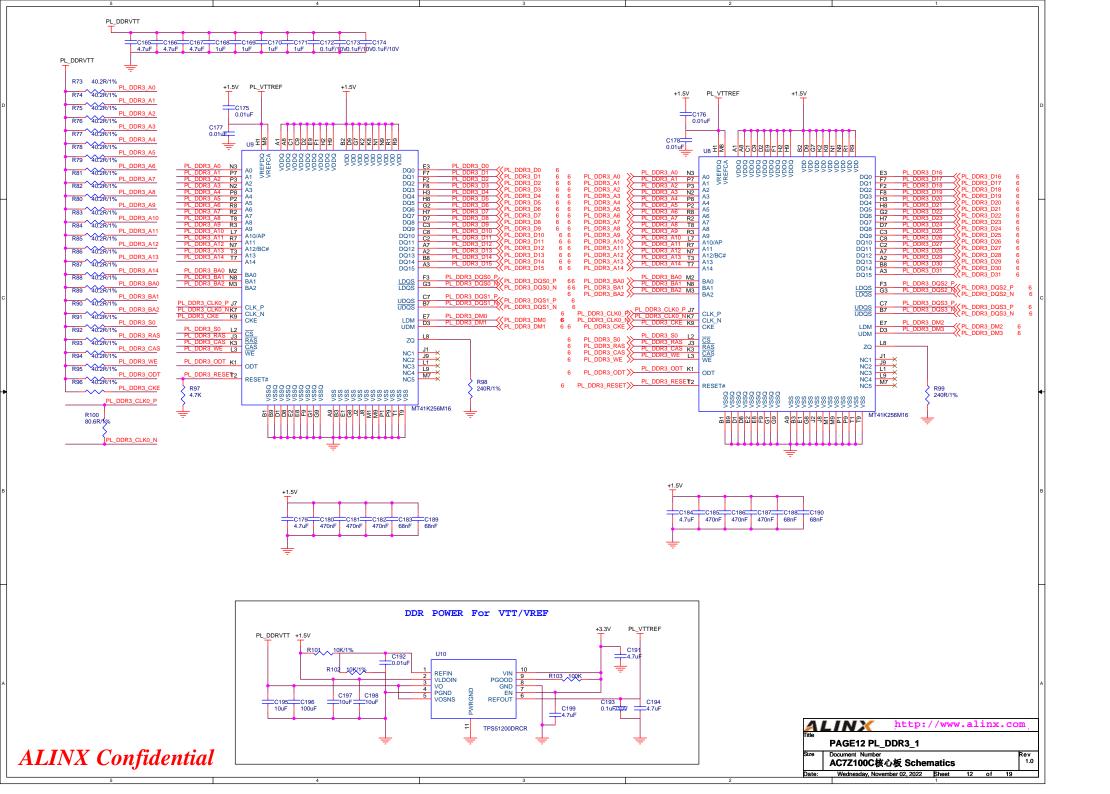


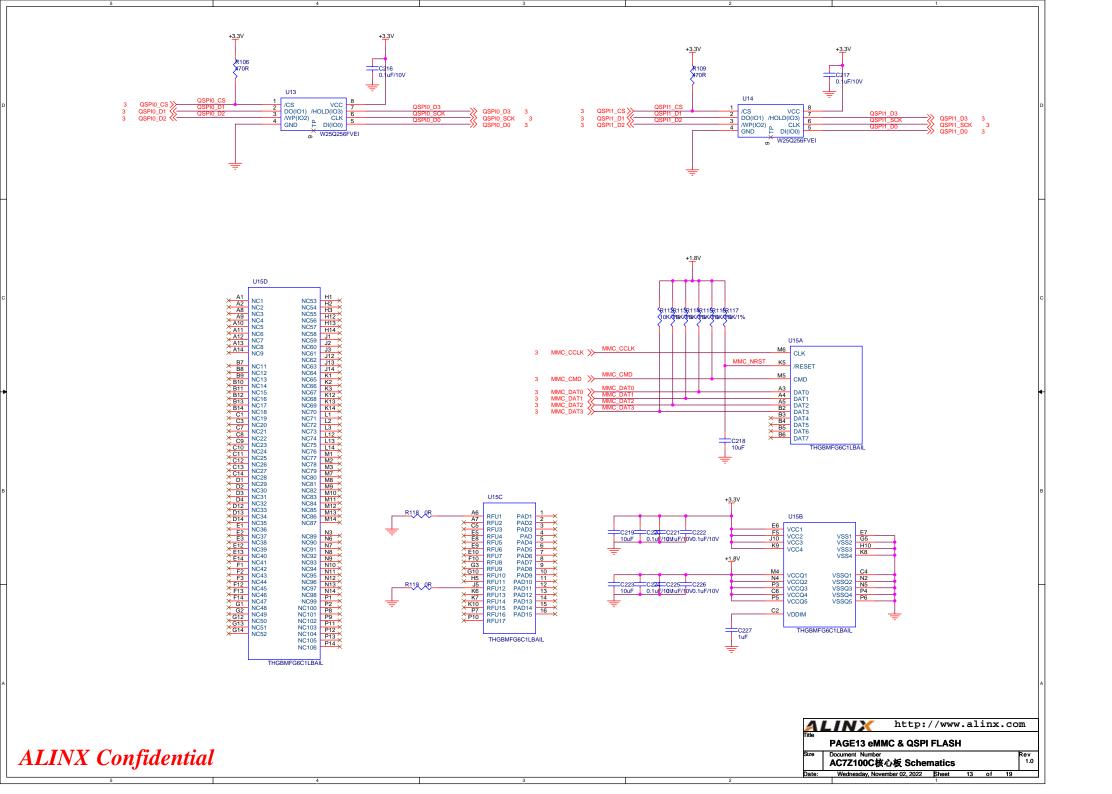




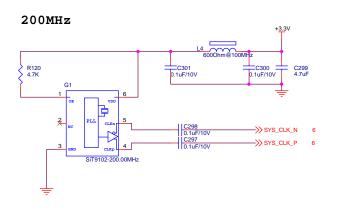


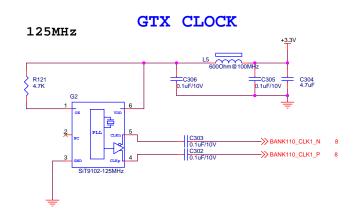




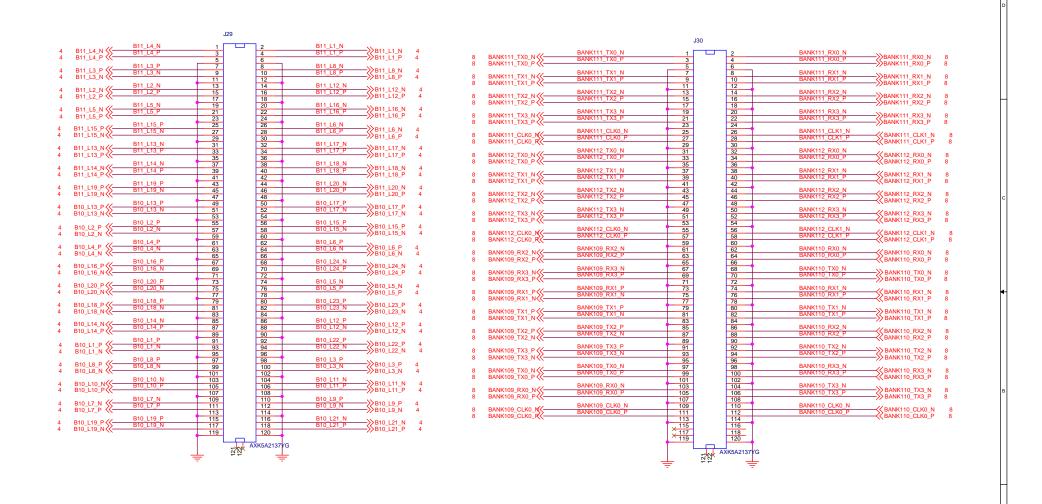


SYSTEM CLOCK











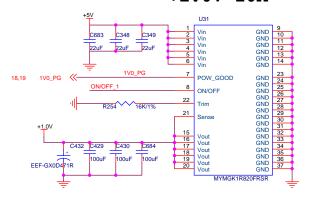
BANK35 IO is 1.8V

				J31				
		FPGA_TCK	1		2	FPGA_TDI		
2	FPGA_TCK <<	FPGA TMS	3	_	4	FPGA TDO		2
2	FPGA_TMS <<		5		6			2
	,,	B35 L2 P	7		8	1 B35 L8 N	**	
7	B35_L2_P	B35 L2 N	9		10	B35 L8 P	——————————————————————————————————————	7
7	B35_L2_N <<		11		12		——>>> B35_L8_P	7
_		B35_L9_P	13		14	B35_L3_N	W ===	_
7	B35_L9_P	B35_L9_N	15		16	B35_L3_P	—————————————————————————————————————	7
7	B35_L9_N ((17		18		——>>> B35_L3_P	7
_	DOE 100 M//	B35_L22_N	19		20	B35_L5_P	W pos 15 p	-
7	B35_L22_P	B35_L22_P	21		22	B35_L5_N	—————————————————————————————————————	7
7	B35_L22_P(\		23		24		——>>> B35_L5_N	7
_	//	B35_L20_N	25		26	B35_L10_P		7
7	B35_L20_N B35_L20_P	B35_L20_P	27		28	B35_L10_N	B35_L10_P	7
7	B35_L20_P		29		30			- /
7	DOE 140 N//	B35_L19_N	31		32	B35_L12_N		7
	B35_L19_N B35_L19_P	B35_L19_P	33		34	B35_L12_P	B35_L12_N	7
7	D35_L19_P\\		35		36			1
-	DOE 1.24 N//-	B35_L24_N	37		38	B35_L11_N	// B35 11 N	7
7	B35_L24_N\\ B35_L24_P\\	B35_L24_P	39		40	B35_L11_P	B35_L11_N B35_L11_P	7
- /	D30_L24_P\\		41		42		(/ poo_r i i _b	1
7	DOE 14 N //-	B35_L4_N	43		44	B35_L23_P	—->>> B35 L23 P	7
7	B35_L4_N ————————————————————————————————————	B35_L4_P	45		46	B35_L23_N	——≫B35_L23_N	7
- '	B35_L4_F ((47		48	1	// DOO_E20_14	,
7	D25 14 N //-	B35_L1_N	49		50	B35_L21_P		7
7	B35_L1_N	B35_L1_P	51		52	B35_L21_N	B35_L21_N	7
- '	B35_E1_F ((53		54	1	((000_E21_14	,
7	DOE 146 NK/-	B35_L16_N	55		56	B35_L14_P	/⟨B35 14 P	7
, ' ₇	B35_L16_N B35_L16_P	B35_L16_P	57		58	B35_L14_N	B35_L14_P B35_L14_N	7
- '	B35_E10_F ((59		60		((500_211_11	
7	D25 10 N//	B35_L18_N	61		62	B35_L13_N		7
7	B35_L18_N B35_L18_P	B35_L18_P	63		64	B35_L13_P	—————————————————————————————————————	7
- '	B35_E16_F ((65		66		((200_2.0_1	
7	B35 L15 NK/	B35_L15_N	67		68	B35_L17_N		7
7	B35_L15_N B35_L15_P	B35_L15_P	69		70	B35_L17_P	B35_L17_P	7
	D00_E10_1 ((71		72		((500_211_1	
7	B35 17 N //	B35_L7_N	73		74	B12_L17_N	——>>> B12_L17_N	5
- 7	B35_L7_N ————————————————————————————————————	B35_L7_P	75		76	B12_L17_P	——≫B12_L17_P	5
	D00_E/_1 ((77		78		// 512_511_1	•
7	B35 16 N ((B35_L6_N	79		80	B12_L18_N	—>>>B12 L18 N	5
7	B35_L6_N B35_L6_P	B35_L6_P	81		82	B12_L18_P	——≫B12 L18 P	5
	200_20_1 ((D40 10 N	83		84	D40 140 N	//	
5	B12 L6 N ((-	B12_L6_N	85		86	B12_L10_N	—————————————————————————————————————	5
5	B12_L6_N ST2_L6_P	B12_L6_P	87		88	B12_L10_P	—————————————————————————————————————	5
9		B12_L11_N	89		90	B12 L13 N	.,	-
5	B12_L11_N <<	B12_L11_N B12_L11_P	91		92	B12_L13_N B12_L13_P	——>>> B12_L13_N	5
5	B12_L11_N	BIZ_LII_P	93		94	BIZ_LI3_P	——≫B12 L13 P	5
		B12 L12 N	95		96	B12 L16 N		
5	B12_L12_N	B12_L12_N	97		98 100	B12_L16_N B12_L16_P	——>>>B12_L16_N	5
5	B12 L12 P <<	DIZ_LIZ_P	99 101			BIZ_LIO_P	——≫B12 L16 P	5
		P12 L0 N			102	B12 L22 N		
5	B12 L9 N <<<──	B12_L9_N B12_L9_P	103 105		104 106	B12_L22_N B12_L22_P	——>>> B12_L22_N	5
5	B12_L9_N ————————————————————————————————————	D12_L3_F	105		106	DIZ_LZZ_F		5
		B12 L14 N	107		1108	B12 L20 N		
5	B12_L14_N <<	B12_L14_N	109		110	B12_L20_N B12_L20_P	—— B12_L20_N	5
5	B12_L14_N	D12_E14_F	113		114	512_L20_F		5
		PS POR B	113		114	B12 L23 N		
3	PS_POR_B <<	SYS_RESET	115		116	B12_L23_N B12_L23_P	——	5
3	SYS RESET <	313_NE3E1			120	B12_L23_P	——≫B12_L23_P	5
	*		119		120	•		
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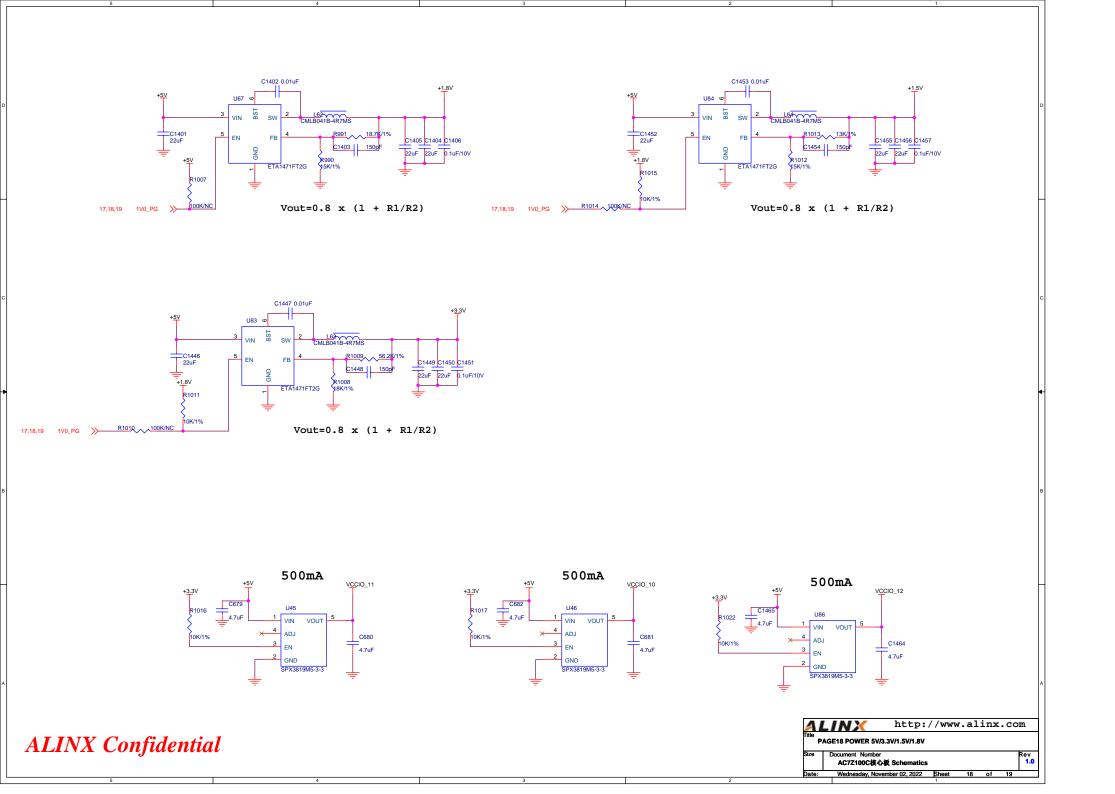
			J	132				
		PS MIO5				DC MIO47		
3	PS_MIO5 <<	PS_MIO5 PS_MIO4	1		2	PS_MIO17 PS_MIO18		3
3	PS_MIO4 <<	F3_IMIO4	3 5		6	F3_WIO16		3
		PS_MIO14	7		8	PS_MIO19		
3	PS_MIO14 <<	PS_MIO15	9		10	PS_MIO20		3
3	PS_MIO15 <<-		11		12		PS_MIO20	3
_	//	PS_MIO52	13		14	PS_MIO16	N ==	_
3	PS_MIO52 PS_MIO53	PS_MIO53	15		16	PS_MIO21	PS_MIO16 PS_MIO21	3
3	PS_MIO53 ((17		18		// PS_MIO21	3
3	PS_MIO7 <<	PS_MIO7	19		20	PS_MIO26		3
3	F3_IVIIO7 ((21		22	PS_MIO25	PS_MIO26 PS_MIO25	3
		PS MIO40	23		24	PS MIO24	//	_
3	PS_MIO40 <<	PS_MIO41	25 27		26 28	PS_MIO23	──>>> PS_MIO24	3
3	PS_MIO41 <<	10_101041	29		30	1 0_WIO25	——>>> PS_MIO23	3
		PS_MIO42	31		32	PS_MIO27		
3	PS_MIO42	PS_MIO43	33		34	PS_MIO22	PS_MIO27 PS_MIO22	3
3	PS_MIO43 <<		35		36			3
3	DC MICH //-	PS_MIO44	37		38	PS_MIO30		-
3	PS_MIO44 PS_MIO45	PS_MIO45	39		40	PS_MIO29	PS_MIO30 PS_MIO29	3
3	F3_IVIIO45 ((41		42		// F3_IVIIO29	3
5	B12 I 2 N <<-	B12_L2_N B12_L2_P	43		44	PS_MIO36 PS_MIO31	———>>> PS_MIO36	3
5	B12_L2_N	BIZ_LZ_P	45 47		46 48	PS_MIO31	PS_MIO31	3
		B12 L4 N	49		50	PS_MIO32		
5	B12_L4_N	B12 L4 P	51		52	PS MIO33	PS_MIO32 PS_MIO33	3
5	B12_L4_P <<		53		54		>> PS_MIO33	3
_	D40 140 D //	B12_L19_P	55		56	PS_MIO34	N	
5 5	B12_L19_P	B12_L19_N	57		58	PS_MIO35	PS_MIO34 PS_MIO35	3
5	P15_F19_14 ((59		60			3
5	B12 3 P ((-	B12_L3_P	61		62	PS_MIO28	—————————————————————————————————————	3
5	B12_L3_P	B12_L3_N	63		64	PS_MIO37	——————————————————————————————————————	3
Ŭ		B12_L5_P	65		66 68	PS_MIO38	// 1 G_IIIIGGI	Ŭ
5	B12_L5_P	B12_L5_P B12_L5_N	67 69		70	PS_MIO39		3
5	B12_L5_N <<	8.2_20_14	71		72	1.0000	PS_MIO38 PS_MIO39	3
		B12 L8 N	73		74	B12_L21_P	***	
5	B12_L8_N	B12_L8_P	75		76	B12_L21_N	—— → → → → → → → → → → → →	5 5
5	B12_L8_P ((77		78		——)) B12_L21_N	5
5	B12 L15 N //-	B12_L15_N	79		80	B12_L7_N	—>>> B12 L7 N	5
5	B12_L15_N	B12_L15_P	81		82	B12_L7_P	—————————————————————————————————————	5
		B11_L23_N	83 85		84 86	B11_L11_P		•
4	B11_L23_N \\— B11_L23_P \\—	B11_L23_P	87		88	B11_L11_N	—≫ B11_L11_P →≫ B11_L11_N	4
4	B11_L23_P << —	511_220_1	89		90	011_211_11	——≫ B11_L11_N	4
		B11_L21_N	91		92	B11_L9_P	**	
4	B11_L21_N	B11_L21_P	93		94	B11_L9_N	——≫B11_L9_P	4
4	B11_L21_P ((95		96		——≫B11_L9_N	4
4	B11 122 N//-	B11_L22_N	97		98	B11_L10_N	—————————————————————————————————————	4
4	B11_L22_N	B11_L22_P	99		100	B11_L10_P		4
		B11_L7_P	101 103		102 104	B11_L24_P		
4	B11_L7_P	B11_L7_N	105		104	B11_L24_N	—— → >>> B11_L24_P	4
4	B11_L7_N <<		107		108		—————————————————————————————————————	4
		+5V	109		110	+5V		
		<u> </u>	111		112	<u>_</u>		
			113		114	<u>i</u>		
			115		116			
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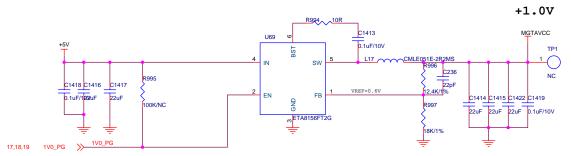
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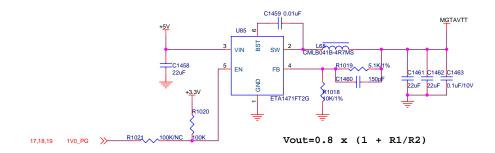
+5V R256 100K/NC ON/OFF_1 R255 100K/NC

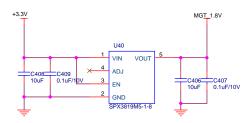


1.0V POWER 6A



 $Vout=0.6 \times (1 + R1/R2)$





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