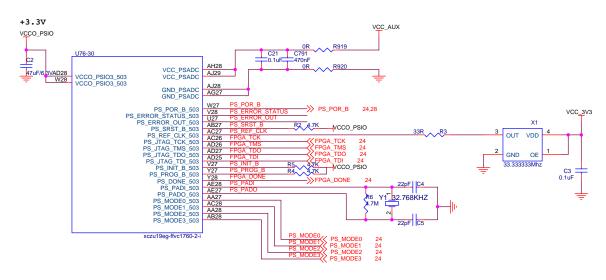
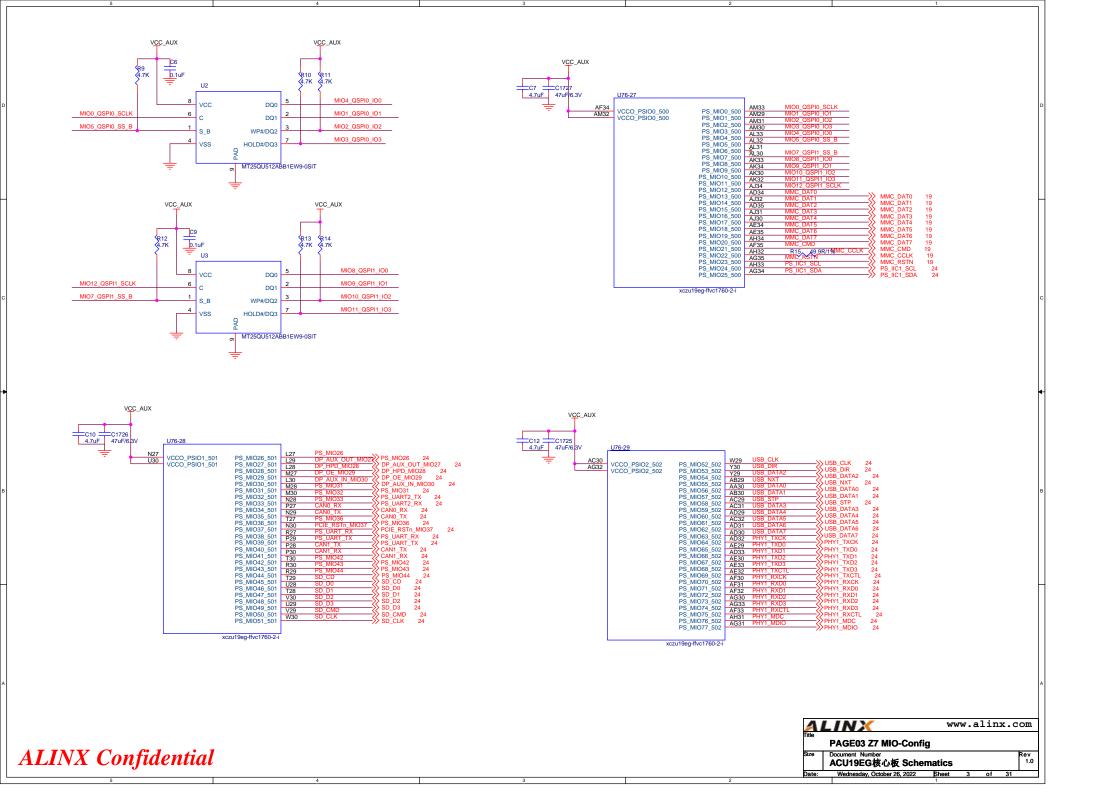


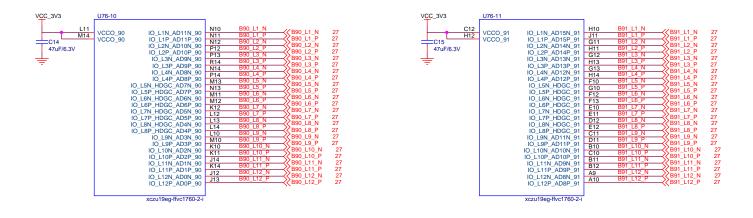
## MODE[3:0] BOOT MODE Descritpion

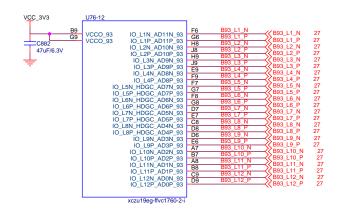
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1110	SD1 LS(3.0)	SD 3.0

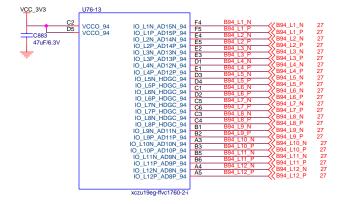


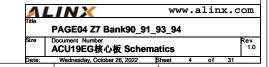


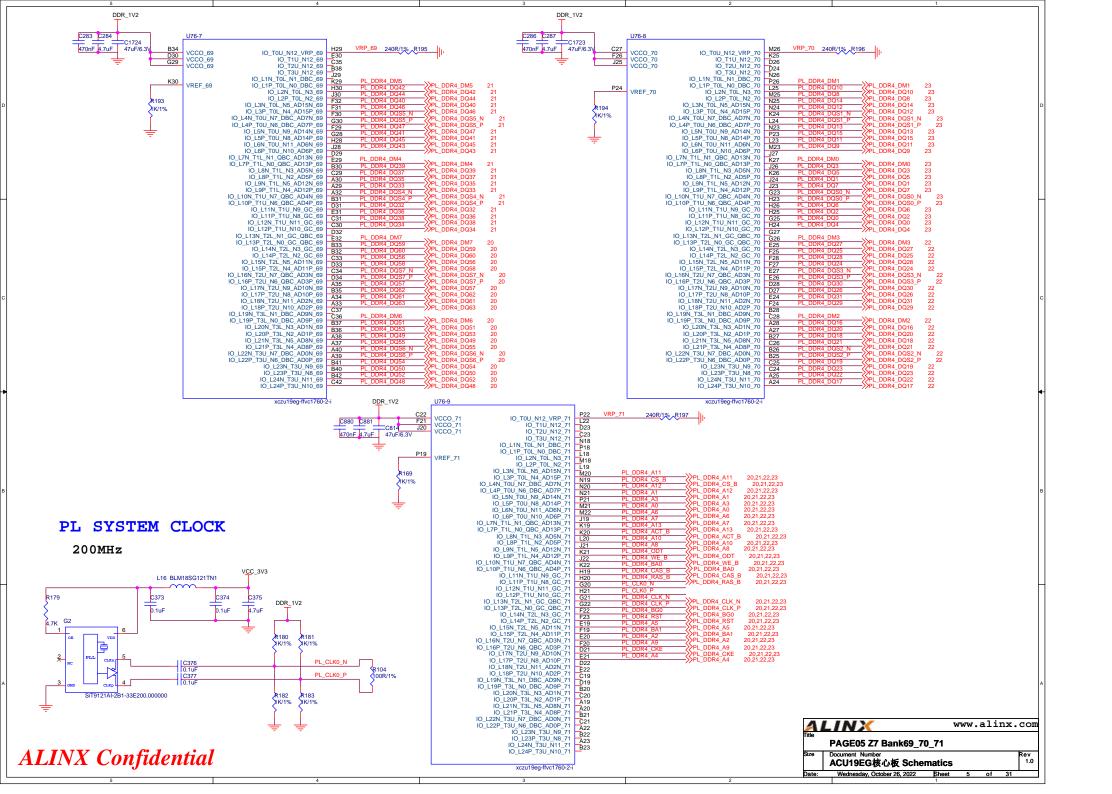


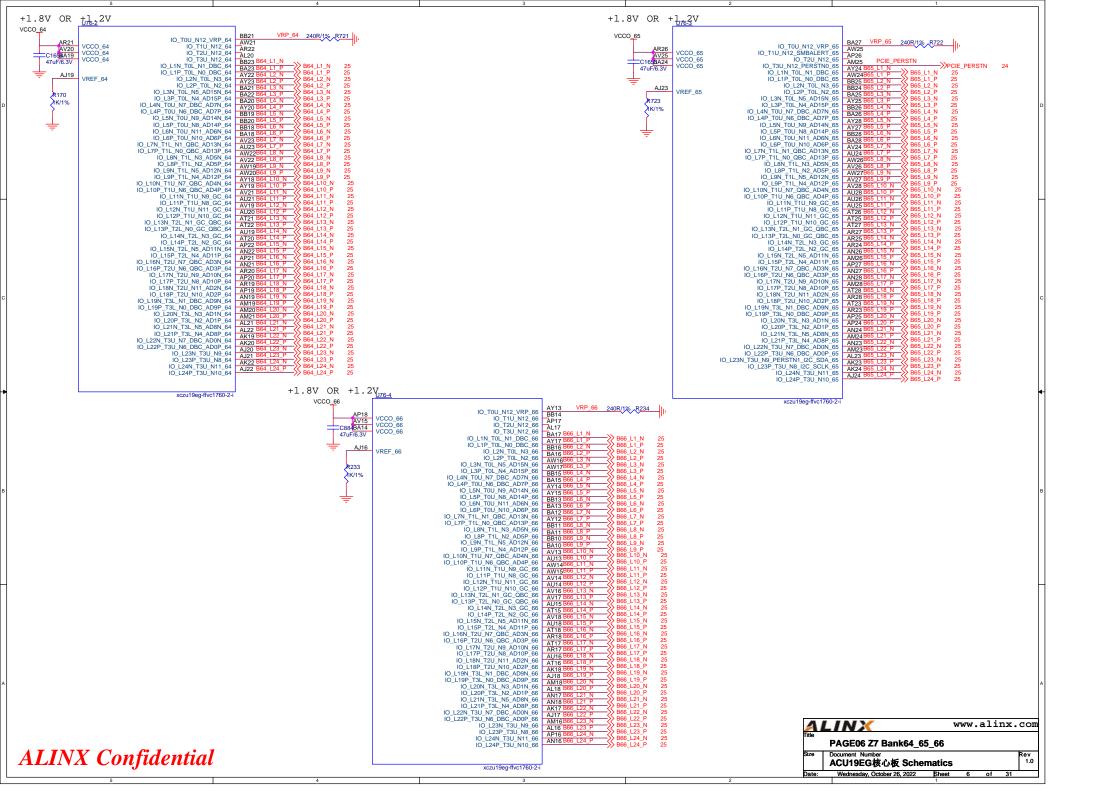


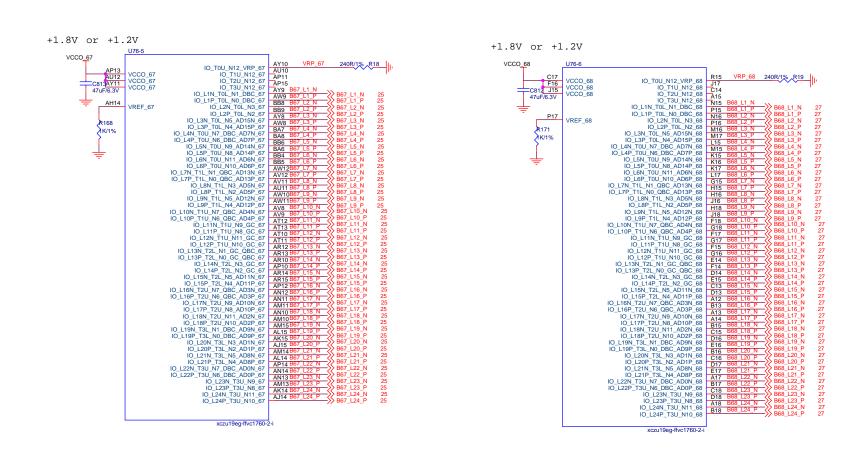


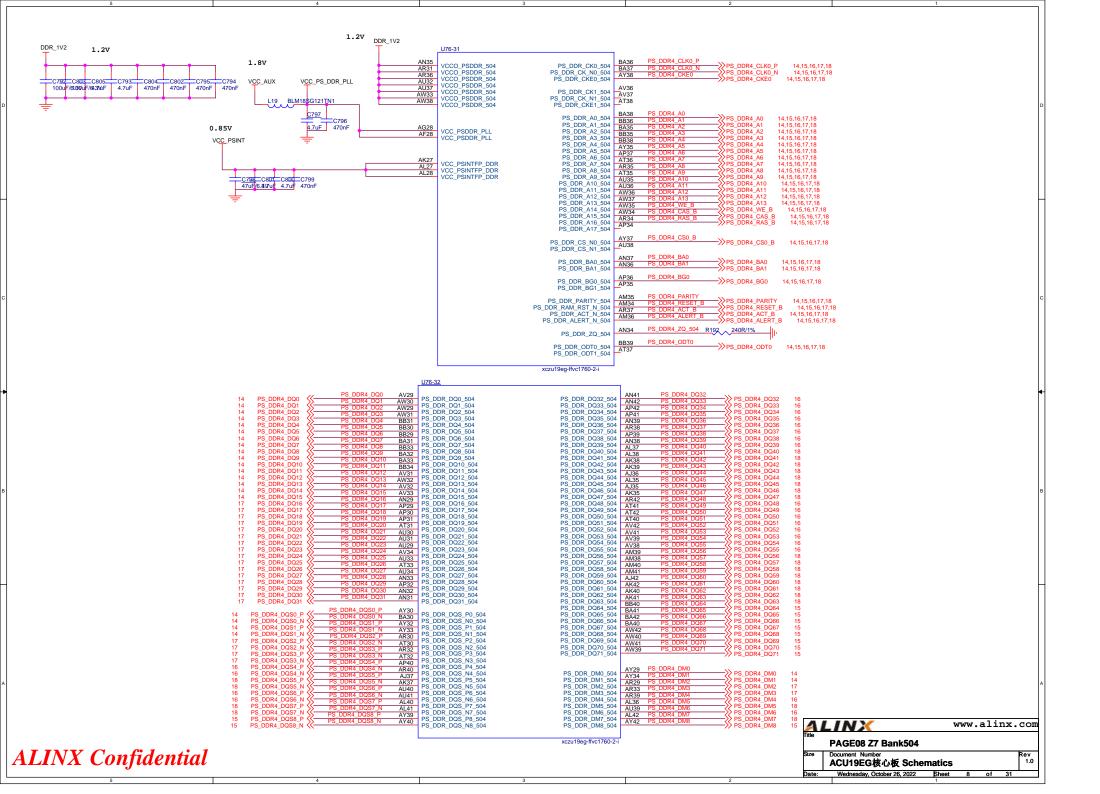


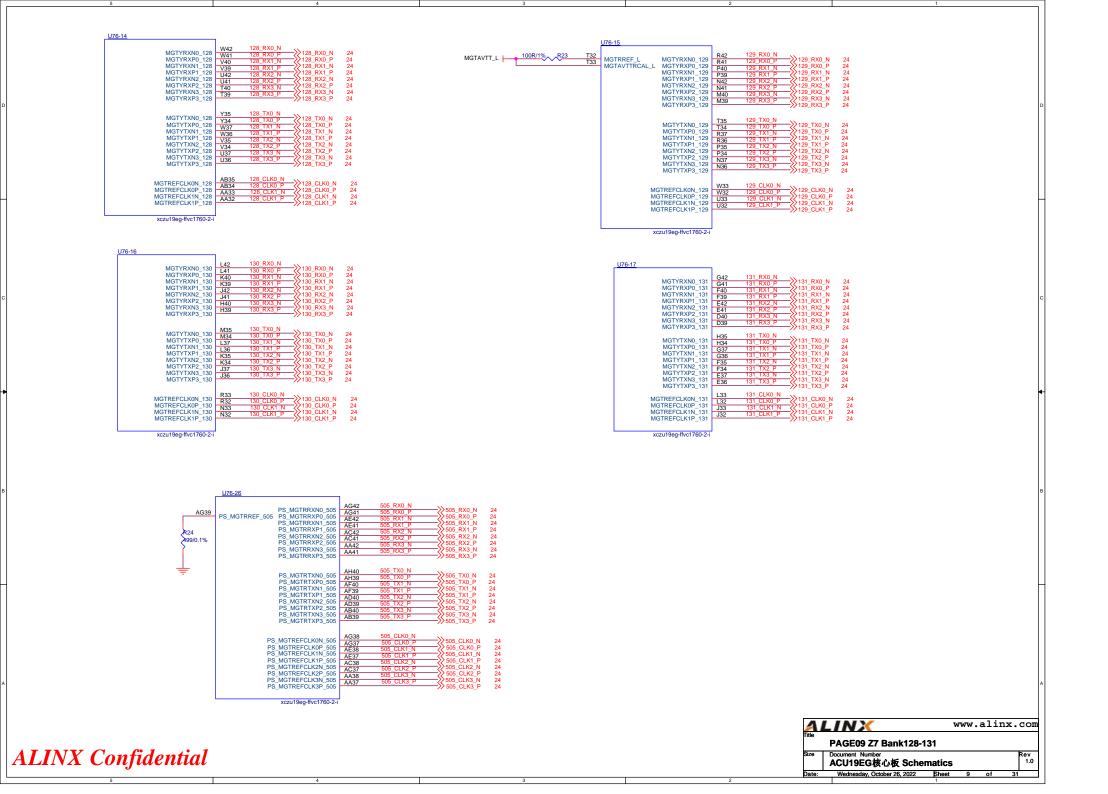


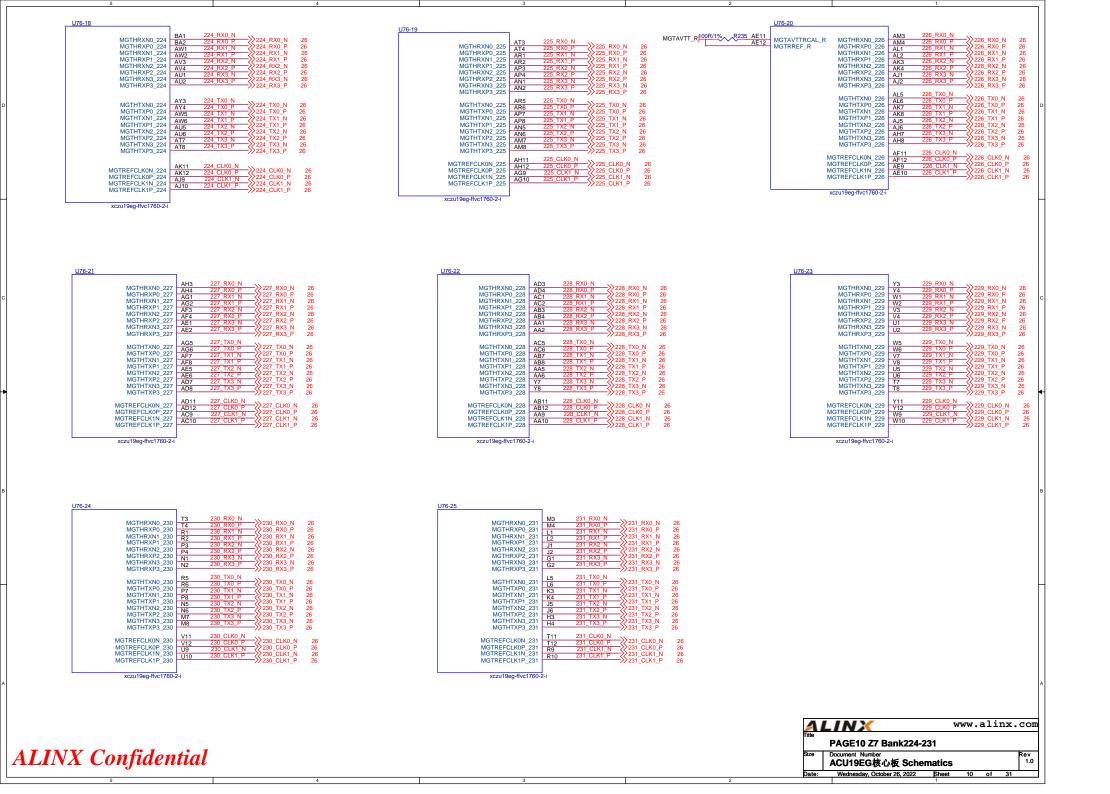


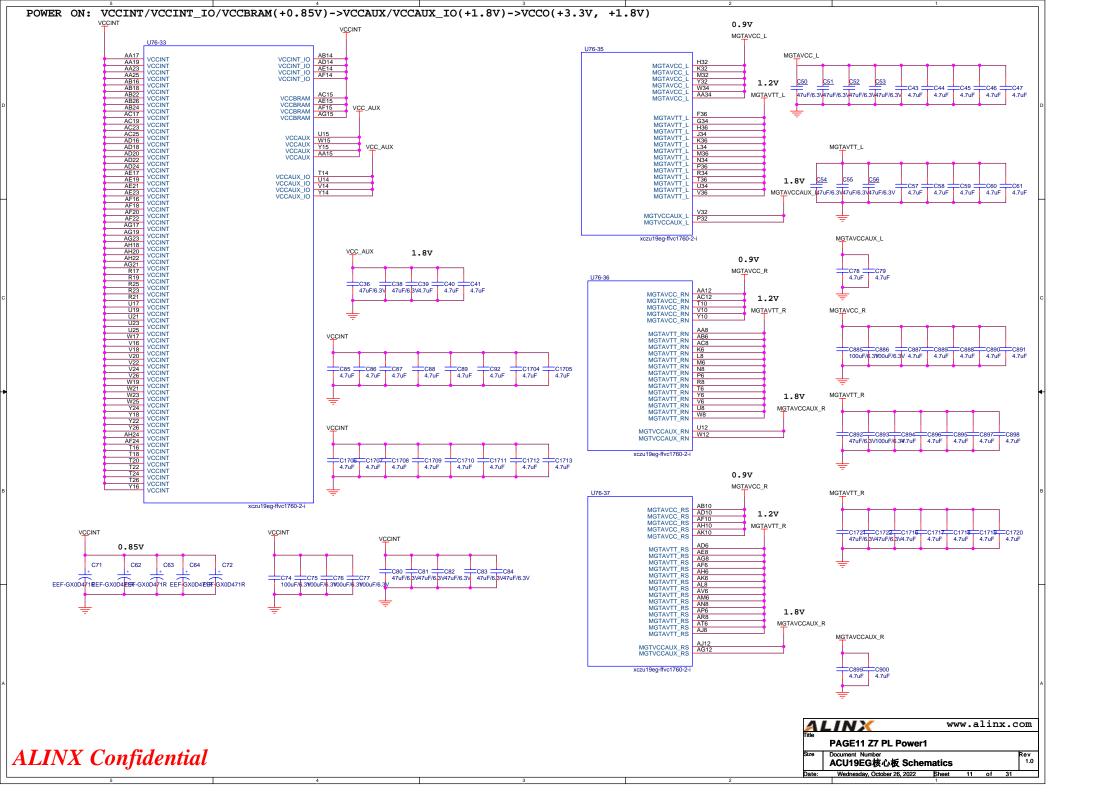




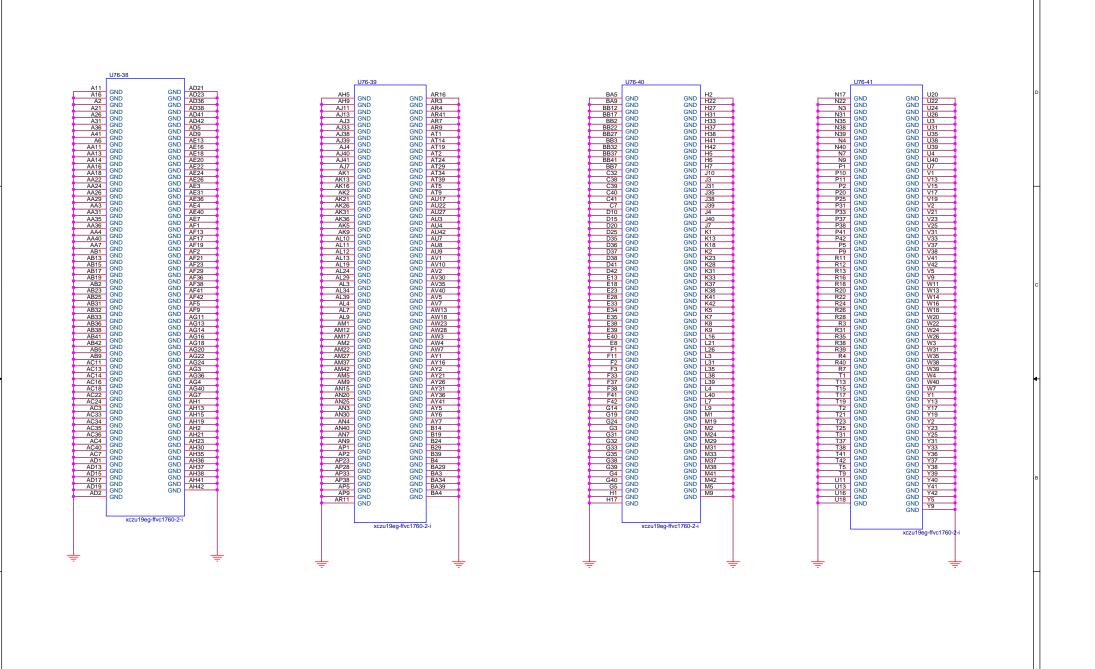


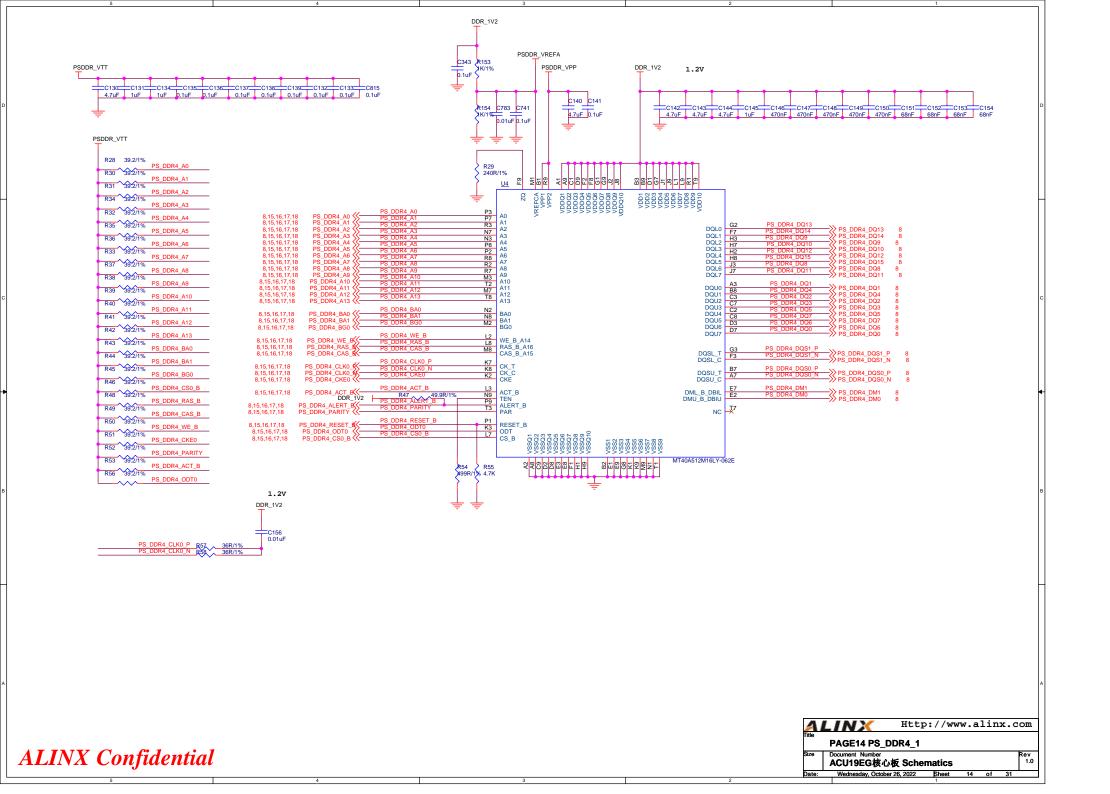


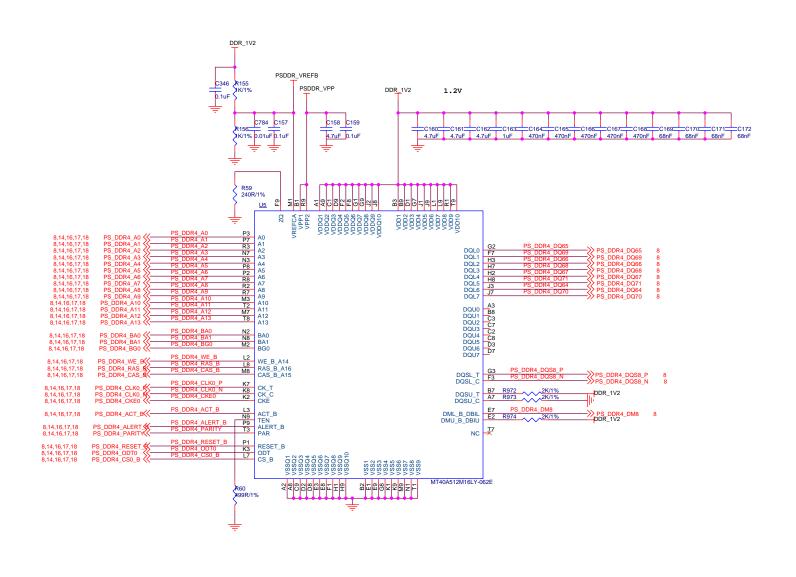




POWER ON: VCC\_PSINTFP\_VCC\_PSINTFP\_DDR(+0.85V)->VPS\_MGTRAVCC(+0.9V), VCC\_PSDDR\_PLL(+1.8V)->VPS\_MGTRAVTT(+1.8V), VCCO\_PSDDR() POWER ON: VCC\_PSINTLP(+0.85V)->VCC\_PSAUX(+1.8V), VCC\_PSADC(+1.8V), VCC\_PSPLL(+1.2V)->VCCO\_PSIO(+1.8V) 0.85V 1.8V VCC\_PSINT VCC\_PSINT U76-34 VCC\_AUX VCC\_AUX PS\_PLL for VCC\_PSINTLP for VCC\_PSAUX 1.2V VCC\_PSAUX VCC\_PSAUX VCC\_PSAUX VCC\_PSAUX VCC\_PSAUX C104 C105 C107 100uF/6.3V 100uF/6.3V 4.7uF PS\_PLL C121 100uF/6.3V 4.7uF 100uF/6.3¥.7uF 4.7uF VCC\_PSPLL VCC\_PSPLL VCC\_PSPLL AF27 PS\_AVCC PS\_MGTRAVCC PS\_MGTRAVCC PS\_MGTRAVCC PS\_AVTT PS\_AVTT PS\_AVCC 1.8V 0.85V 0.85V PS\_MGTRAVTT PS\_MGTRAVTT PS\_MGTRAVTT VCC\_PSINT for VCC\_PSINTFP C93 4.7uF C95 C96 470nF 470nF C94 4.7uF VBAT\_IN C108 C109 C110 C115 100uF/6.3V 100uF/6.3V 100uF/6.3V 4.7uF VCC\_PSBATT 4.7uF 4.7uF 4.7uF xczu19eg-ffvc1760-2-i ALINX www.alinx.com PAGE12 Z7 PS Power2 **ALINX** Confidential Rev 1.0 ACU19EG核心板 Schematics Wednesday, October 26, 2022



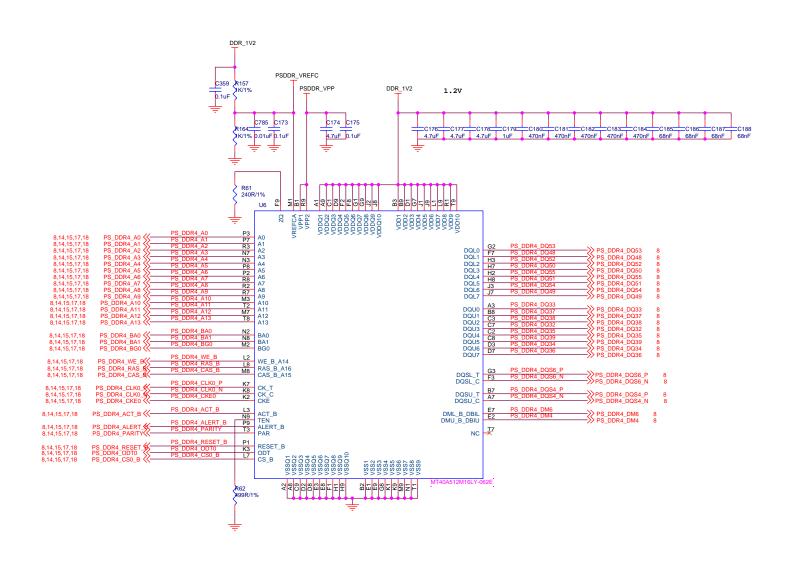




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PAGE15 PS\_DDR4\_2

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Date: Wednesday, October 26, 2022 Sheet 15 of 31

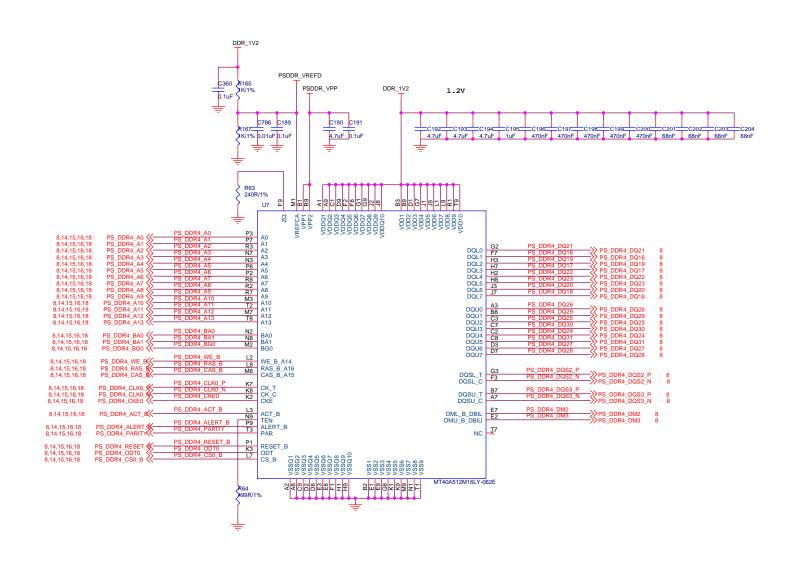


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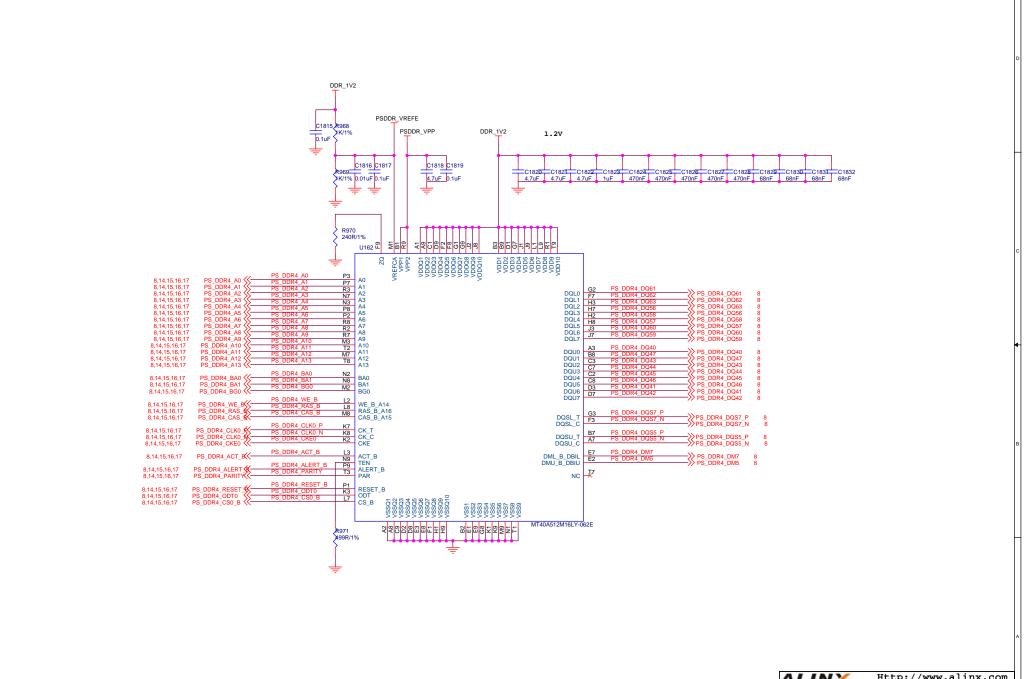
PAGE16 PS\_DDR4\_3

Size Document Number ACU19EG核心板 Schematics

Date: Wednesday, October 26, 2022 Sheet 16 of 31



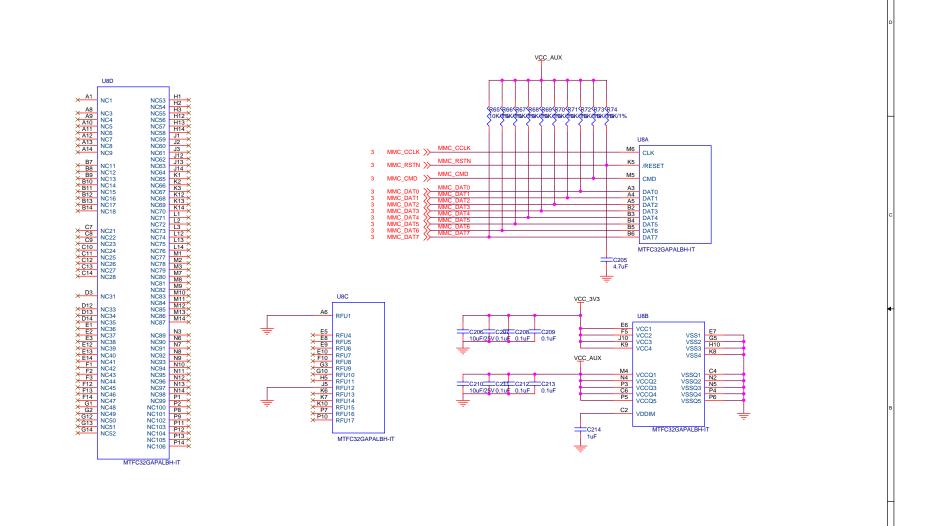
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PAGE17 PS\_DDR4\_4
Size Document Number
ACU19EG核心板 Schematics
Date: Wednesday, October 26, 2022 Sheet 17 of 31



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PAGE17 PS\_DDR4\_4

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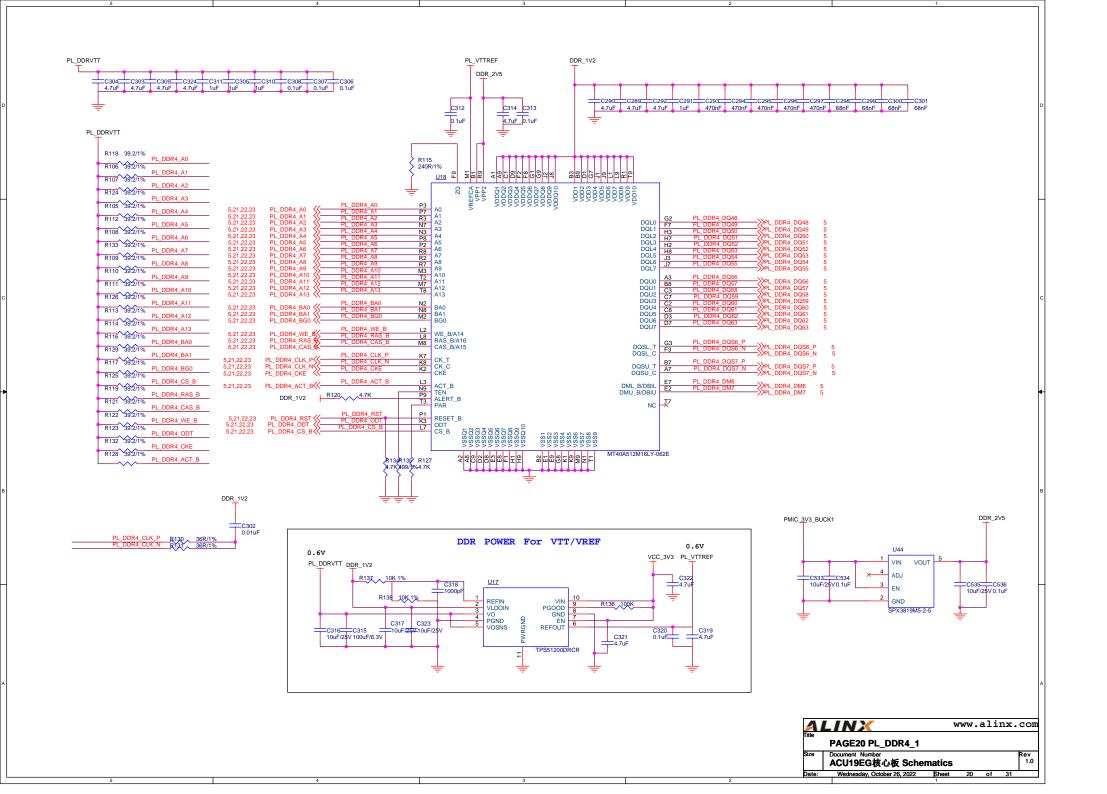
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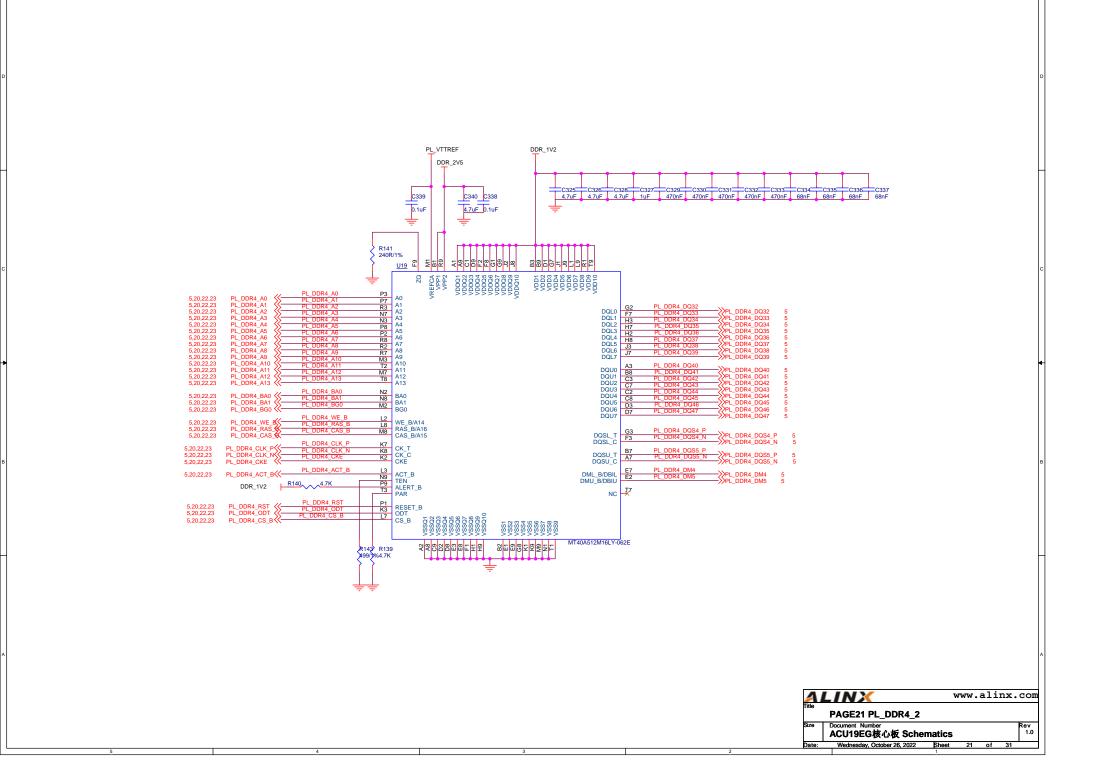
PAGE19 eMMC

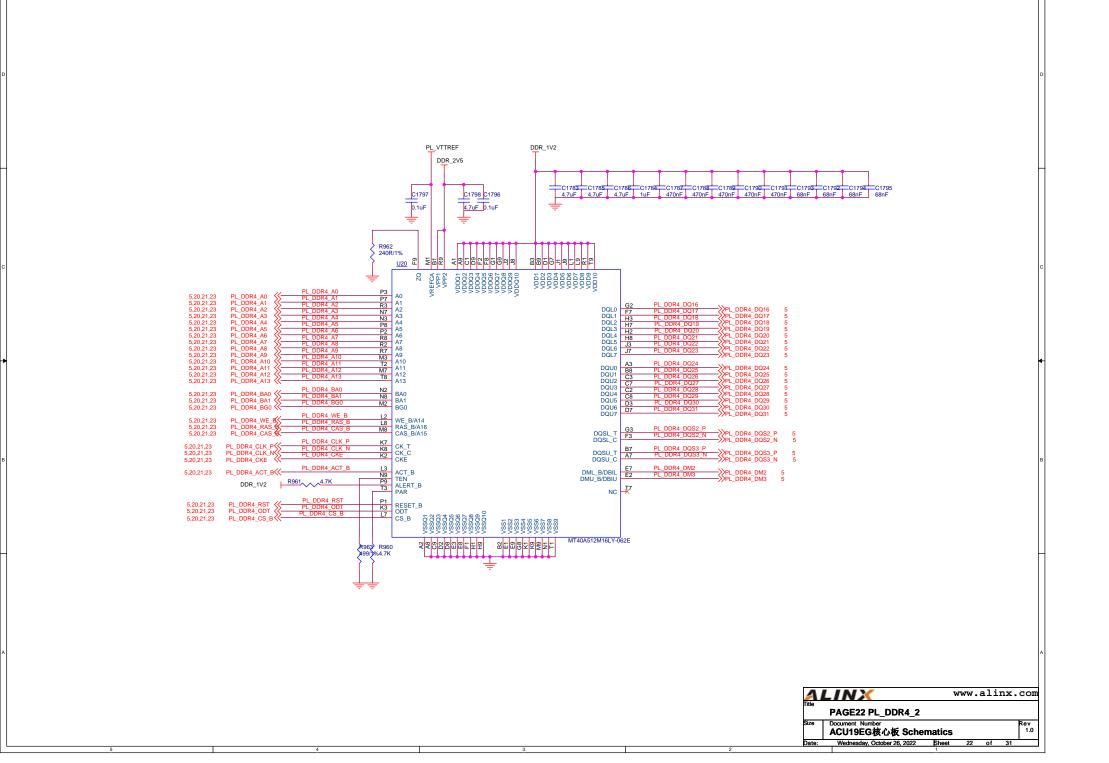
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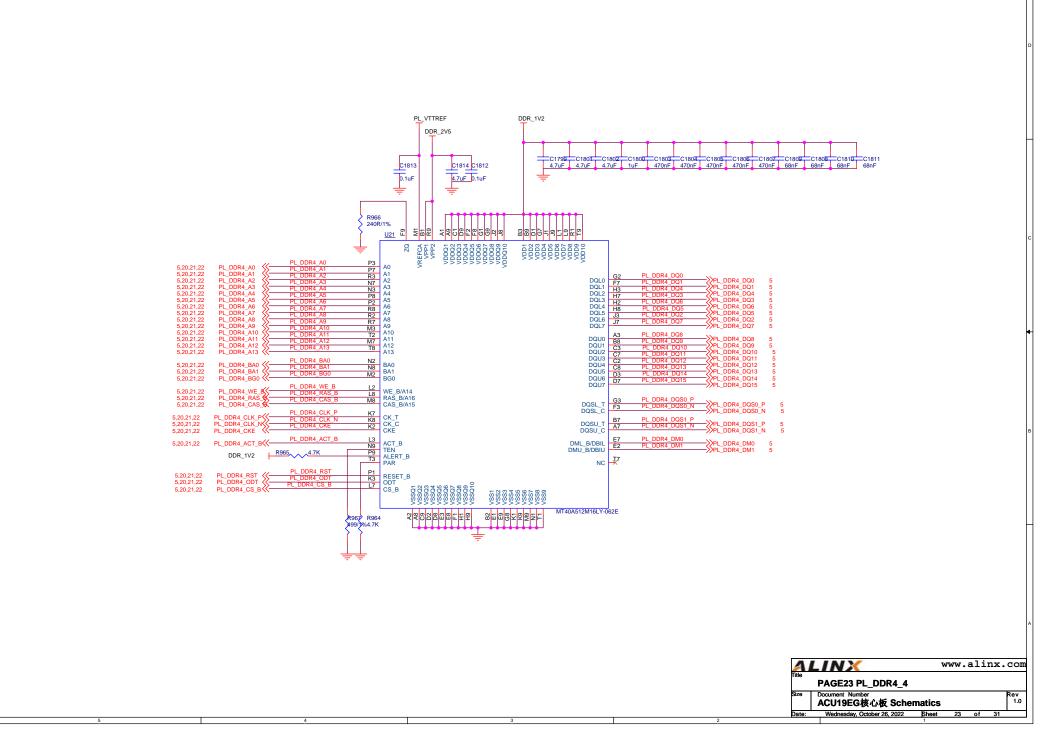
Date: Wednesday, October 26, 2022 Sheet 19 of 31

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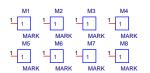




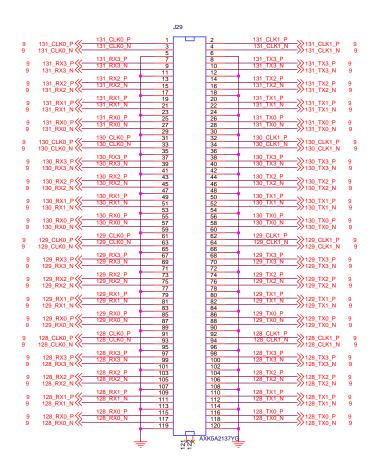
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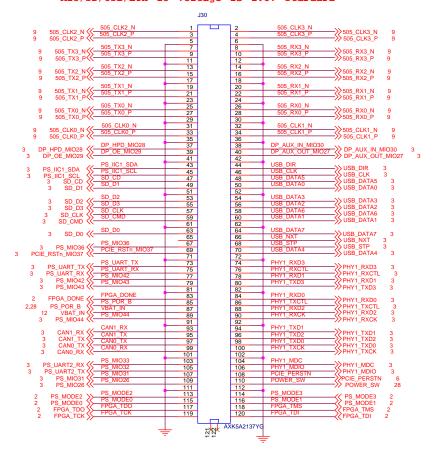
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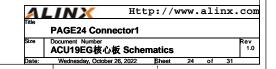
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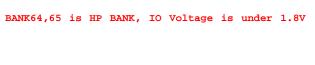


## MIO/SD/USB/ETH IO Voltage is 1.8V Stardard



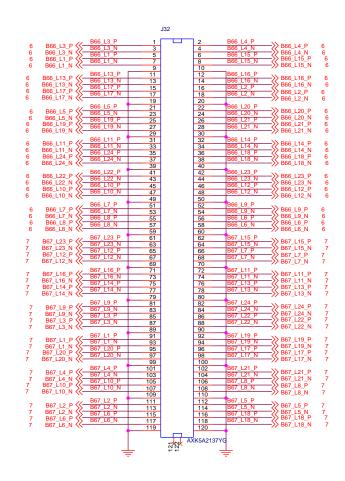


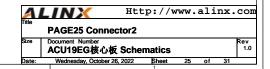


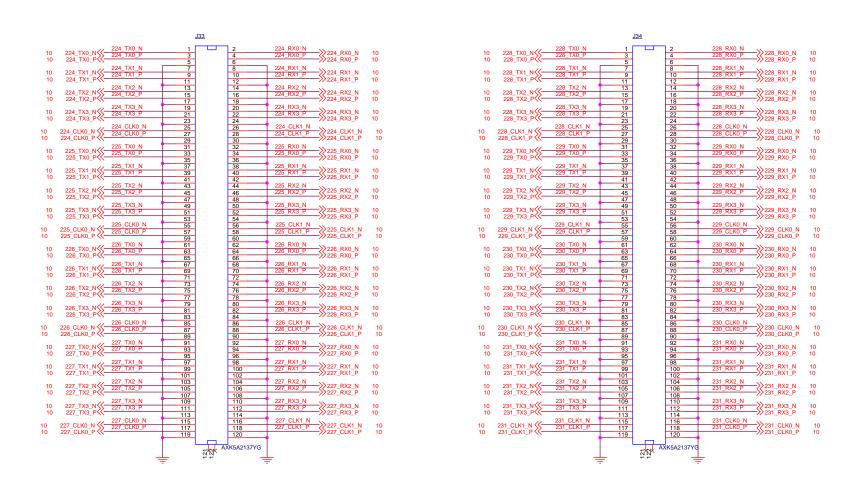


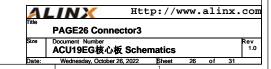
BANK66 67 is HP BANK, IO Voltage is under 1.8V

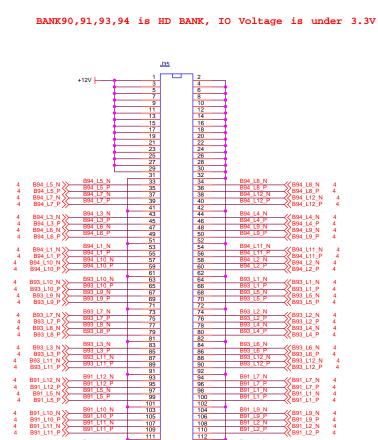
			J31				
_	B65_L10_N \( \frac{\text{B65_L10}}{\text{B65_L10}} \)	D_N 1		2	B65_L18_N	N 205 140 N	
6	B65_L10_N \SB65_L10	D_P 3		4	B65_L18_P	B65_L18_N	6 6
6 6	B65_L10_P B65_L16_N B65_L16_N	6_N 5		6	B65_L15_N	- S B65_L18_P	6
6	B65_L16_N B65_L10	6_P 7		8	B65_L15_P	->> B65_L15_N ->> B65_L15_P	6
0	P02_F10_F	9		10		-// D02_L15_P	0
6	B65_L1:	3_N 11		12	B65_L9_N	W pos 10 N	
6	B65_L13_P B65_L13_P B65_L5	3_P 13		14	B65_L9_P	B65_L9_N	6
	B65_L13_P \> B65_L5	N 15		16	B65_L12_N	→>> B65_L9_P →>> B65_L12_N	6 6
6	B65_L5_N SB65_L5	_P 17		18	B65_L12_P	B65_L12_N	6
6	B65_L5_P <<	19		20		-// B03_L12_F	U
6	B65_L8_N \( \int \) \( \frac{\text{B65_L8}}{\text{B65_L8}} \)	_N 21		22	B65_L11_N	→>> B65_L11_N	6
	B65_L8_N >> B65_L8	P 23		24	B65_L11_P	B65_L11_P	6
6 6	B65_L8_P B65_L2			26	B65_L6_N		6
6	B65_L20_P B65_L20			28	B65_L6_P	−>>> B65_L6_N −>>> B65_L6_P	6
U	B05_L20_F	29		30		// B05_L0_P	0
6	B65_L4_N << B65_L4			32	B65_L14_N	→>> B65 L14 N	6
6				34	B65_L14_P	->> B65_L14_P	6
6				36	B65_L3_N		6
6	B65_L2_N B65_L2			38	B65_L3_P	→ B65_L3_N	6
٠		39		40	DOE LA N	// DOO_LO_F	·
6	B65_L7_N < B65_L7	N 41		42	B65_L1_N	→>> B65_L1_N	6
6		. 10		44	B65_L1_P		6
6				46	B65_L22_N	-SS B65_L22_N	6
6	B65_L21_P B65_L2			48	B65_L22_P	->> B65_L22_P	6
		49		50	DOE LOO N	//	
6	B65_L19_N \( \sum_{\text{B65}} \text{L1!}			52	B65_L23_N B65_L23_P	->> B65 L23 N	6
6				54		->> B65_L23_P	6
6	B65_L24_N B65_L24_N B65_L24_P	4_N 55		56	B65_L17_N	→>> B65_L17_N	6
6	B65 L24 P ( B65_L24	4_P 57		58	B65_L17_P	->> B65_L17_P	6
	"	59		60	B64_L1_P	//	
6	B64_L23_P \( \frac{\text{B64_L23}}{\text{B64_L23}}	3_P 61 3_N 63		62	B64_L1_N	→>> B64_L1_P	6
6	B64_L23_N \\ B64_L2			64	B64 L16 P	→>> B64_L1_N	6
6	B64_L24_P \$\int\{\text{B64_L24}\}\\ \text{B64_L24}\]			66 68	B64 L16 N	->> B64 L16 P	6
6	B64_L24_N <<	60		70	B04_L10_N	->> B64_L16_N	6
	B64_L7_P \$\frac{\text{B64_L7}}{\text{B64_L7}}\$	P 71		72	B64 L22 P		
6	B64_L7_P \$\int \text{B64_L7}	N 73		74	B64 L22 N	->> B64_L22_P	6
6	B64_L7_N \$\infty \begin{array}{c} \text{B64} \text{L2} \\ \text{B64} \text{L2} \end{array}	P 75		76	B64 L19 P	→>> B64_L22_N	6
6	B64_L2_P \$\int \text{B64_L2}			78	B64_L19_N	→>> B64_L19_P	6
6	B64 L2 N (	79		80	B01_E10_11	→>> B64_L19_N	6
	B64 L2			82	B64 L14 P		
6	B64_L20_P << B64_L20			84	B64 L14 N	−>>> B64_L14_P	6
6	B64_L20_N \$\infty \text{B64_L1}			86	B64_L3_P	−>>> B64_L14_N	6
6	B64_L11_P << B64_L1			88	B64_L3_N	>> B64_L3_P	6
6	B64_L11_N ((	90		90		→>> B64_L3_N	6
_	B64_L18_P \( \frac{\text{B64_L18}}{\text{B64_L18}} \)	B_P 91		92	B64_L5_P	W == = =	_
6	B64_L18_P \$\infty \text{B64_L1}	8_N 93		94	B64_L5_N	→>> B64_L5_P	6
6_	B64 L18 N X DC4 L0	P 95		96	B64_L6_P	→>> B64_L5_N	6
6	B64_L8_P \$\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	N 97		98	B64_L6_N	→ B64_L6_P	6
6	B64_L8_N <<	99		100		→>> B64_L6_N	6
6	P64 L24 P // B64_L2	1_P 101		102	B64_L9_P	-W pc4 10 p	
ь 6	B64_L21_P 864_L2 B64_L21_N 864_L2	1_N 103		104	B64_L9_N	→>> B64_L9_P →>> B64_L9_N	6
6	B64_L15_P 864_L1	5_P 105		106	B64_L10_P	S B64_L9_N B64_L10_P	6 6
6	B64_L15_P 864_L1			108	B64_L10_N	S B64_L10_P	6
		109		110		// DO4_E10_IV	0
6	B64_L13_P \( \frac{\text{B64_L13}}{\text{B64_L13}} \)	3_P 111		112	B64_L4_P	→>> B64_L4_P	6
6	B64_L13_P B64_L13_N B64_L13_N			114	B64_L4_N	B64_L4_P B64_L4_N	6 6
6				116	B64_L12_P		6
6	B64_L17_N \\ B64_L1			118	B64_L12_N	→ B64 L12 N	6
•	P04_F1/_14 //	119		120		// DU4_L12_N	U
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		Ŧ	44,	-	₹		





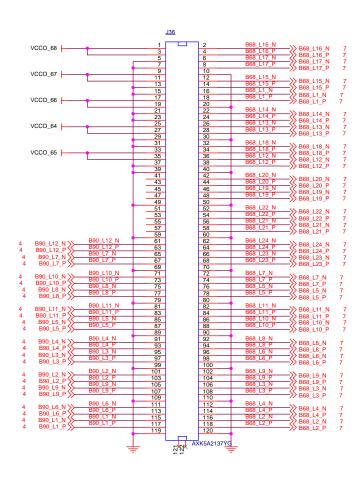


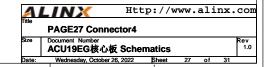


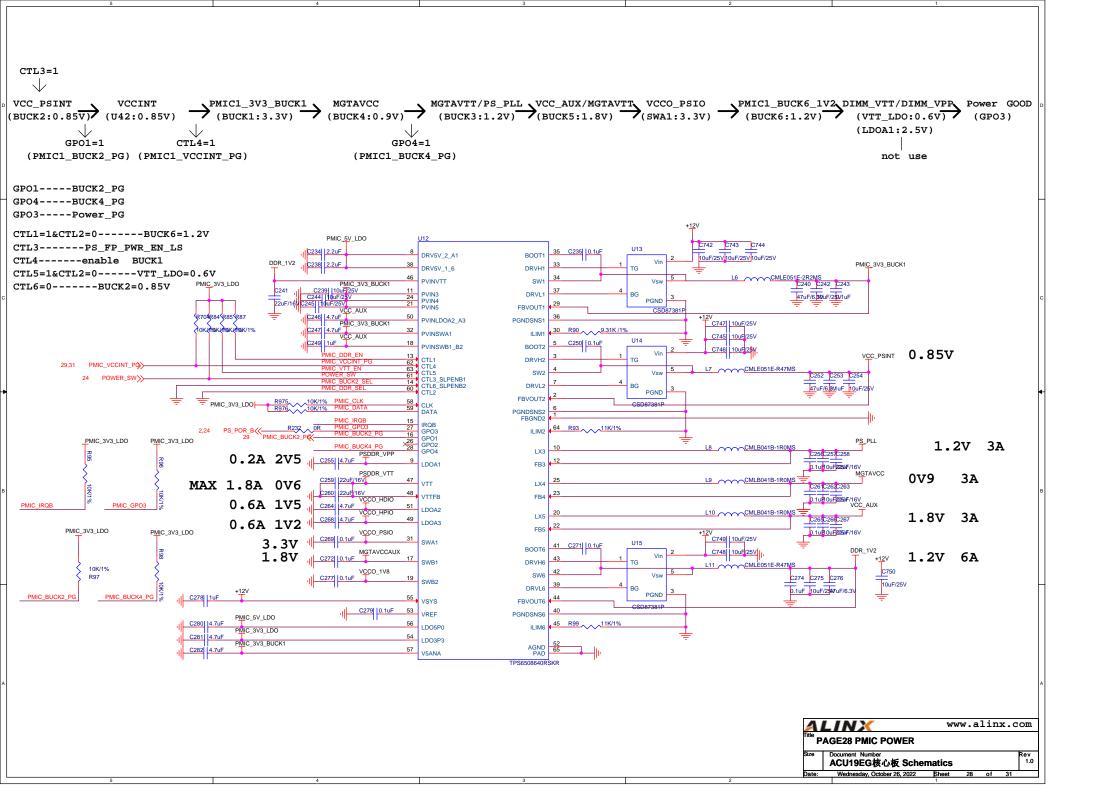


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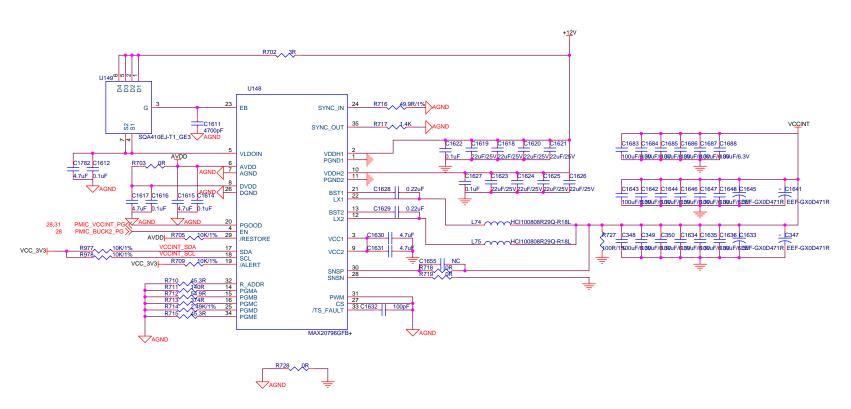
BANK68 is HP BANK, IO Voltage is under 1.8V



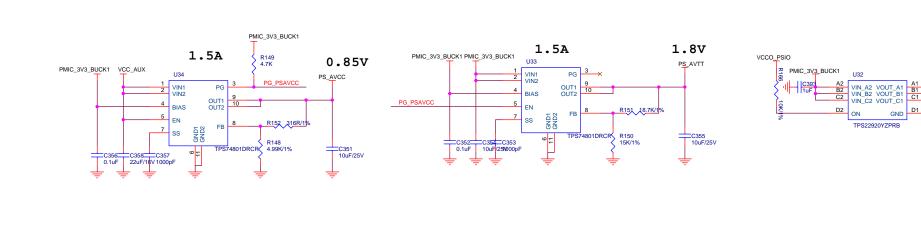




## +0.85V 60A



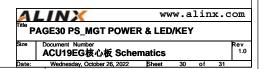




R910 330R

LED

VCC\_3V3 -



VCC\_3V3

TPS22920YZPRB

