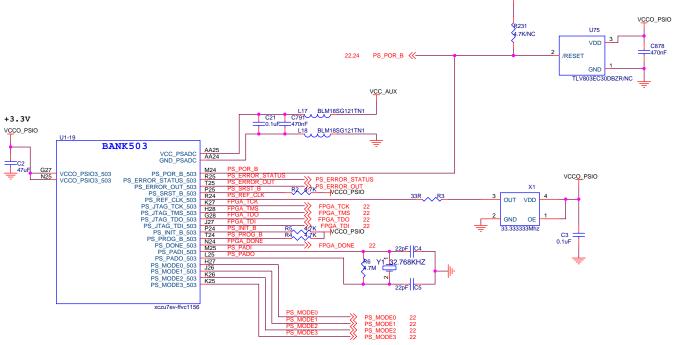
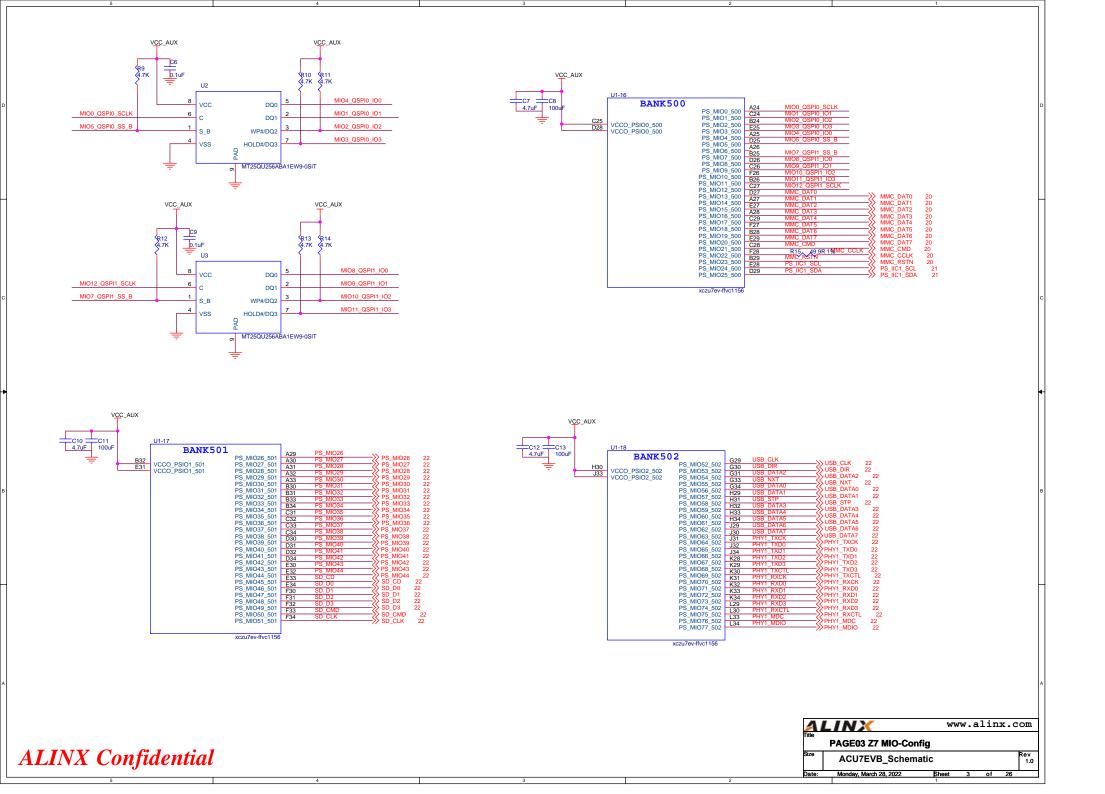
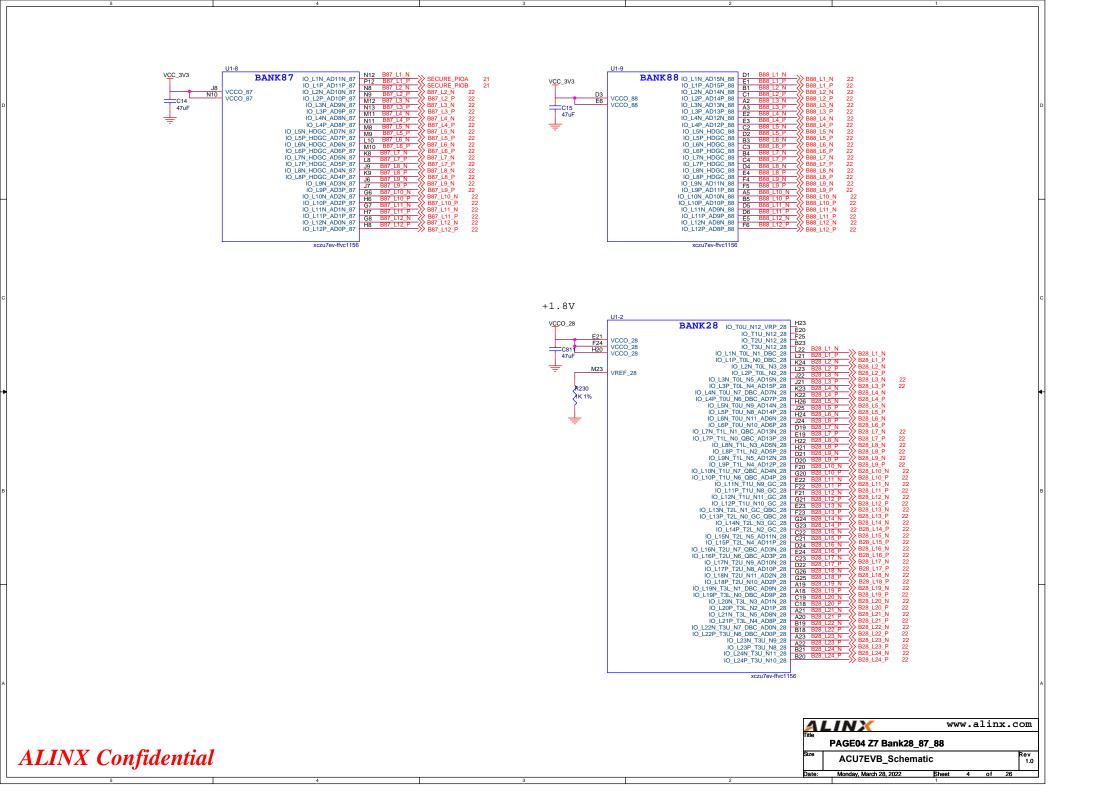


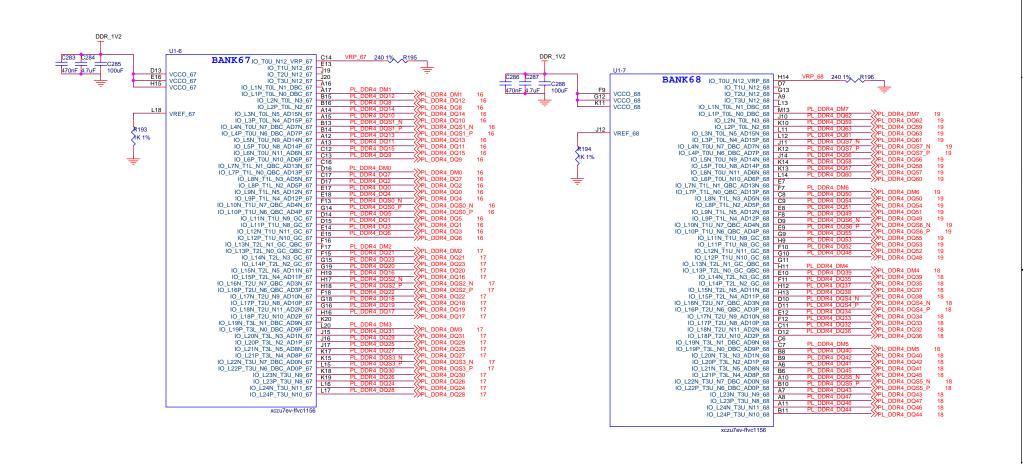
VCCO\_PSIO







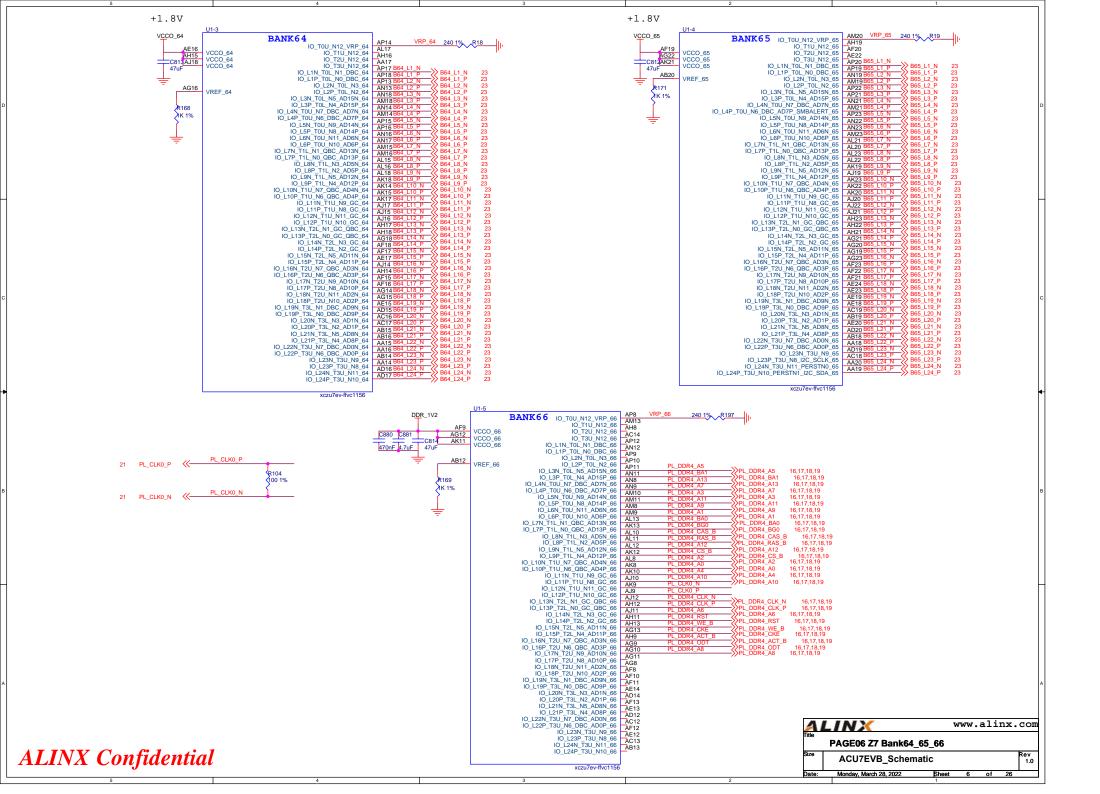


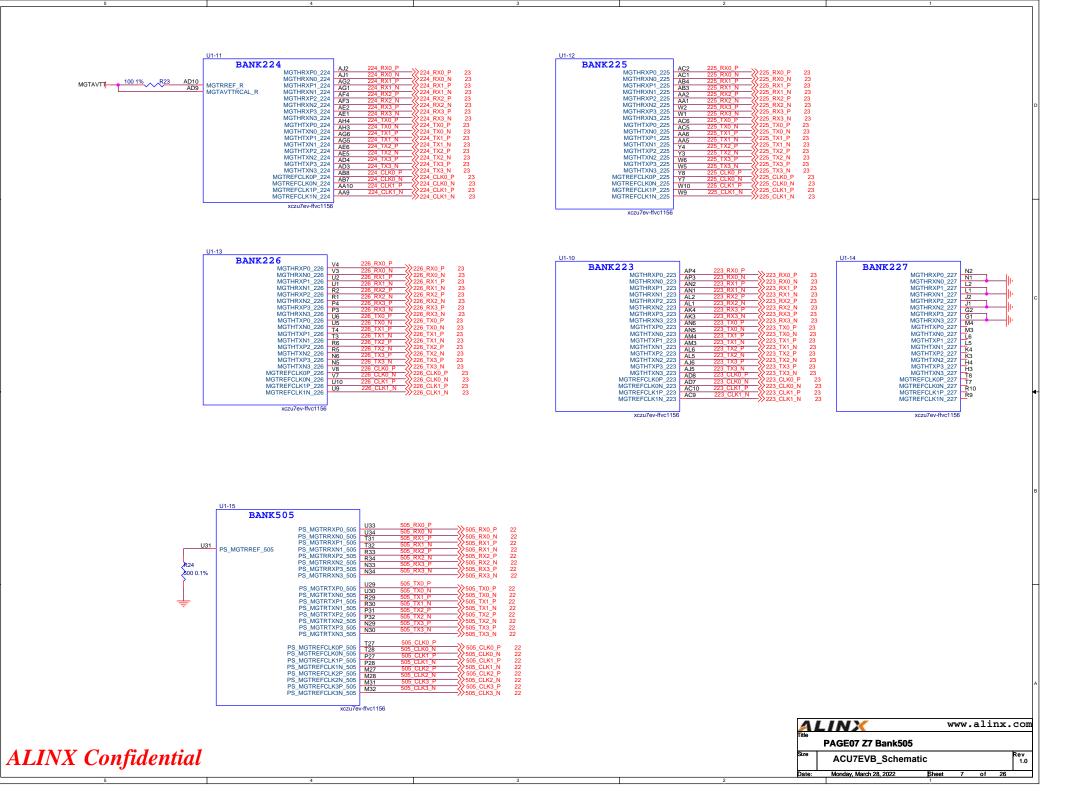


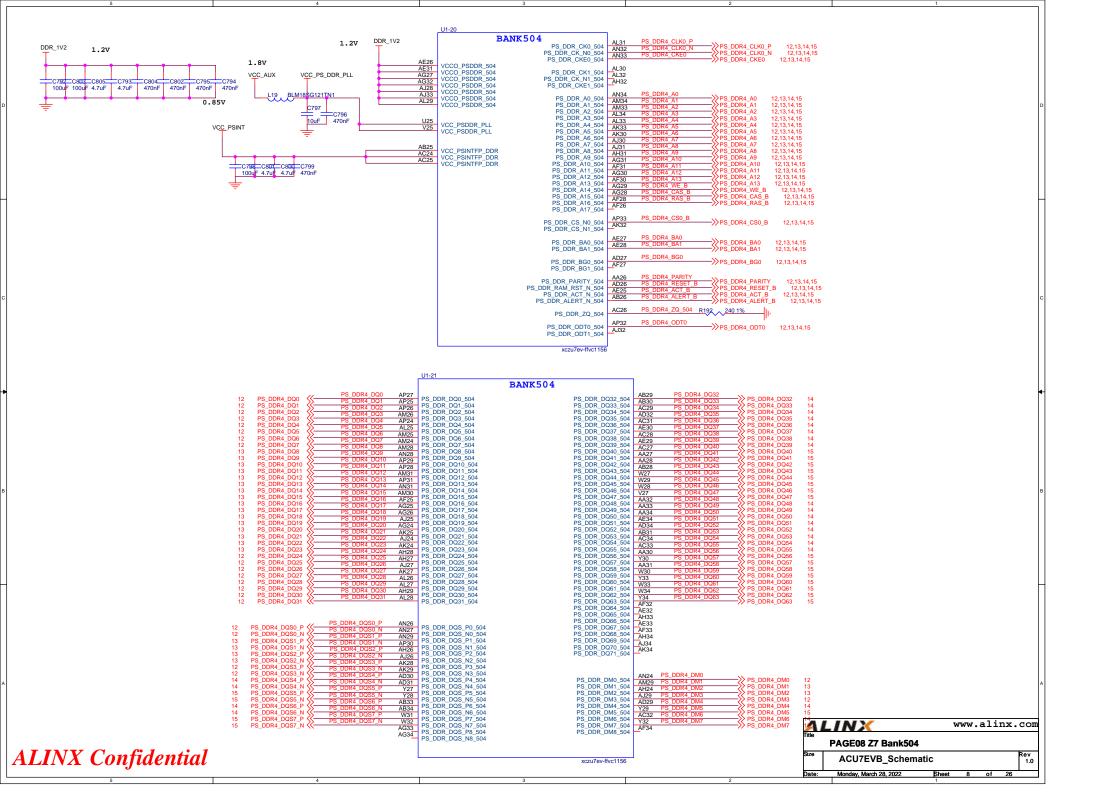
Title PAGE05 Z7 Bank67\_68

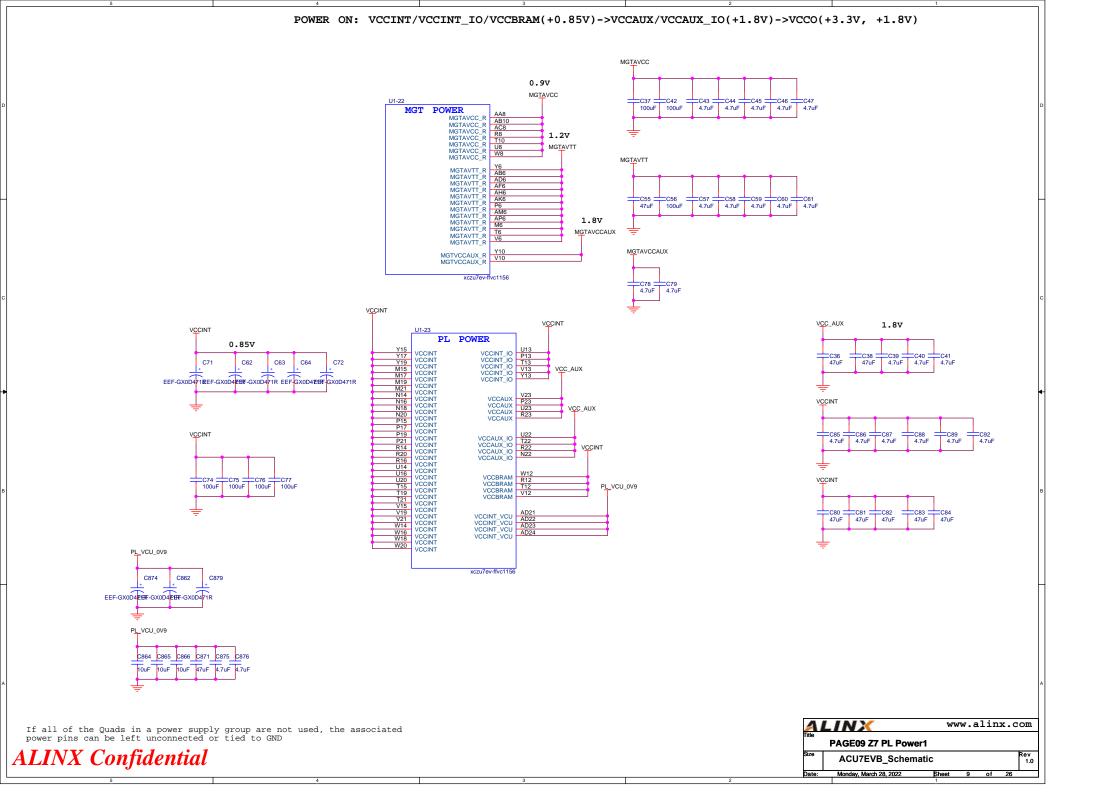
Size ACU7EVB\_Schematic Rev 1.0

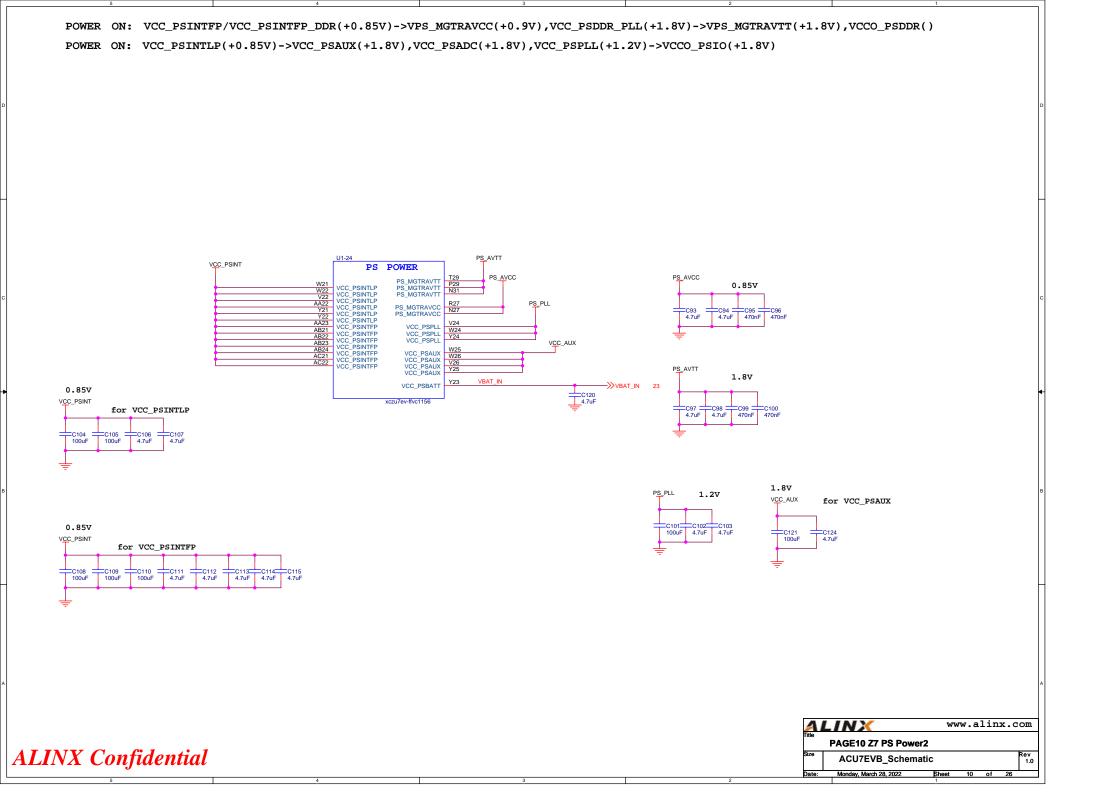
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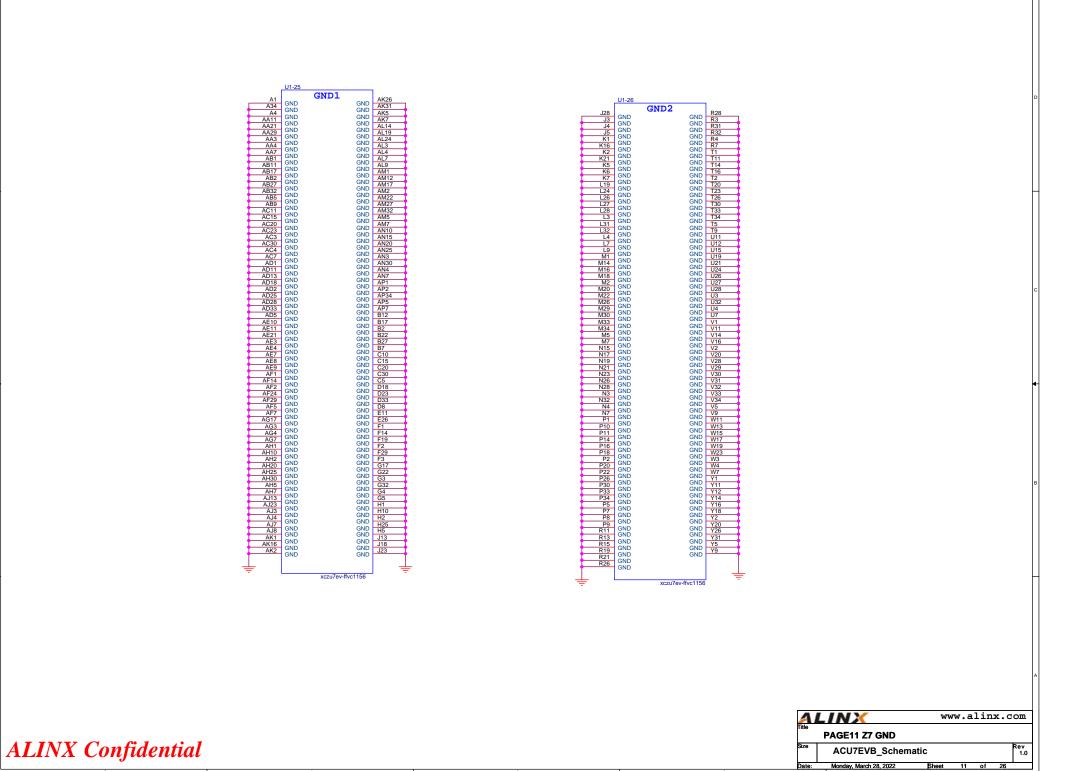


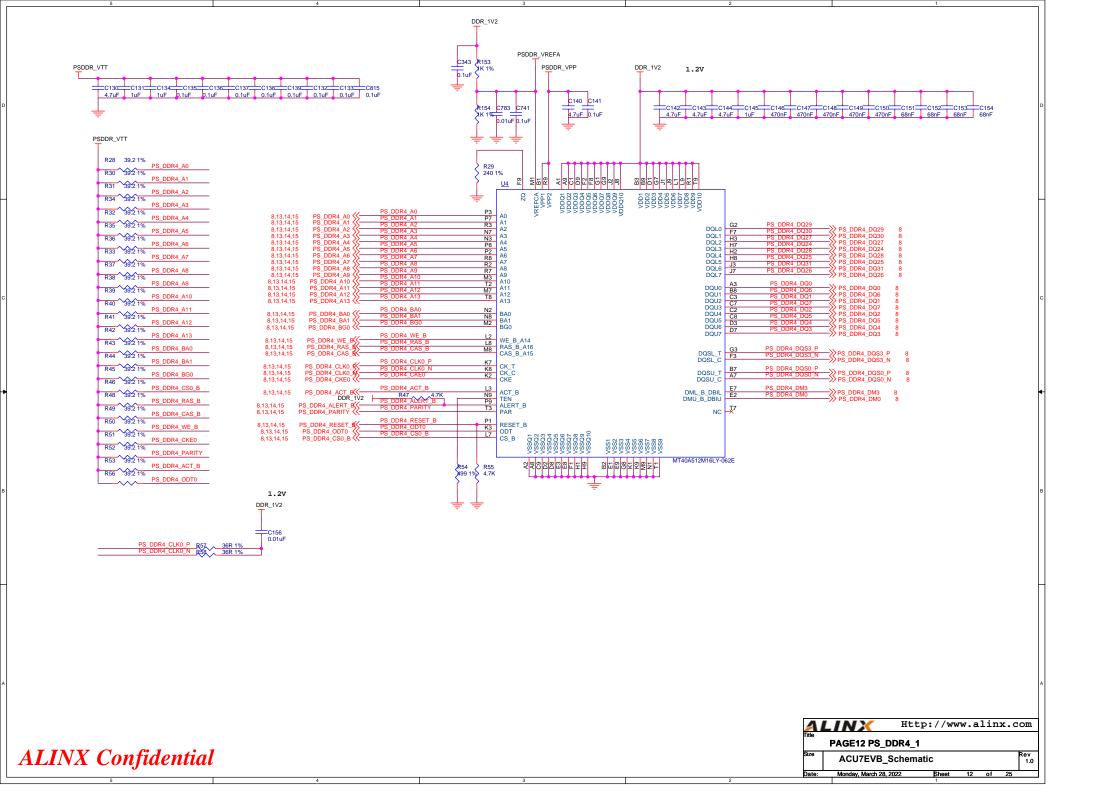


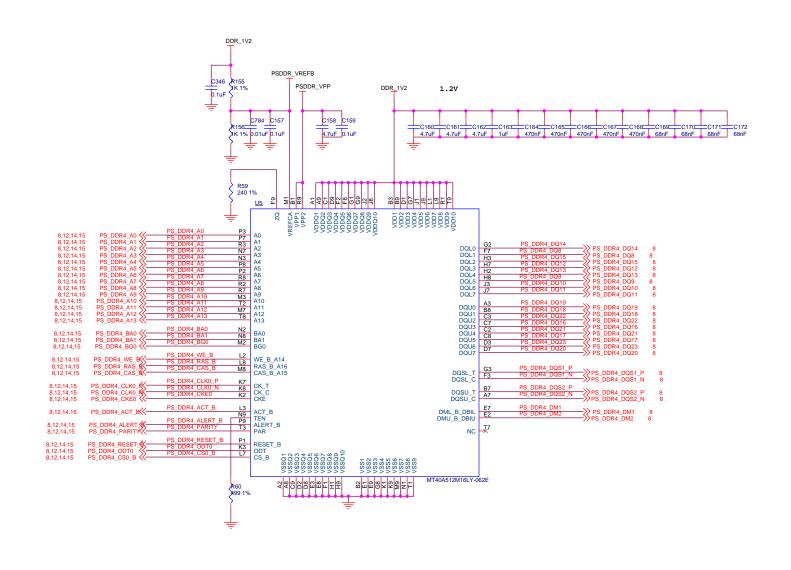










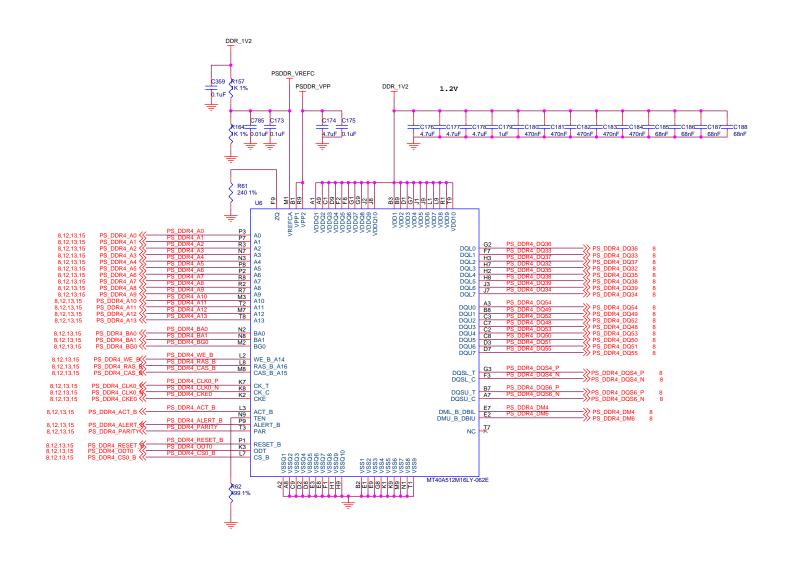


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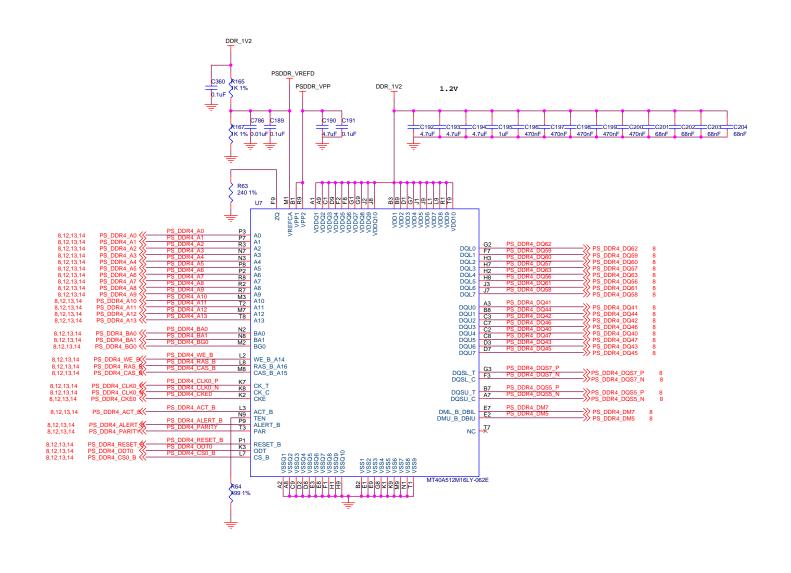
PAGE14 PS\_DDR4\_3

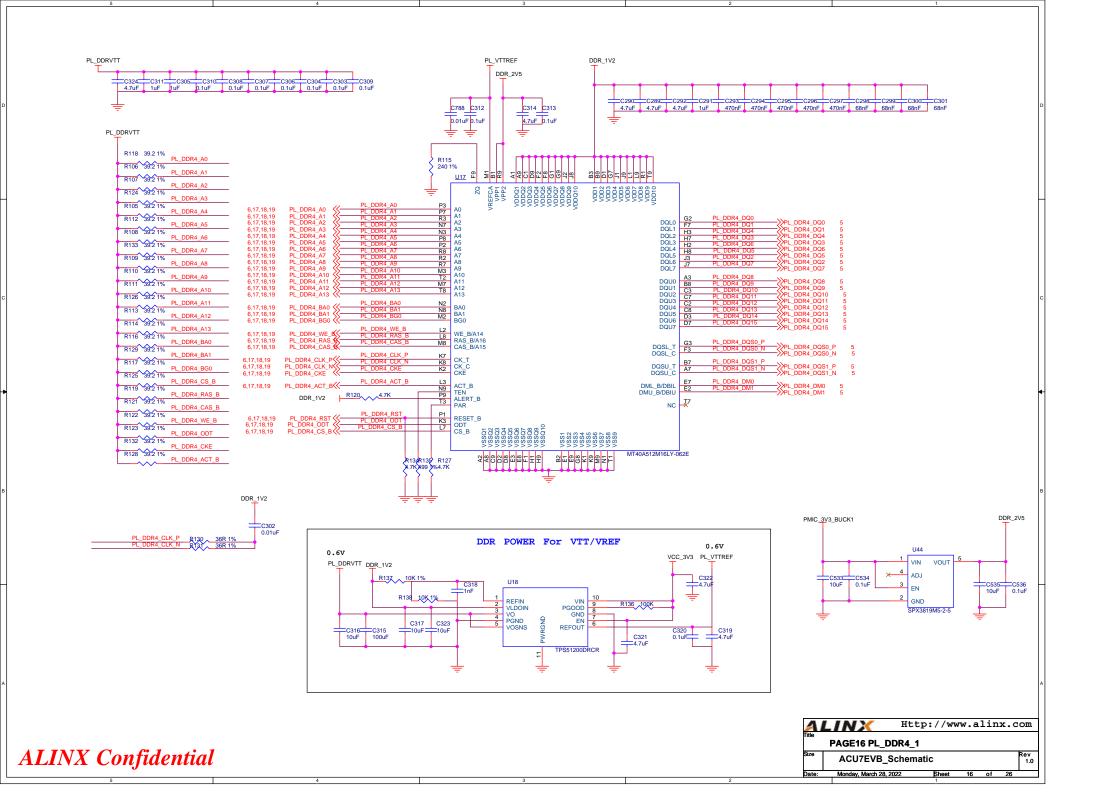
Size

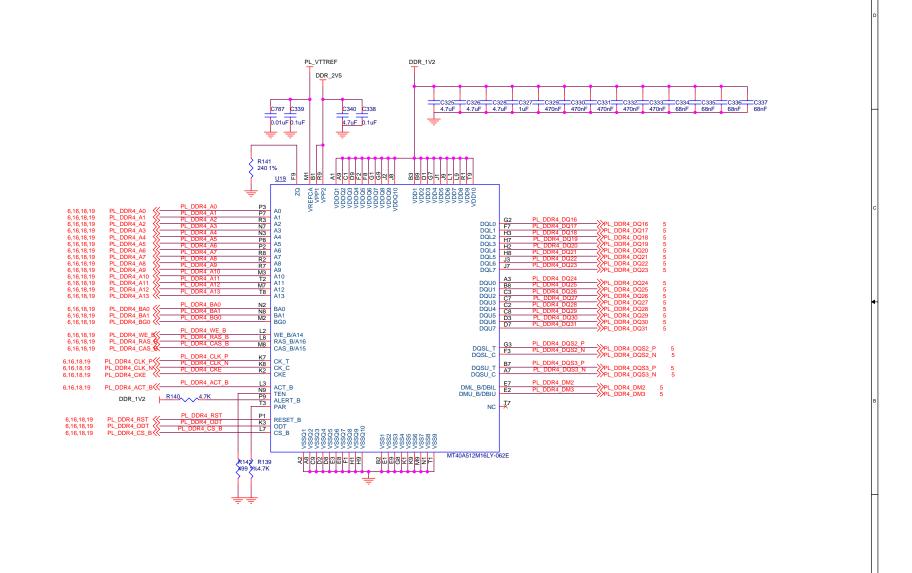
ACU7EVB\_Schematic

Date: Monday, March 28, 2022

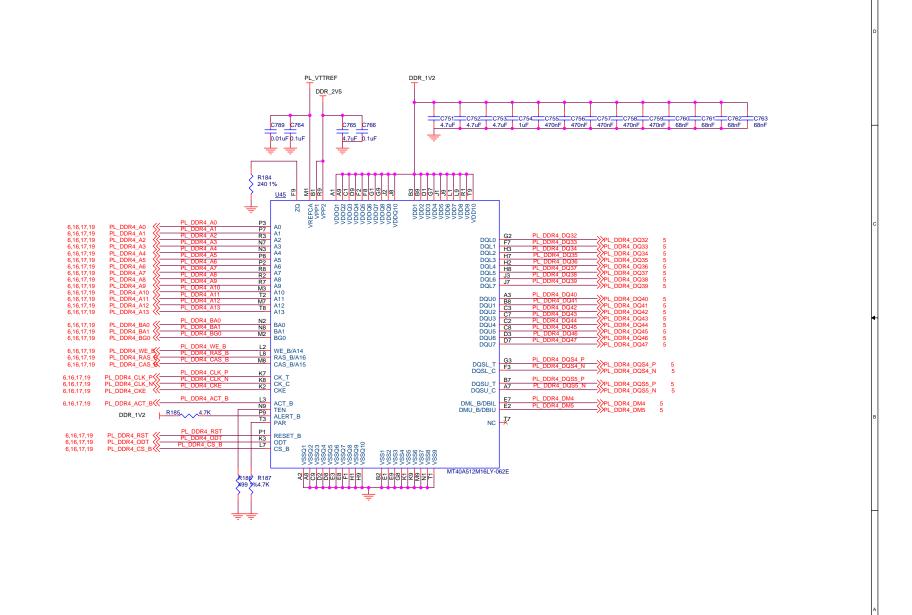
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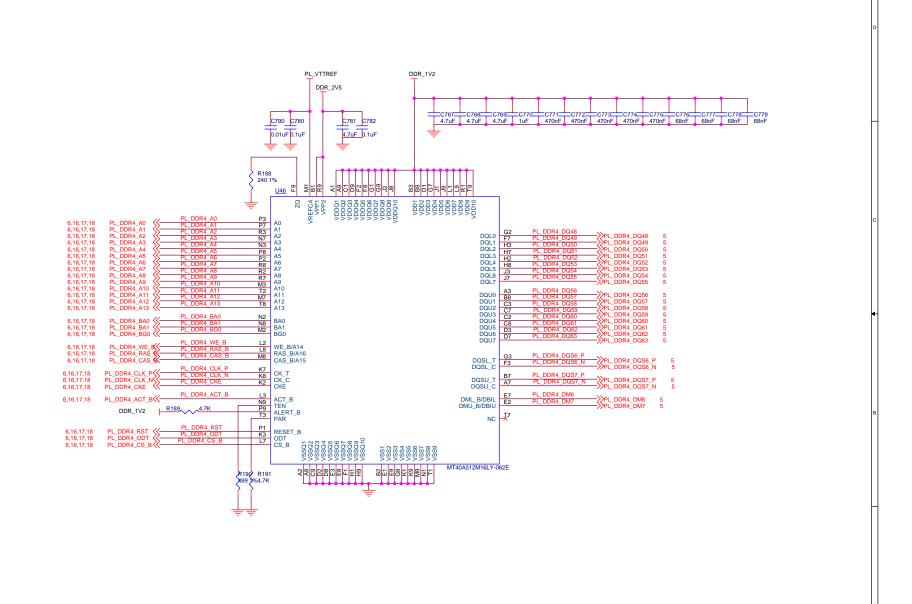
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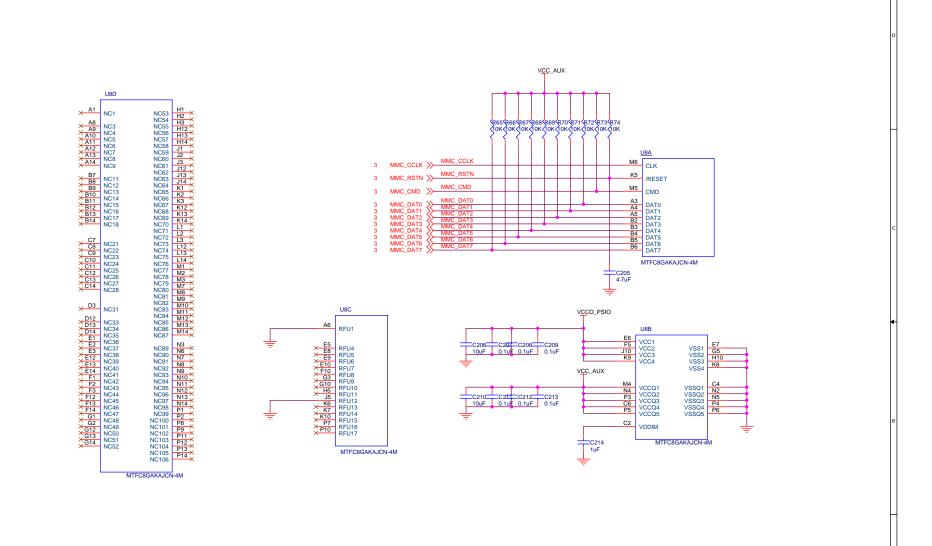
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Fitle PAGE19 PL\_DDR4\_4

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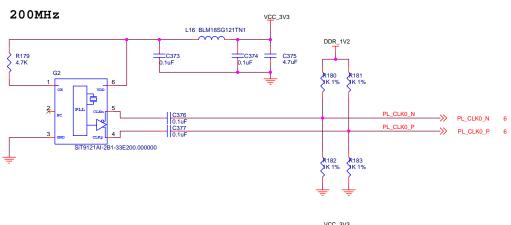
Title PAGE20 eMMC

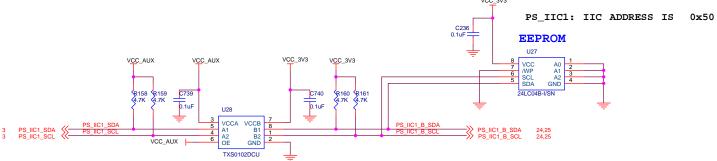
Size ACU7EVB\_Schematic Rev 1.0

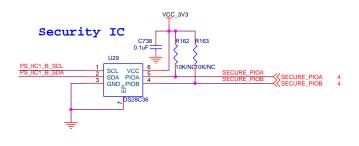
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## PL SYSTEM CLOCK

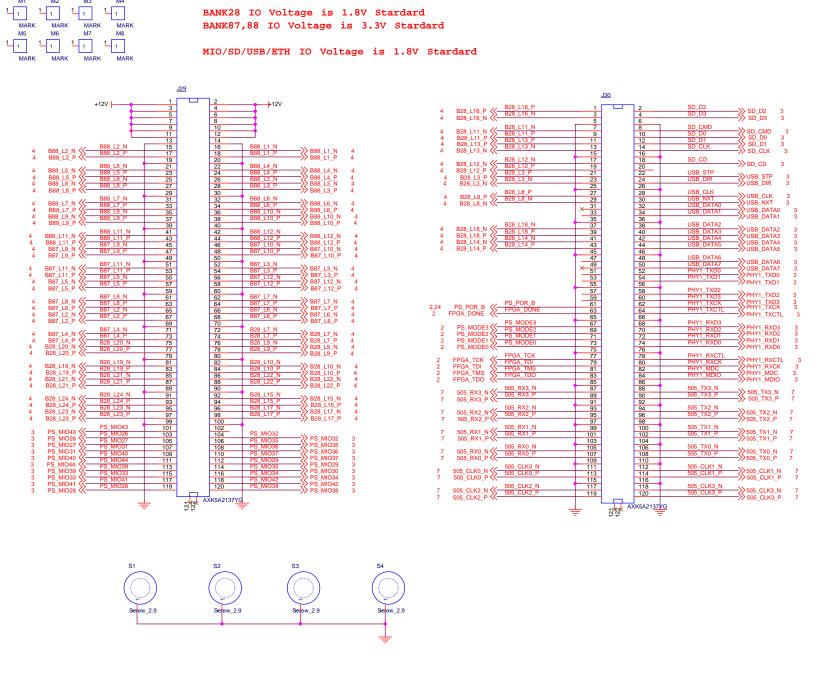






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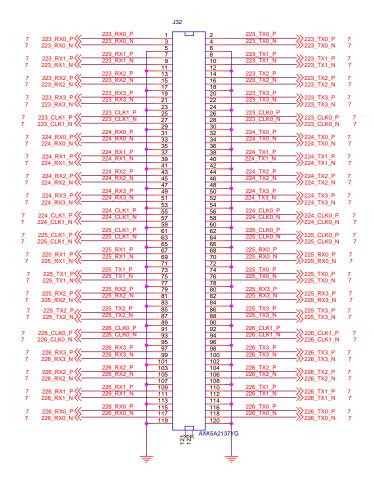
PS\_TIC1: IIC ADDRESS IS 0x1B

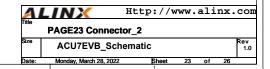


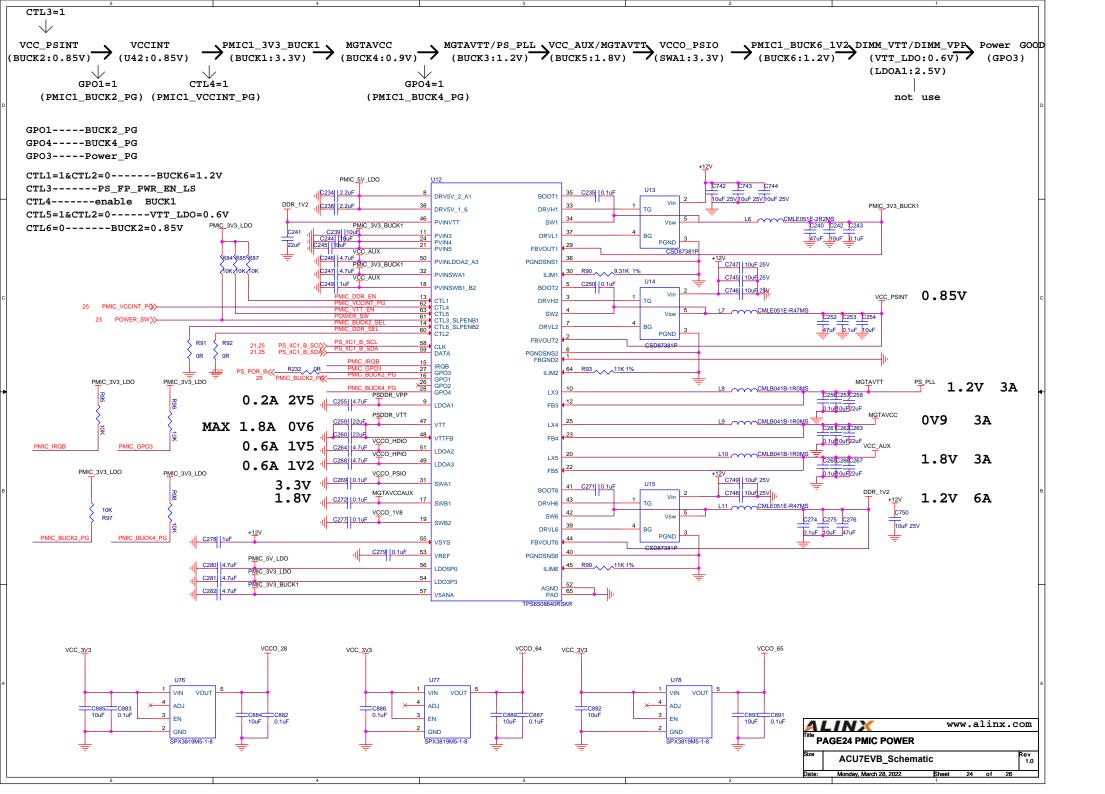
**ALINX Confidential** 

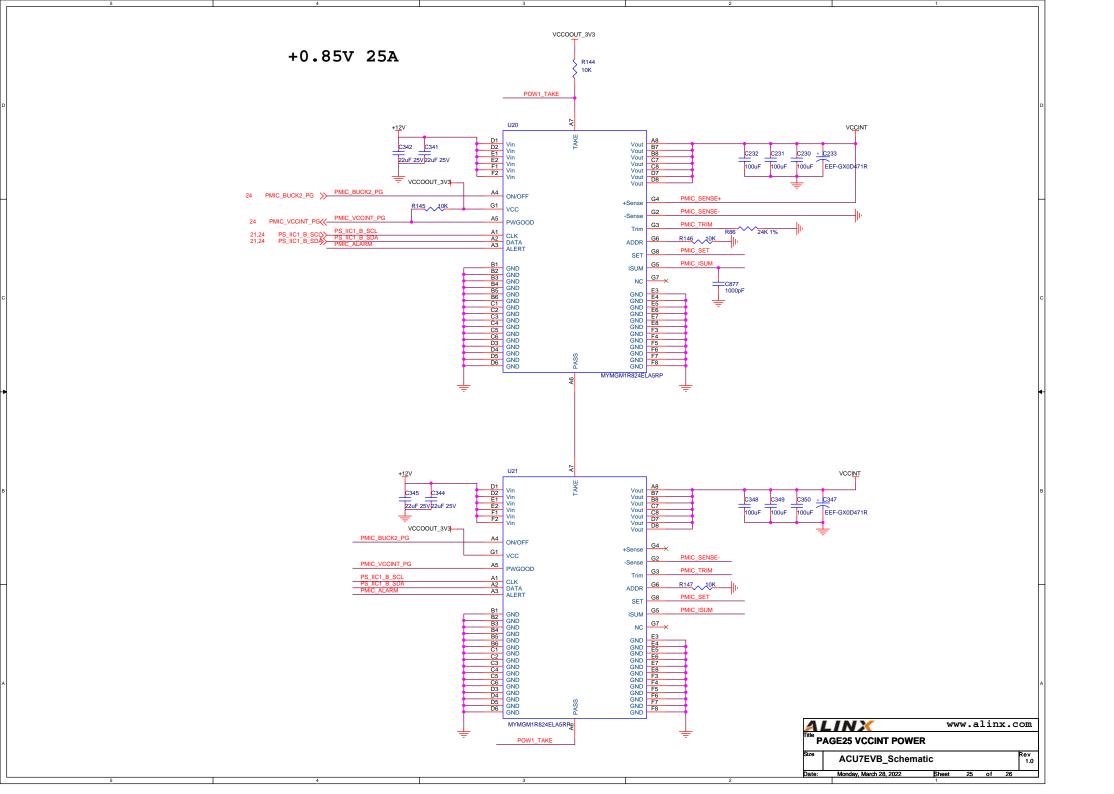
## BANK65,66 IO Voltage is 1.8V Stardard

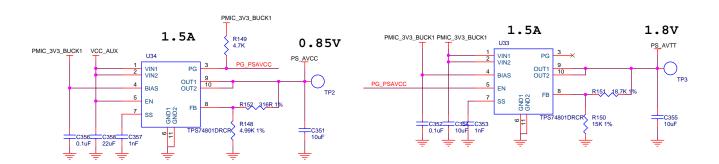
<u>J31</u>												
		,,	POWER_SW	1		2	VBAT_IN					
24 6	POWER_SW <	>>-	B65_L24_N	3		4	B65_L2_N VBAT_IN	10 6				
6	B65_L24_N \	≫	B65_L24_P	5		6	B65_L2_P B65_L2_N B65_L18_N B65_L2_P	6				
6	B65_L13_N <	??_	B65_L13_N	7		8		6				
6	B65 L13 P <	⟨⟨−	B65_L13_P	9 11		10 12	B65_L18_P S65_L18_N	6				
			B65 L8 N	11		14	B65 L16 N >> ===					
6	B65_L8_N <	<<-	B65_L8_P	15		16	B65   16 P >> B65_L16_N	6				
_6	B65_L8_P <	SS-	B65 L12 N	17		18	B65 I 3 N >> B65_L16_P	6				
6 6	B65_L12_N <	SS-	B65_L12_P	19		20	B65_L3_P	6				
0	B65_L12_P <	"		21		22	>>> B65_L3_P	6				
6	B65_L5_N <	11_	B65_L5_N	23		24	B65_L7_N >>> B65_L7_N	6				
6	B65 L5 P	??_	B65_L5_P B65_L10_N	25		26	B65_L7_P	6				
6	B65_E10_N <	//—	B65_L10_N	27		28	B65_L21_N B65_L21_N	<b>6</b>				
6	B65_L10_P <	<<−	B03_L10_F	29 31		30 32	B65_L21_P	6				
			B65_L14_N	33		34	B65 L6 N 💢					
6	B65_L14_N <	<<	B65_L14_P	35		36	B65_L6_P >> B65_L6_N	6				
6	B65_L14_P <	SS-	B65_L19_N	37		38	R65   17 N /> B65 L6 P	6				
6	B65_L19_N <	555	B65_L19_P	39		40	B65_L17_P >> B65_L17_N	6				
6	B65_L19_P <	"		41		42	>>> B65_L17_P	6				
6	B65 L15 N <	11_	B65_L15_N	43		44	B65_L4_N >>> B65_L4_N	6				
6	B65_L15_P <	?>_	B65_L15_P	45		46	B00_L4_F	6				
6	B65 L20 N <	??—	B65_L20_N B65_L20_P	47		48	B65_L11_N B65_L4_P B65_L11_N	6				
6	B65_L20_P <	<<−	D05_L20_P	49 51		50 52	B65_L11_P S65_L11_P	6				
			B65_L23_N	53		54	B65 L1 N 🔍					
6	B65_L23_N <	<<	B65_L23_P	55		56	B65   1 P >> B65_L1_N	6				
6	B65_L23_P <	SS-	B65 L22 N	57		58	B65 L9 N // B65_L1_P	6				
6	B65_L22_N <	SS-	B65_L22_P	59		60	B65_L9_P >> B65_L9_N	6				
6	B65_L22_P <	~		61		62	>>> B65_L9_P	6				
6	B64_L1_P <	11_	B64_L1_P	63		64	B64_L9_P B64_L9_N →>> B64_L9_P	6				
6	B64_L1_N <	?>_	B64_L1_N	65		66	D04_E3_14 ( D04_10_1)	6				
6	B64_L6_P <	??—	B64_L6_P B64_L6_N	67		68	B64_L14_P 864_L9_N B64_L14_P	6				
6	B64_L6_N <	<<−	D04_L0_IN	69 71		70 72	B64_L14_N >> B64_L14_N	6				
			B64 L5 P	73		74	B64_L11_P \\					
6	B64_L5_P <	<u>~</u>	B64_L5_N	75		76	B64 I 11 N >> B64_L11_P	6				
6	B64_L5_N <	SS-	B64_L3_P	77		78	B64 I 4 P 22 B64_L11_N	6				
6 6	B64_L3_P <	>>-	B64_L3_N	79		80	B64_L4_N >> B64_L4_P	6				
0	D04_L3_N V	"		81		82	>>> B64_L4_N	6				
6	B64 L24 P <	//-	B64_L24_P	83		84	B64_L2_P >>> B64_L2_P	6				
6	B64 L24 N <	??—	B64_L24_N B64_L21_P	85		86	B64_L2_N	6				
6	B64_L21_P <	<<-	B64 L21 N	87 89		90	B64 L8 N >> B64_L8_P	6				
6	B64_L21_N <	<<−	BO1_EE1_IV	91		92	>> B64_L8_N	6				
			B64_L7_P	93		94	B64 L12 P					
6	B64_L7_P <	SS-	B64_L7_N	95		96	B64_L12_N >> B64_L12_P	6				
6 6	B64_L7_N < B64_L10_P <	>>-	B64_L10_P	97		98	B64_L16_P	6 6				
6	B64_L10_N <	≫	B64_L10_N	99		100	B64_L16_N S64_L16_N	6				
0	D04_E10_N N	"	DO4 100 E	101		102	,,	0				
6	B64_L20_P <	//-	B64_L20_P	103		104	B64_L15_P B64_L15_N >>> B64_L15_P	6				
6	B64 L20 N <	⟨⟨–	B64_L20_N B64_L18_P	105 107		106 108	B64_L15_N B64_L15_N B64_L15_N	6				
6	B64_L18_P <	<<−	B64 L18 N	107		110	B64 L17 N >> B64_L17_P	6				
6	B64_L18_N <	<b>~</b>		111		112	B64_L17_N	6				
	D04 100 5	,,	B64_L22_P	113		114	B64_L19_P \\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \					
6	B64_L22_P <		B64_L22_N	115		116	B64_L19_N \\ B64_L19_P	6				
6 6	B64_L22_N < B64_L13_P <	»	B64_L13_P	117		118	B64_L23_P B64_L19_N B64_L23_N B64_L23_P	6 6				
6	B64_L13_N <	<u>}&gt;</u>	B64_L13_N	119		120	B64_L23_N S64_L23_P B64_L23_N	6				
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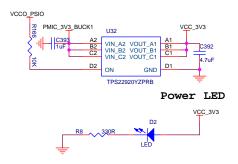


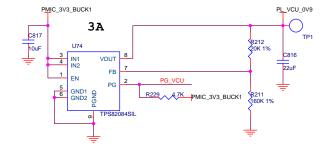












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