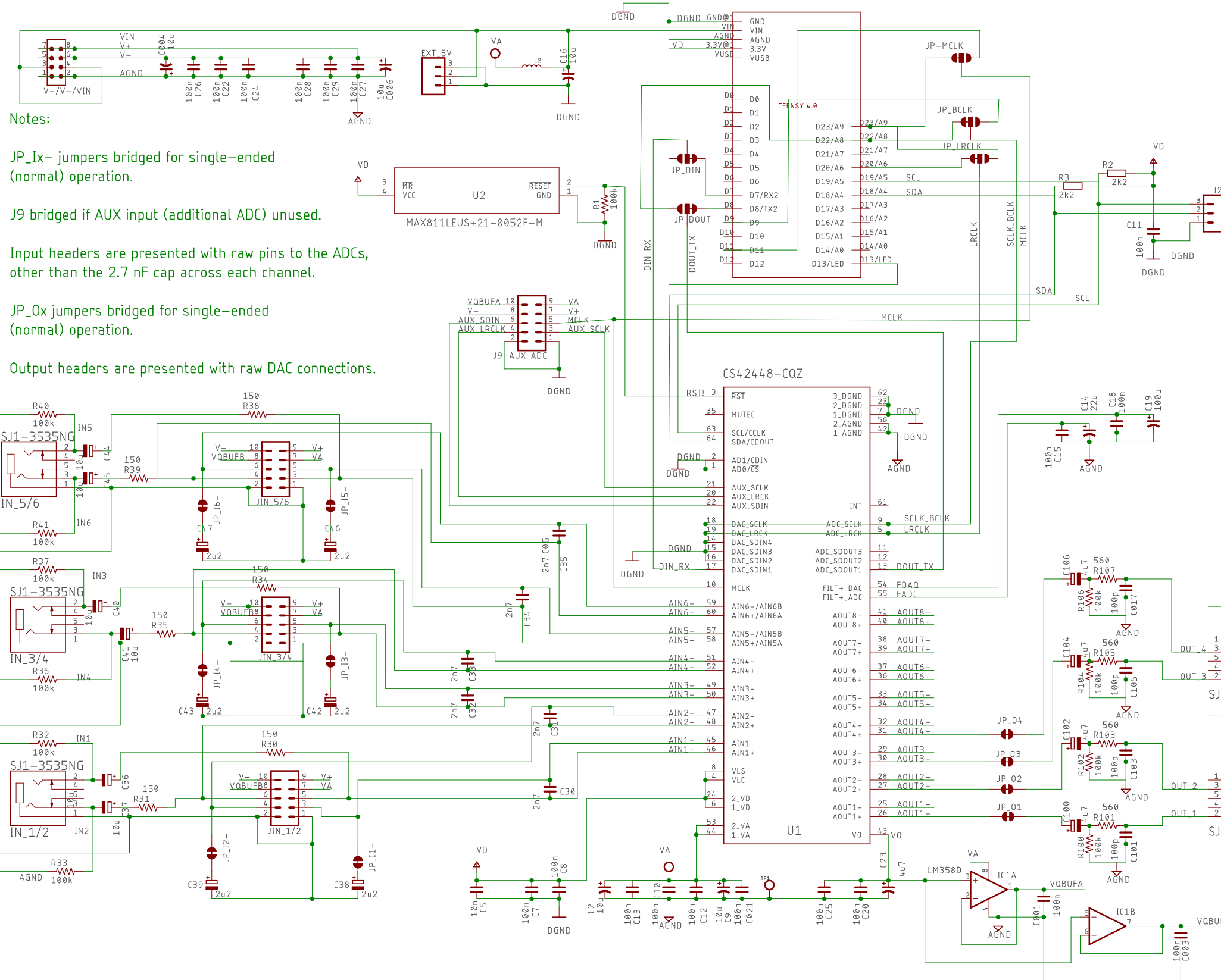


Do not connect ext 5V power and USB at the same time.



Notes:

JP_Ix- jumpers bridged for single-ended (normal) operation.

J9 bridged if AUX input (additional ADC) unused.

Input headers are presented with raw pins to the ADCs, other than the 2.7 nF cap across each channel.

JP_Ox jumpers bridged for single-ended (normal) operation.

Output headers are presented with raw DAC connections.

