Notes:

JP_Ix- (NC) jumpers bridged for single-ended (normal) operation.

JP_AUX_IN (NC) cut if AUX input (additional ADC) used.

Input headers are presented with raw pins to the ADCs, other than the 2.7 nF cap across each channel.

JP_Ox (NC) jumpers bridged for single-ended (normal) operation.

Output headers are presented with raw DAC connections.

Jumpers under Teensy (JP_DIN, DOUT, LRCLK, MCLK, BCLK) default to Teensy $4.0\,$

For Teensy 3.x operation, swap all jumpers over.

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