Subject: CEA201\_Test01aaaaaaaaa

Number of question: 1

**Multiple choice question:**

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| QN=1 | The Pentium 4 \_\_\_\_\_\_\_\_\_ component executes micro-operations, fetching the required data from the L1 data cache and temporarily storing results in registers. |
| A. | Fetch/decode unit |
| B. | Out-of-order execution logic |
| C. | Execution unit |
| D. | Memory subsystem |
| ANSWER: |  |

|  |  |
| --- | --- |
| QN=2 | The Pentium 4 \_\_\_\_\_\_\_\_\_ component executes micro-operations, fetching the required data from the L1 data cache and temporarily storing results in registers. |
| A. | Fetch/decode unit |
| B. | Out-of-order execution logic |
| C. | Execution unit |
| D. | Memory subsystem |
| ANSWER: |  |