CPEN 311: Digital Systems Design

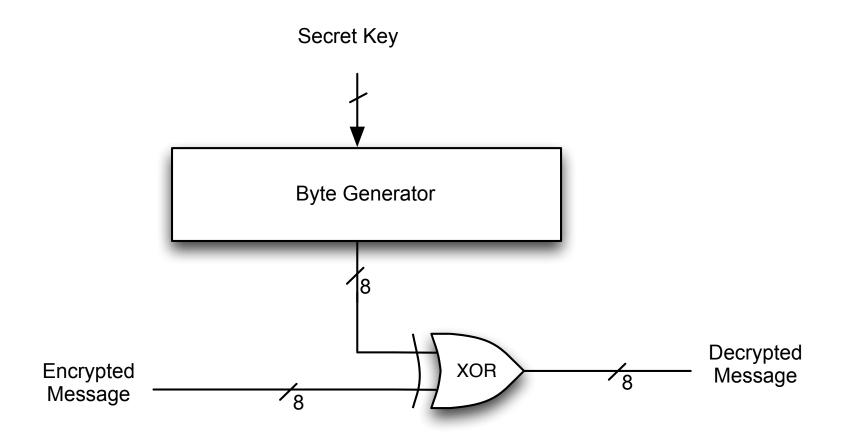
Introduction to Lab 6

2015/2016 Term 1

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Stream Cipher



```
// Input:
         secret key []: array of bytes that represent the secret key. In our implementation,
//
//
                        we will assume a key of 24 bits, meaning this array is 3 bytes long
         encrypted input []: array of bytes that represent the encrypted message. In our
//
                        implementation, we will assume the input message is 32 bytes
//
// Output:
         decrypted output []: array of bytes that represent the decrypted result. This will
//
                        always be the same length as encrypted input [].
// initialize s array. You will build this in Task 1
for i = 0 to 255 {
         s[i] = i;
// shuffle the array based on the secret key. You will build this in Task 2
i = 0
for i = 0 to 255 {
        j = (j + s[i] + secret key[i mod keylength]) mod 256 //keylength is 3 in our impl.
         swap values of s[i] and s[j]
// compute one byte per character in the encrypted message. You will build this in Task 2
i = 0, j = 0
for k = 0 to message length-1 { // message length is 32 in our implementation
         i = (i+1) \mod 256
         i = (i \pm s[i]) \mod 256
         swap values of s[i] and s[j]
         f = s[(s[i]+s[j]) \mod 256]
         decrypted_output[k] = f xor encrypted_input[k] // 8 bit wide XOR function
}
                                                                               Introduction to Lab 6,
                                                                                              Page 3
```

Task 1

Implement first loop of algorithm.

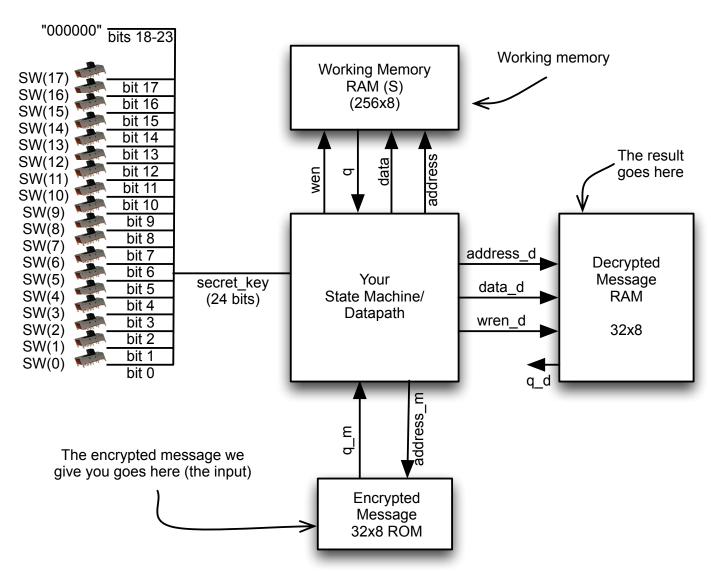
This will give you practice:

- Instantiating memories using MegaWizard
- Writing into memories
- Using the In-System Memory
 Content Editor

```
for i = 0 to 255 {
    s[i] = i;
}
```

```
Instance 0: S
000000
000014
000028
00003c
000050
000064
000078
00008c
0000a0
0000b4
0000c8
0000dc
                                                                               Introduction to Lab 6,
0000f0
                                                                                            Page 4
```

Task 2



Task 3

RC-4 Cracking:

Cycle through all keys. For each key, if the message is something readable, you have cracked the message!

To make this feasible, you will assume 24 bit keys

- My implementation takes about 10 minutes to cycle through
- In the lab, bits 24 and 23 are both 0 -> 2.5 minutes

Scaling to a real implementation: 40 bits

$$(10 \text{ min}) * 2^{(40-24)} = 455 \text{ days}$$

We can probably fit about 8 of these on a chip -> 56 days If the 20 groups in a lab got together -> 2.8 days

Challenge Task

Implement multiple "cores" on the chip

- When one finds the solution, all should stop