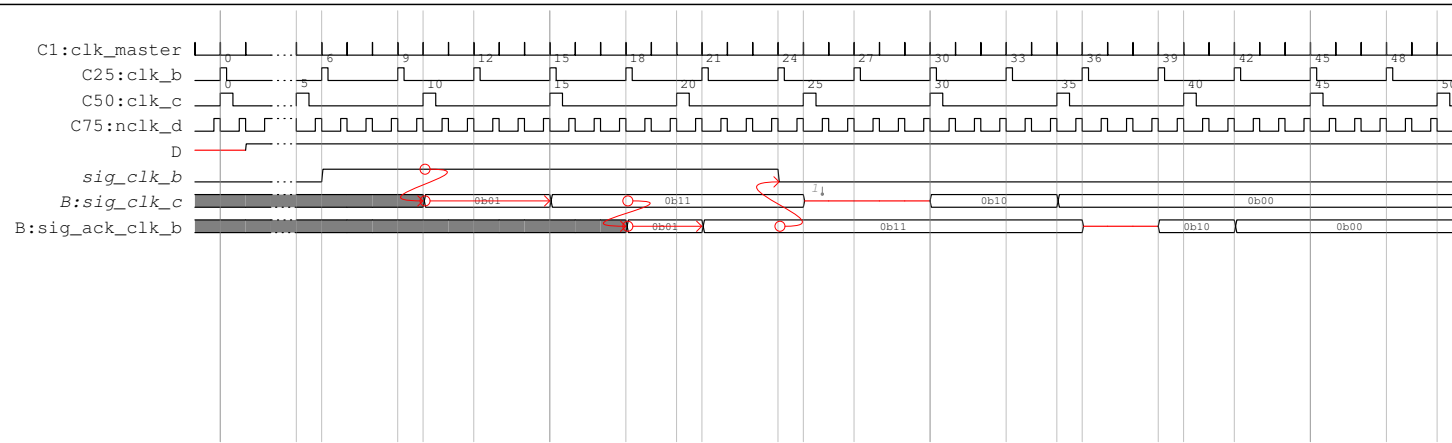


A simple clock domain crossover with REQ/ACK, and double synchronisation either way



NOTE:

- (1) Possible metastability on landing flop in clk_c
Possible Metastability on landing flop in clk_b