$n_d = 32$

$n_a = 20$

MEM FISICHE

2 chip RAM 32k
4 chip EPROM 32k
2 chip RAM 32k
4 chip RAM 64k

→ 256k = 3FFFF

MEM LOGICHE

RAM 0 A0000h
EPROM D0000h
RAM 1 40000h

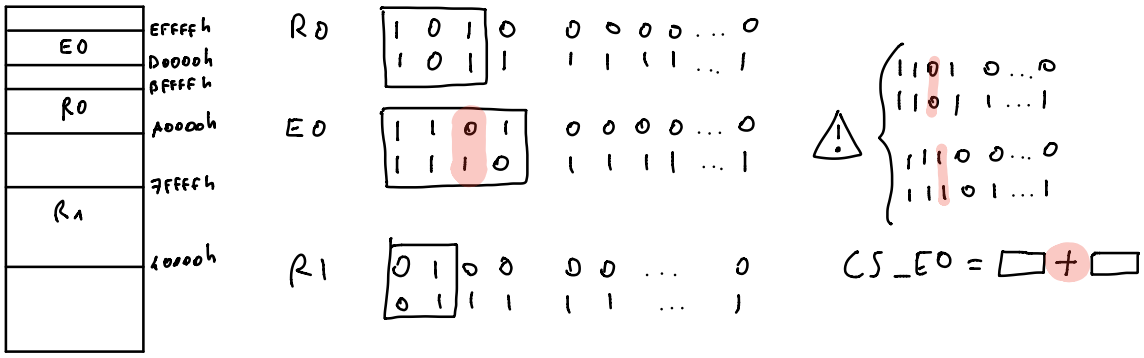
A0000h + 3FFFF = DFFFF ✗

SI SOVRAPP. A EPROM

A0000h + 1FFFF = BFFFF ✓

→ corrisp. chip → RAM logica

RAM 0 → 128k
RAM 1 → 256k
EPROM → 128k

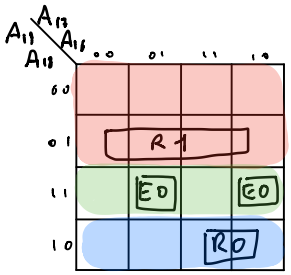


$$CS\_E0 = (A19 \cdot A18 \cdot \overline{A17} \cdot A16) + (A19 \cdot A18 \cdot A17 \cdot \overline{A16})$$

$$= A19 \cdot A18 \cdot (\overline{A17} \cdot A16 + A17 \cdot \overline{A16})$$

$$CS\_R0 = A19 \cdot \overline{A18} \cdot A17$$

$$CS\_R1 = \overline{A19} \cdot A1$$



$$CS\_R1' = \overline{A19}$$

$$CS\_E0' = A19 \cdot A18$$

$$CS\_R0' = A19 \cdot \overline{A18}$$