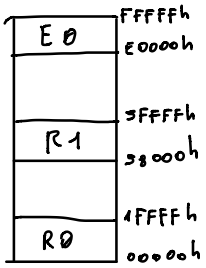
8086 — 2 chip EPROM — 64 kB — ALTI → E0 — $2^{20} = 1M$

$n_d = 16$ — 8 chip RAM — 64 kB — BASSI → R0 — $2^{19} = 512k$

$n_a = 20$ — 2 chip RAM — 16 kB — 58000h → R1 — $2^{18} = 256k$



E0 $\boxed{111}\,00\ldots0$ / $\boxed{111}\,11\ldots1$

R0 $\boxed{000}\,00\ldots0$ / $\boxed{000}\,11\ldots1$

R1 $\boxed{0\,101}\,00\ldots0$ / $\boxed{0\,101}\,11\ldots1$

$CS\_E0 = A19 \cdot A18 \cdot A17$

$CS\_R0 = \overline{A19} \cdot \overline{A18} \cdot \overline{A17}$

$CS\_R1 = \overline{A19} \cdot A19 \cdot \overline{A17} \cdot A16 \cdot A15$

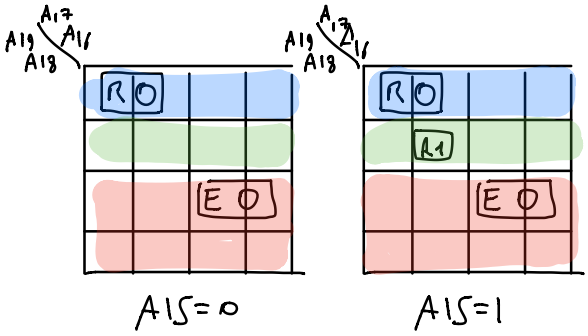
$CS\_E0\_0 = CS\_E0 \cdot M/\overline{IO\#} \cdot \overline{RD\#} \cdot WR\# \cdot \overline{A0}$

$CS\_E0\_1 = CS\_E0 \cdot M/\overline{IO\#} \cdot \overline{RD\#} \cdot WR\# \cdot \overline{BHE\#}$

$CS\_R0\_0 = CS\_R0 \cdot M/\overline{IO\#} \cdot \overline{(RD\# \cdot WR\#)} \cdot \overline{A0}$

Mappa k si può fare tenendo in considerazione tutti i letterali presenti oppure escludendo quelli che non servono

Numero minimo segnali di indirizzo per poter distinguere le varie memorie



$CS\_E0' = A19$

$CS\_R0' = \overline{A19} \cdot \overline{A18}$

$CS\_R1' = \overline{A19} \cdot A18$