# Publish\_Subscribe\_Model\_Test Design Description Francesco Grella

# Publish\_Subscribe\_Model\_Test: Design Description Francesco Grella

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# **Model Version**

Version: 1.16

**Last modified:** Tue Mar 06 20:46:36 2018

Checksum: 2036333480 3933327456 710489154 3096384493

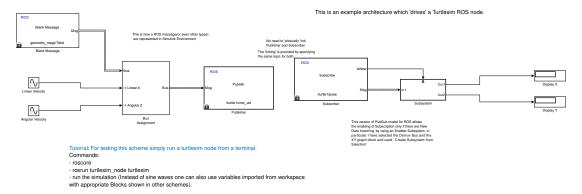
# **Root System**

# **Indice**

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#### Figura 1. Publish\_Subscribe\_Model\_Test

Publish/Subscribe Model for a Mobile Robot with Enabler: Reading Track from file



# **Blocks**

### **Parameters**

# "Angular Velocity" (Sin)

#### Tabella 1. "Angular Velocity" Parameters

Parameter	Value
Sine type	Time based
Time (t)	Use simulation time
Amplitude	1
Bias	0
Frequency (rad/sec)	1
Phase (rad)	0
Samples per period	10
Number of offset samples	0
Sample time	0

Parameter	Value
Interpret vector parameters as 1-D	on

### "Blank Message" (SubSystem)

#### Tabella 2. "Blank Message" Parameters

Parameter	Value
roboticsrobotslrosbl- ockmaskMessageTyp- ePrompt	O   O   P   O   P   P   P   P   P   P
Simulinkblkprm_pro- mptsAllSrcBlksSamp- leTime	

### "Bus Assignment" (BusAssignment)

#### Tabella 3. "Bus Assignment" Parameters

Parameter	Value
Signals that are being assigned	Linear.X,Angular.Z
	{ Linear , X Y Z } { Angular , X Y Z }

### "Display X" (Display)

#### Tabella 4. "Display X" Parameters

Parameter	Value
Format	short
Decimation	1
Floating display	off

### "Display Y" (Display)

# Tabella 5. "Display Y" Parameters

Parameter	Value
Format	short

Parameter	Value
Decimation	1
Floating display	off

# "Linear Velocity" (Sin)

### Tabella 6. "Linear Velocity" Parameters

Parameter	Value
Sine type	Time based
Time (t)	Use simulation time
Amplitude	1
Bias	0
Frequency (rad/sec)	1
Phase (rad)	0
Samples per period	10
Number of offset samples	0
Sample time	0
Interpret vector par- ameters as 1-D	on

# "Publisher" (SubSystem)

#### Tabella 7. "Publisher" Parameters

Parameter	Value
roboticsrobotslrosbl- ockmaskTopicSourc- ePrompt	Select from ROS network
roboticsrobotslrosbl- ockmaskTopicProm- pt	/turtle1/cmd_vel
roboticsrobotslrosbl- ockmaskMessageTyp- ePrompt	geometry_msgs/Twist
roboticsrobotslrosbl- ockmaskPublisherM- sgQueuePrompt	1

# "Subscriber" (SubSystem)

#### Tabella 8. "Subscriber" Parameters

Parameter	Value
roboticsrobotslrosbl- ockmaskTopicSourc- ePrompt	Select from ROS network
roboticsrobotslrosbl- ockmaskTopicProm- pt	/turtle1/pose
roboticsrobotslrosbl- ockmaskMessageTyp- ePrompt	turtlesim/Pose
roboticsrobotslrosbl- ockmaskSampleTim- ePrompt	-1
roboticsrobotslrosbl- ockmaskSubscriber- MsgQueuePrompt	1

# **Block Execution Order**

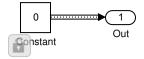
# Subsystems

# **Indice**

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# **Blank Message**

Figura 2. Publish\_Subscribe\_Model\_Test/Blank Message



### **Blocks**

#### **Parameters**

"Constant" (Constant)

Tabella 9. "Constant" Parameters

Parameter	Value
Constant value	0
Interpret vector parameters as 1-D	on
Output minimum	
Output maximum	
Output data type	Bus: SL_Bus_Publish_Subscribe_Model_Test_geometry_msgs_Twist
Lock output data ty- pe setting against	on

Parameter	Value
changes by the fixe- d-point tools	
Sample time	SampleTime
Frame period	inf

### "Out" (Outport)

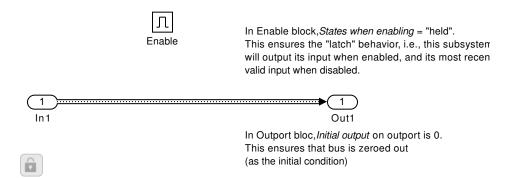
#### Tabella 10. "Out" Parameters

Parameter	Value
Port number	1
Icon display	Port number
Minimum	
Maximum	
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	on
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	
MustResolveToSigna- lObject	off

# **Enabled Subsystem**

**Checksum:** 3156389260 2757582424 2106409614 2552456957

# Figura 3. Publish\_Subscribe\_Model\_Test/Subscriber/Enabled Subsystem



#### **Interface**

#### **Input Signals**

The following tables describe external signals used to compute the subsystem's inputs. The name of the input signal is the name of the input port that accepts the signal. The number in angle brackets is the number of the input port. A dimension of [1 1] indicates a scalar signal.

#### Tabella 11.

Description:
Data Type:
Width: 0
Dimensions:

#### **Output Signals**

The following tables describe the signals output by this system. The name of the output signal is the name of the signal's parent block, i.e., the block that computes the signal. The number in angle brackets is the number of the port that emits the signal.

#### Tabella 12.

Description:
Data Type:
Width: 0
Dimensions:

#### **Blocks**

#### **Parameters**

### "Enable" (EnablePort)

#### Tabella 13. "Enable" Parameters

Parameter	Value
States when enabling	held
Propagate sizes of variable-size signals	Only when enabling
Show output port	off
Enable zero-crossing detection	on
Port dimensions	1
Sample time	-1
Minimum	
Maximum	
Data type	double
Interpolate data	on

#### "In1" (Inport)

#### Tabella 14. "In1" Parameters

Parameter	Value
Port number	1
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	-1
Minimum	
Maximum	
Data type	Inherit: auto

#### "Out1" (Outport)

#### Tabella 15. "Out1" Parameters

Parameter	Value
Port number	1
Icon display	Port number
Minimum	
Maximum	[]

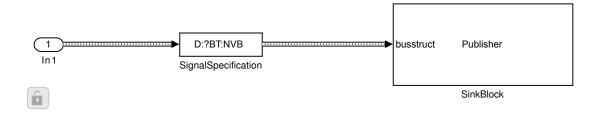
Parameter	Value
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	0
MustResolveToSigna- lObject	off

# **Block Execution Order**

# **Publisher**

**Checksum:** 2398764243 2084222166 661248326 1971036020

Figura 4. Publish\_Subscribe\_Model\_Test/Publisher



# **Blocks**

#### **Parameters**

### "In1" (Inport)

#### Tabella 16. "In1" Parameters

Parameter	Value
Port number	1
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	-1
Minimum	П
Maximum	
Data type	Inherit: auto

#### "SignalSpecification" (SignalSpecification)

### Tabella 17. "SignalSpecification" Parameters

Parameter	Value
Minimum	
Maximum	
Data type	Bus: SL_Bus_Publish_Subscribe_Model_Test_geometry_msgs_Twist
Lock output data ty- pe setting against changes by the fixe- d-point tools	on
Require nonvirtual bus	on
Unit (e.g., m, m/s^2, N*m)	inherit
Dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1

#### "SinkBlock" (MATLABSystem)

#### Tabella 18. "SinkBlock" Parameters

Parameter	Value
Topic to publish to	/turtle1/cmd_vel
Message type of topic	geometry_msgs/Twist

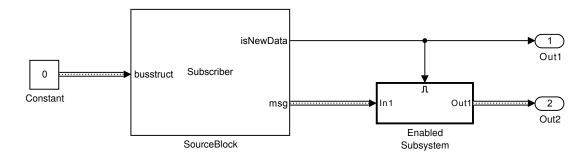
Parameter	Value
Length of publish queue	messageQueueLength
Simulink Bus Name for message type	SL_Bus_Publish_Subscribe_Model_Test_geometry_msgs_Twist
Name of Simulink model	Publish_Subscribe_Model_Test
Simulink Block Identifier	Pub_Publish_Subscribe_Model_Test_5
Simulate using	Interpreted execution

#### **Block Execution Order**

# **Subscriber**

Checksum: 1625469279 1920057775 1714994085 2357093347

Figura 5. Publish\_Subscribe\_Model\_Test/Subscriber





If IsNewData is false, the Msg output of the MATLAB System block will be a zeroed-out bus. So use an Enabled subsystem to hold the most-recent valid output.

### **Blocks**

#### **Parameters**

#### "Constant" (Constant)

Tabella 19. "Constant" Parameters

Parameter	Value
Constant value	0
Interpret vector parameters as 1-D	off
Output minimum	

Parameter	Value
Output maximum	
Output data type	Bus: SL_Bus_Publish_Subscribe_Model_Test_turtlesim_Pose
Lock output data ty- pe setting against changes by the fixe- d-point tools	on
Sample time	sampleTime
Frame period	inf

# "Out1" (Outport)

#### Tabella 20. "Out1" Parameters

Parameter	Value
Port number	1
Icon display	Port number
Minimum	
Maximum	
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	
MustResolveToSigna- lObject	off

### "Out2" (Outport)

#### Tabella 21. "Out2" Parameters

Parameter	Value
Port number	2
Icon display	Port number
Minimum	
Maximum	
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	on
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	
MustResolveToSigna- lObject	off

#### "SourceBlock" (MATLABSystem)

#### Tabella 22. "SourceBlock" Parameters

Parameter	Value
Topic to subscribe to	/turtle1/pose
Message type of topic	turtlesim/Pose
Length of subscribe callback queue	messageQueueLength
Simulink Bus Name for message type	SL_Bus_Publish_Subscribe_Model_Test_turtlesim_Pose

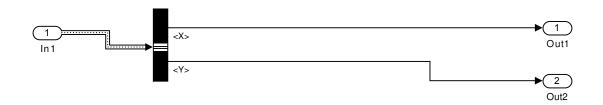
Parameter	Value
Name of Simulink model	Publish_Subscribe_Model_Test
Simulink Block Identifier	Sub_Publish_Subscribe_Model_Test_9
Simulate using	Interpreted execution

### **Block Execution Order**

# Subsystem

Figura 6. Publish\_Subscribe\_Model\_Test/Subsystem





#### **Blocks**

#### **Parameters**

"Bus Selector" (BusSelector)

Tabella 23. "Bus Selector" Parameters

Parameter	Value
Output signals	X,Y
Output as bus	off
	X
	Y
	Theta
	LinearVelocity
	AngularVelocity

#### Output Hierarchy:

1. Bus Selector

1. <X>

2. <Y>

#### "Enable" (EnablePort)

#### Tabella 24. "Enable" Parameters

Parameter	Value
States when enabling	held
Propagate sizes of variable-size signals	Only when enabling
Show output port	off
Enable zero-crossing detection	on
Port dimensions	1
Sample time	-1
Minimum	
Maximum	
Data type	double
Interpolate data	on

#### "In1" (Inport)

#### Tabella 25. "In1" Parameters

Parameter	Value
Port number	1
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	-1
Minimum	
Maximum	
Data type	Inherit: auto

### "Out1" (Outport)

#### Tabella 26. "Out1" Parameters

Parameter	Value
Port number	1
Icon display	Port number

Parameter	Value
Minimum	
Maximum	
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	
MustResolveToSigna- lObject	off

# "Out2" (Outport)

#### Tabella 27. "Out2" Parameters

Parameter	Value
Port number	2
Icon display	Port number
Minimum	
Maximum	
Data type	Inherit: auto
Lock output data ty- pe setting against changes by the fixe- d-point tools	off
Output as nonvirtual bus in parent model	off

#### Subsystems

Parameter	Value
Unit (e.g., m, m/s^2, N*m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Ensure outport is virtual	off
Source of initial output value	Dialog
Output when disabled	held
Initial output	
MustResolveToSigna- lObject	off

# **System Design Variables**

# **Indice**

# **Design Variable Summary**

#### Tabella 28. Design Variables

Variable Name	Parent Blocks	Size	Bytes	Class	Value
	Blank Message [3] Publisher [4]	1x1	532	Simulink- .Bus	< Simulink.Bus>
	Blank Message [3] Publisher [4]	1x1	224	Simulink- .Bus	< Simulink.Bus>
SL_Bus_P- ublish_S- ubscribe- _Model Test_turt- lesim_Po- se	Subscriber [4]	1x1	372	Simulink- .Bus	< Simulink.Bus>

# **Requirements Traceability**

 $Publish\_Subscribe\_Model\_Test\ does\ not\ contain\ requirements\ traceability\ links.$ 

# **System Model Configuration**

Source: Model

Source Name: Publish\_Subscribe\_Model\_Test

Tabella 29. Publish\_Subscribe\_Model\_Test Configuration Set

Property	Value
Description	
Components	[Publish_Subscribe_Model_Test Configuration Set.Components(1) [21], Publish_Subscribe_Model_Test Configuration Set.Components(2) [22], Publish_Subscribe Model_Test Configuration Set.Components- (3) [23], Publish_Subscribe_Model_Test Configuration Set.Components(4) [24], Publish_Subscribe_Model_Test Configuration Set.Components(5) [27], Publish_Subscribe_Model_Test Configuration Set.Components(6) [29], Publish_Subscribe Model_Test Configuration Set.Components- (7) [29], Publish_Subscribe_Model_Test Configuration Set.Components(8) [30], Publish_Subscribe_Model_Test Configuration Set.Components(9) [32]]
Name	Configuration
SimulationMode	normal
ConfigType	Model

Tabella 30. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](1)

Property	Value
Name	Solver
Description	
Components	
StartTime	0.0
StopTime	inf
AbsTol	auto
FixedStep	auto
InitialStep	auto
MaxNumMinSteps	-1
MaxOrder	5
ZcThreshold	auto

ConsecutiveZCsStepRelTol	10*128*eps
MaxConsecutiveZCs	1000
ExtrapolationOrder	4
NumberNewtonIterations	1
MaxStep	auto
MinStep	auto
MaxConsecutiveMinStep	1
RelTol	1e-3
SolverMode	SingleTasking
EnableMultiTasking	off
EnableConcurrentExecution	off
ConcurrentTasks	off
Solver	VariableStepAuto
SolverName	VariableStepAuto
SolverType	Variable-step
SolverJacobianMethodControl	auto
ShapePreserveControl	DisableAll
ZeroCrossControl	UseLocalSettings
ZeroCrossAlgorithm	Nonadaptive
SolverResetMethod	Fast
PositivePriorityOrder	off
AutoInsertRateTranBlk	off
SampleTimeConstraint	Unconstrained
InsertRTBMode	Whenever possible
SampleTimeProperty	

# Tabella 31. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](2)

Property	Value
Name	Data Import/Export
Description	
Components	
Decimation	1
ExternalInput	[t, u]
FinalStateName	xFinal
InitialState	xInitial
LimitDataPoints	off
MaxDataPoints	1000
LoadExternalInput	off

LoadInitialState	off
SaveFinalState	off
SaveCompleteFinalSimState	off
SaveFormat	Dataset
SaveOutput	on
SaveState	off
SignalLogging	on
DSMLogging	on
InspectSignalLogs	off
SaveTime	on
ReturnWorkspaceOutputs	off
StateSaveName	xout
TimeSaveName	tout
OutputSaveName	yout
SignalLoggingName	logsout
DSMLoggingName	dsmout
OutputOption	RefineOutputTimes
OutputTimes	П
ReturnWorkspaceOutputsName	out
Refine	1
LoggingToFile	off
DatasetSignalFormat	timeseries
LoggingFileName	out.mat
LoggingIntervals	[-inf, inf]

# Tabella 32. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](3)

Property	Value
Name	Optimization
Description	
Components	
BlockReduction	on
BooleanDataType	on
ConditionallyExecuteInputs	on
DefaultParameterBehavior	Tunable
InlineParams	off
UseDivisionForNetSlopeComputation	off
UseFloatMulNetSlope	off
DefaultUnderspecifiedDataType	double

UseSpecifiedMinMax	off
InlineInvariantSignals	off
OptimizeBlockIOStorage	on
BufferReuse	on
GlobalBufferReuse	on
GlobalVariableUsage	None
StrengthReduction	off
AdvancedOptControl	
EnforceIntegerDowncast	on
ExpressionFolding	on
BooleansAsBitfields	off
BitfieldContainerType	uint_T
EnableMemcpy	on
MemcpyThreshold	64
PassReuseOutputArgsAs	Structure reference
PassReuseOutputArgsThreshold	12
FoldNonRolledExpr	on
LocalBlockOutputs	on
RollThreshold	5
StateBitsets	off
DataBitsets	off
ActiveStateOutputEnumStorageType	Native Integer
UseTempVars	off
ZeroExternalMemoryAtStartup	on
ZeroInternalMemoryAtStartup	on
InitFltsAndDblsToZero	off
NoFixptDivByZeroProtection	off
EfficientFloat2IntCast	off
EfficientMapNaN2IntZero	on
LifeSpan	auto
EvaledLifeSpan	Inf
MaxStackSize	Inherit from target
BufferReusableBoundary	on
SimCompilerOptimization	off
AccelVerboseBuild	off
OptimizeBlockOrder	off
OptimizeDataStoreBuffers	on
BusAssignmentInplaceUpdate	on

# Tabella 33. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](4)

Property	Value
Name	Diagnostics
Description	
Components	
RTPrefix	error
ConsistencyChecking	none
ArrayBoundsChecking	none
SignalInfNanChecking	none
SignalRangeChecking	none
ReadBeforeWriteMsg	UseLocalSettings
WriteAfterWriteMsg	UseLocalSettings
WriteAfterReadMsg	UseLocalSettings
AlgebraicLoopMsg	warning
ArtificialAlgebraicLoopMsg	warning
SaveWithDisabledLinksMsg	warning
SaveWithParameterizedLinksMsg	warning
CheckSSInitialOutputMsg	on
UnderspecifiedInitializationDetection	Simplified
MergeDetectMultiDrivingBlocksExec	error
CheckExecutionContextRuntimeOutputM-sg	off
SignalResolutionControl	UseLocalSettings
BlockPriorityViolationMsg	warning
MinStepSizeMsg	warning
TimeAdjustmentMsg	none
MaxConsecutiveZCsMsg	error
MaskedZcDiagnostic	warning
IgnoredZcDiagnostic	warning
SolverPrmCheckMsg	none
InheritedTsInSrcMsg	warning
MultiTaskDSMMsg	error
MultiTaskCondExecSysMsg	error
MultiTaskRateTransMsg	error
SingleTaskRateTransMsg	none
TasksWithSamePriorityMsg	warning
SigSpecEnsureSampleTimeMsg	warning
CheckMatrixSingularityMsg	none

IntegerOverflowMsg	warning
Int32ToFloatConvMsg	warning
ParameterDowncastMsg	error
ParameterOverflowMsg	error
ParameterUnderflowMsg	none
ParameterPrecisionLossMsg	warning
ParameterTunabilityLossMsg	warning
FixptConstUnderflowMsg	none
FixptConstOverflowMsg	none
FixptConstPrecisionLossMsg	none
UnderSpecifiedDataTypeMsg	none
UnnecessaryDatatypeConvMsg	none
VectorMatrixConversionMsg	none
InvalidFcnCallConnMsg	error
FcnCallInpInsideContextMsg	error
SignalLabelMismatchMsg	none
UnconnectedInputMsg	warning
UnconnectedOutputMsg	warning
UnconnectedLineMsg	warning
UseOnlyExistingSharedCode	error
SFcnCompatibilityMsg	none
FrameProcessingCompatibilityMsg	error
UniqueDataStoreMsg	none
BusObjectLabelMismatch	warning
RootOutportRequireBusObject	warning
AssertControl	UseLocalSettings
Echo	
EnableOverflowDetection	off
AllowSymbolicDim	on
ModelReferenceIOMsg	none
Model Reference Version Mismatch Message	none
ModelReferenceIOMismatchMessage	none
ModelReferenceCSMismatchMessage	none
ModelReferenceSimTargetVerbose	off
UnknownTsInhSupMsg	warning
ModelReferenceDataLoggingMessage	warning
ModelReferenceSymbolNameMessage	warning
ModelReferenceExtraNoncontSigs	error
StateNameClashWarn	none

SimStateInterfaceChecksumMismatchMsg	warning
SimStateOlderReleaseMsg	error
InitInArrayFormatMsg	warning
StrictBusMsg	ErrorLevel1
BusNameAdapt	WarnAndRepair
NonBusSignalsTreatedAsBus	none
SFUnusedDataAndEventsDiag	warning
SFUnexpectedBacktrackingDiag	error
SFInvalidInputDataAccessInChartInitDiag	warning
SFNoUnconditionalDefaultTransitionDiag	error
SFTransitionOutsideNaturalParentDiag	warning
SFUnconditional Transition Shadowing Diag	warning
SFUnreachableExecutionPathDiag	warning
SFUndirectedBroadcastEventsDiag	warning
SFTransitionActionBeforeConditionDiag	warning
SFOutputUsedAsStateInMooreChartDiag	error
SFTemporalDelaySmallerThanSampleTimeDiag	warning
SFUnconditionalPathOutOfParentDiag	warning
SFSelfTransitionDiag	warning
SFExecutionAtInitializationDiag	warning
SFMachineParentedDataDiag	warning
SFUnreachableStateOrJunctionDiag	warning
SFDanglingTransitionDiag	warning
IntegerSaturationMsg	warning
AllowedUnitSystems	all
UnitsInconsistencyMsg	warning
AllowAutomaticUnitConversions	on

# Tabella 34. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](5)

Property	Value
Name	Hardware Implementation
Description	
Components	
ProdBitPerChar	8
ProdBitPerShort	16
ProdBitPerInt	32
ProdBitPerLong	32

rodBitPerFloat  rodBitPerDouble  fodBitPerPointer  rodBitPerSizeT  rodBitPerPtrDiffT  fodLargestAtomicInteger  rodLargestAtomicFloat  fodBitPerFloat  fodBitPerPtrDiffT  fodChargestAtomicFloat  fodBitPerPtrDiffT  fodBitPerPointer  fodBitPerPtrDiffT  fodBitPerPointer  fodBitPerPtrDiffT  fodBitPerPointer  fodBitPerPtrDiffT  fodBitPerPtrDi	
rodBitPerPointer 64 rodBitPerSizeT 64 rodBitPerPtrDiffT 64 rodLargestAtomicInteger Char	
rodBitPerSizeT 64 rodBitPerPtrDiffT 64 rodLargestAtomicInteger Char	
rodBitPerPtrDiffT 64 rodLargestAtomicInteger Char	
rodLargestAtomicInteger Char	
0	
rodI argaetAtomicFloat	
Float	
rodIntDivRoundTo Zero	
rodEndianess Little	Endian
rodWordSize 64	
rodShiftRightIntArith on	
rodLongLongMode off	
rodHWDeviceType Intel-	>x86-64 (Windows64)
argetBitPerChar 8	
argetBitPerShort 16	
argetBitPerInt 32	
argetBitPerLong 32	
argetBitPerLongLong 64	
argetBitPerFloat 32	
argetBitPerDouble 64	
argetBitPerPointer 32	
argetBitPerSizeT 32	
argetBitPerPtrDiffT 32	
argetLargestAtomicInteger Char	
argetLargestAtomicFloat None	
argetShiftRightIntArith on	
argetLongLongMode off	
argetIntDivRoundTo Unde	fined
argetEndianess Unsp	ecified
argetWordSize 32	
argetTypeEmulationWarnSuppressLevel 0	
argetPreprocMaxBitsSint 32	
argetPreprocMaxBitsUint 32	
argetHWDeviceType Speci	fied
argetUnknown off	
rodEqTarget on	
seEmbeddedCoderFeatures on	
seSimulinkCoderFeatures on	

Tabella 35. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](6)

Property	Value
Name	Model Referencing
Description	
Components	
UpdateModelReferenceTargets	IfOutOfDateOrStructuralChange
SkipRefExpFcnMdlSchedulingOrderCheck	off
EnableRefExpFcnMdlSchedulingChecks	on
CheckModelReferenceTargetMessage	error
EnableParallelModelReferenceBuilds	off
ParallelModelReferenceErrorOnInvalidPo-ol	on
ParallelModelReferenceMATLABWorkerInit	None
ModelReferenceNumInstancesAllowed	Multi
PropagateVarSize	Infer from blocks in model
ModelDependencies	
ModelReferencePassRootInputsByReference	on
ModelReferenceMinAlgLoopOccurrences	off
PropagateSignalLabelsOutOfModel	on
SupportModelReferenceSimTargetCustom-Code	off

Tabella 36. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](7)

Property	Value
Name	Simulation Target
Description	
Components	
SimCustomSourceCode	
SimCustomHeaderCode	
SimCustomInitializer	
SimCustomTerminator	
SimReservedNameArray	
SimUserSources	
SimUserIncludeDirs	
SimUserLibraries	

SimUserDefines	
SFSimEnableDebug	off
SFSimOverflowDetection	on
SFSimEcho	on
SimBlas	on
SimCtrlC	on
SimExtrinsic	on
SimIntegrity	on
SimUseLocalCustomCode	off
SimParseCustomCode	on
SimBuildMode	sf_incremental_build
SimDataInitializer	
SimGenImportedTypeDefs	off
CompileTimeRecursionLimit	50
EnableRuntimeRecursion	on
MATLABDynamicMemAlloc	on
MATLABDynamicMemAllocThreshold	65536
CustomSymbolStrEMXArray	nothing
CustomSymbolStrEMXArrayFcn	nothing

# Tabella 37. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](8)

Property	Value
Name	Code Generation
SystemTargetFile	grt.tlc
HardwareBoard	None
TLCOptions	
CodeGenDirectory	
GenCodeOnly	off
MakeCommand	make_rtw
GenerateMakefile	on
PackageGeneratedCodeAndArtifacts	off
PackageName	
TemplateMakefile	grt_default_tmf
PostCodeGenCommand	
Description	
GenerateReport	off
SaveLog	off
RTWVerbose	on

RetainRTWFile	off
ProfileTLC	off
TLCDebug	off
TLCCoverage	off
TLCAssert	off
ProcessScriptMode	Default
ConfigurationMode	Optimized
ProcessScript	
ConfigurationScript	
ConfigAtBuild	off
RTWUseLocalCustomCode	off
RTWUseSimCustomCode	off
CustomSourceCode	
CustomHeaderCode	
CustomInclude	
CustomSource	
CustomLibrary	
CustomDefine	
CustomLAPACKCallback	
CustomInitializer	
CustomTerminator	
Toolchain	Automatically locate an installed toolchain
BuildConfiguration	Faster Builds
CustomToolchainOptions	
IncludeHyperlinkInReport	off
LaunchReport	off
RecursionLimit	50
PortableWordSizes	off
GenerateErtSFunction	off
CreateSILPILBlock	None
CodeExecutionProfiling	off
CodeExecutionProfileVariable	executionProfile
CodeProfilingSaveOptions	SummaryOnly
CodeProfilingInstrumentation	off
CodeCoverageSettings	Publish_Subscribe_Model_Test Configuration Set.Components(8).CodeCoverageSettings [33]
SILDebugging	off
TargetLang	С

IncludeERTFirstTime	off
GenerateTraceInfo	off
GenerateTraceReport	off
GenerateTraceReportSl	off
GenerateTraceReportSf	off
GenerateTraceReportEml	off
GenerateCodeInfo	off
GenerateWebview	off
GenerateCodeMetricsReport	off
GenerateCodeReplacementReport	off
RTWCompilerOptimization	off
ObjectivePriorities	
RTWCustomCompilerOptimizations	
CheckMdlBeforeBuild	Off
CustomRebuildMode	OnUpdate
DataInitializer	
Components	[Publish_Subscribe_Model_Test Configuration Set.Components(8).Components(1) [-33], Publish_Subscribe_Model_Test Configuration Set.Components(8).Components(2) [35]]

# Tabella 38. Publish\_Subscribe\_Model\_Test Configuration Set.Components [21](9)

Property	Value
Description	Simulink Coverage Configuration Component
Components	
Name	Simulink Coverage
CovEnable	off
CovScope	EntireSystem
CovIncludeTopModel	on
RecordCoverage	off
CovPath	/
CovSaveName	covdata
CovCompData	
CovMetricSettings	dwe
CovFilter	
CovHTMLOptions	
CovNameIncrementing	off

CovHtmlReporting	off
CovForceBlockReductionOff	on
CovEnableCumulative	on
CovSaveCumulativeToWorkspaceVar	off
CovSaveSingleToWorkspaceVar	off
CovCumulativeVarName	covCumulativeData
CovCumulativeReport	off
CovSaveOutputData	on
CovOutputDir	slcov_output/\$ModelName\$
CovDataFileName	\$ModelName\$_cvdata
CovShowResultsExplorer	on
CovReportOnPause	on
CovModelRefEnable	off
CovModelRefExcluded	
CovExternalEMLEnable	on
CovSFcnEnable	on
CovBoundaryAbsTol	1.0000e-05
CovBoundaryRelTol	0.0100
CovUseTimeInterval	off
CovStartTime	0
CovStopTime	0
CovMetricStructuralLevel	Decision
CovMetricLookupTable	off
CovMetricSignalRange	off
CovMetricSignalSize	off
CovMetricObjectiveConstraint	off
CovMetricSaturateOnIntegerOverflow	off
CovMetricRelationalBoundary	off
CovLogicBlockShortCircuit	off
CovUnsupportedBlockWarning	on
CovHighlightResults	off
CovMcdcMode	Masking

# Tabella 39. Publish\_Subscribe\_Model\_Test Configuration Set.Components(8) [30].CodeCoverageSettings

Property	Value
TopModelCoverage	off
ReferencedModelCoverage	off
CoverageTool	None

# Tabella 40. Publish\_Subscribe\_Model\_Test Configuration Set.Components(8).Components [32](1)

Property	Value
Name	Code Appearance
Description	
Components	
ForceParamTrailComments	off
GenerateComments	on
CommentStyle	Auto
IgnoreCustomStorageClasses	on
IgnoreTestpoints	off
IncHierarchyInIds	off
MaxIdLength	31
PreserveName	off
PreserveNameWithParent	off
ShowEliminatedStatement	off
OperatorAnnotations	off
IncAutoGenComments	off
SimulinkDataObjDesc	off
SFDataObjDesc	off
MATLABFcnDesc	off
IncDataTypeInIds	off
PrefixModelToSubsysFcnNames	on
MangleLength	1
CustomSymbolStr	\$R\$N\$M
CustomSymbolStrGlobalVar	\$R\$N\$M
CustomSymbolStrType	\$N\$R\$M_T
CustomSymbolStrField	\$N\$M
CustomSymbolStrFcn	\$R\$N\$M\$F
CustomSymbolStrSimulinkFcn	\$R\$N
CustomSymbolStrFcnArg	rt\$I\$N\$M
CustomSymbolStrBlkIO	rtb_\$N\$M
CustomSymbolStrTmpVar	\$N\$M
CustomSymbolStrMacro	\$R\$N\$M
CustomSymbolStrUtil	\$N\$C
CustomSymbolStrEmxType	emxArray_\$M\$N
CustomSymbolStrEmxFcn	emx\$M\$N
CustomUserTokenString	
CustomCommentsFcn	

DefineNamingRule	None
DefineNamingFcn	
ParamNamingRule	None
ParamNamingFcn	
SignalNamingRule	None
SignalNamingFcn	
InsertBlockDesc	off
InsertPolySpaceComments	off
SimulinkBlockComments	on
MATLABSourceComments	off
EnableCustomComments	off
InternalIdentifier	Shortened
InlinedPrmAccess	Literals
ReqsInCode	off
UseSimReservedNames	off
ReservedNameArray	

# Tabella 41. Publish\_Subscribe\_Model\_Test Configuration Set.Components(8).Components [32](2)

Property	Value
Name	Target
Description	
Components	
IsERTTarget	off
TargetFcnLib	ansi_tfl_table_tmw.mat
TargetLibSuffix	
TargetPreCompLibLocation	
GenFloatMathFcnCalls	NOT IN USE
TargetLangStandard	C99 (ISO)
TargetFunctionLibrary	NOT IN USE
CodeReplacementLibrary	None
UtilityFuncGeneration	Auto
ERTMultiwordTypeDef	System defined
MultiwordTypeDef	System defined
ERTMultiwordLength	2048
MultiwordLength	2048
GenerateFullHeader	on
InferredTypesCompatibility	off
ExistingSharedCode	

GenerateSampleERTMain	off
GenerateTestInterfaces	off
ModelReferenceCompliant	on
ParMdlRefBuildCompliant	on
CompOptLevelCompliant	on
ConcurrentExecutionCompliant	on
IncludeMdlTerminateFcn	on
CombineOutputUpdateFcns	on
CombineSignalStateStructs	off
SuppressErrorStatus	off
ERTFirstTimeCompliant	off
IncludeFileDelimiter	Auto
ERTCustomFileBanners	off
SupportAbsoluteTime	on
LogVarNameModifier	rt_
MatFileLogging	on
MultiInstanceERTCode	off
CodeInterfacePackaging	Nonreusable function
SupportNonFinite	on
SupportComplex	on
PurelyIntegerCode	off
SupportContinuousTime	on
SupportNonInlinedSFcns	on
RemoveDisableFunc	off
RemoveResetFunc	off
SupportVariableSizeSignals	off
ParenthesesLevel	Nominal
CastingMode	Nominal
GenerateClassInterface	off
ModelStepFunctionPrototypeControlComp- liant	off
CPPClassGenCompliant	on
GRTInterface	off
GenerateAllocFcn	off
UseToolchainInfoCompliant	on
GenerateSharedConstants	on
ExtMode	off
ExtModeStaticAlloc	off
ExtModeTesting	off

#### System Model Configuration

ExtModeStaticAllocSize	1000000
ExtModeTransport	0
ExtModeMexFile	ext_comm
ExtModeMexArgs	
ExtModeIntrfLevel	Level1
RTWCAPISignals	off
RTWCAPIParams	off
RTWCAPIStates	off
RTWCAPIRootIO	off
GenerateASAP2	off
MultiInstanceErrorCode	Error

# **Glossary**

**Atomic Subsystem.** A subsystem treated as a unit by an implementation of the design documented in this report. The implementation computes the outputs of all the blocks in the atomic subsystem before computing the next block in the parent system's block execution order (sorted list).

**Block Diagram.** A Simulink block diagram represents a set of simultaneous equations that relate a system or subsystem's inputs to its outputs as a function of time. Each block in the diagram represents an equation of the form y = f(t, x, u) where t is the current time, u is a block input, y is a block output, and x is a system state (see the Simulink documentation for information on the functions represented by the various types of blocks that make up the diagram). Lines connecting the blocks represent dependencies among the blocks, i.e., inputs whose current values are the outputs of other blocks. An implementation of a design described in this document computes a root or atomic system's outputs at each time step by computing the outputs of the blocks in an order determined by block input/output dependencies.

**Block Parameter.** A variable that determines the output of a block along with its inputs, for example, the gain parameter of a Gain block.

**Block Execution Order.** The order in which Simulink evaluates blocks during simulation of a model. The block execution order determined by Simulink ensures that a block executes only after all blocks on whose outputs it depends are executed.

**Checksum.** A number that indicates whether different versions of a model or atomic subsystem differ functionally or only cosmetically. Different checksums for different versions of the same model or subsystem indicate that the versions differ functionally.

**Design Variable.** A symbolic (MATLAB) variable or expression used as the value of a block parameter. Design variables allow the behavior of the model to be altered by altering the value of the design variable.

**Signal.** A block output, so-called because block outputs typically vary with time.

**Virtual Subsystem.** A subsystem that is purely graphical, i.e., is intended to reduce the visual complexity of the block diagram of which it is a subsystem. An implementation of the design treats the blocks in the subsystem as part of the first nonvirtual ancestor of the virtual subsystem (see Atomic Subsystem).

# **About this Report**

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# **Report Overview**

This report describes the design of the Publish\_Subscribe\_Model\_Test system. The report was generated automatically from a Simulink model used to validate the design. It contains the following sections:

**Model Version.** Specifies information about the version of the model from which this design description was generated. Includes the model checksum, a number that indicates whether different versions of the model differ functionally or only cosmetically. Different checksums for different versions indicate that the versions differ functionally.

**Root System.** Describes the design's root system.

**Subsystems.** Describes each of the design's subsystems.

**Design Variables.** Describes system design variables, i.e., MATLAB variables and expressions used as block parameter values.

**System Model Configuration.** Lists the configuration parameters, e.g., start and stop time, of the model used to simulate the system described by this report.

**Requirements Traceability.** Shows design requirements associated with elements of the design model. This section appears only if the design model contains requirements links.

**Glossary.** Defines Simulink terms used in this report.

# **Root System Description**

This section describes a design's root system. It contains the following sections:

**Diagram.** Simulink block diagram that represents the algorithm used to compute the root system's outputs.

**Description.** Description of the root system. This section appears only if the model's root system has a Documentation property or a Doc block.

**Interface.** Name, data type, width, and other properties of the root system's input and output signals. The number of the block port that outputs the signal appears in angle brackets appended to the signal name. This section appears only if the root system has input or output ports.

**Blocks.** This section has two subsections:

- **Parameters.** Describes key parameters of blocks in the root system. This section also includes graphical and/or tabular representations of lookup table data used by lookup table blocks, i.e., blocks that use lookup tables to compute their outputs.
- **Block Execution Order.** Order in which blocks must be executed at each time step in order to ensure that each block's inputs are available when it executes.

**State Charts.** Describes state charts used in the root system. This section appears only if the root system contains Stateflow blocks.

# **Subsystem Descriptions**

This section describes a design's subsystems. Each subsystem description contains the following sections:

**Checksum.** This section appears only if the subsystem is an atomic subsystem. The checksum indicates whether the version of the model subsystem used to generate this report differs functionally from other versions of the model subsystem. If two model checksums differ, the corresponding versions of the model differ functionally.

**Diagram.** Simulink block diagram that graphically represents the algorithm used to compute the subsystem's outputs.

**Description.** Description of the subsystem. This section appears only if the subsystem has a Documentation property or contains a Doc block.

**Interface.** Name, data type, width, and other properties of the subsystem's input and output signals. The number of the block port that outputs the signal appears in angle brackets appended to the signal name. This section appears only if the subsystem is atomic and has input or output ports.

**Blocks.** Blocks that this subsystem contains. This section has two subsections:

- Parameters. Key parameters of blocks in the subsystem. This section also includes graphical and/or tabular representations of lookup table data used by lookup table blocks, blocks that use lookup tables to compute their outputs.
- **Block Execution Order.** Order in which the subsystem's blocks must be executed at each time step in order to ensure that each block's inputs are available when the block executes .This section appears only if the subsystem is atomic. Note: in Acrobat(PDF) reports, the number in square brackets next to the block name is a hyperlink to the block parameter table. The number has no model significance.

**State Charts.** Describes state charts used in the subsystem. This section appears only if the root system contains Stateflow blocks.

# **State Chart Descriptions**

This section describes the state machines used by Stateflow blocks to compute their outputs, i.e., Stateflow blocks. Each state machine description contains the following sections:

**Chart.** Diagram representing the state machine.

**States.** Describes the state machine's states. Each state description includes the state's diagram and diagrams and/or descriptions of graphical functions, Simulink functions, truth tables, and MATLAB functions parented by the state.

**Transitions.** Transitions between the state machine's states. Each transition description specifies the values of key transition properties. Appears only if a transition has properties that do not appear on the chart.

**Junctions.** Transition junctions. Each junction description specifies the values of key junction properties. Appears only if a junction has properties that do not appear on the chart.

**Events.** Events that trigger state transitions. Each event description specifies the values of key event properties.

**Data.** Data types and other properties of the Stateflow block's inputs, outputs, and other state machine data.

**Targets.** Executable implementations of the state machine used to compute the outputs of the corresponding Stateflow block.

**MATLAB Supporting Functions.** List of functions invoked by MATLAB functions defined in the chart.