| Transition | Modeled tasks and gateways | Comments |
|------------|---|--|
| S0/S1 | Tasks: | The driver must engage |
| | som_start (Engage | the procedure using the |
| | the procedure) | DMI to start the mission |
| S1/S2 | Tasks: som_enterid_dmi_1 (Enter the Driver-ID) som_retry_dmi_1 (Retry) som_storeid_evc_1 (Store the Driver-ID) som_validate_evc_1 (Validate level and position) Gateways: XOR between som_enterid_dmi_1 som_retry_dmi_1 som_storeid_evc_1 | The driver has to enter the Driver-ID, but during the process he/she could fail. The system provides a retry mechanism which is abstracted in BPMN terms with a XOR-split between Enter the Driver-ID, Retry and Store the Driver-ID, which allows for only one activity to be run between Retry and Store the Driver-ID |
| S1/S4 | Tasks: som_enterid_dmi_1 (Enter the Driver-ID) som_retry_dmi_1 (Retry) som_storeid_evc_1 (Store the Driver-ID) som_validate_evc_1 (Validate level and position) Gateways: XOR between som_enterid_dmi_1 som_retry_dmi_1 som_storeid_evc_1 | This is basically the same trace as followed by the transition S1/S2, but the On-Board system recognizes the validity of the level and position, preparing the system to contact the RBC. |
| S2/S3 | Tasks: som_validate_evc_1 (Validate level and position) | This same activity takes in account the insertion of the level by the user |

Tasks: som_openconn_rtm_1 (Open the connection) som_giveup_evc_1 (Give up) som_checkpos_rbc_1 (Check the position) textbfsom_checktrain_rbc_1 (Check the train) som_storepos_rbc_1 (Store the valid position) $som_storevalacc_rbc_1$ (Store the 'valid' and 'accepted' flag (RBC)) $som_storeacc_evc_1$ (Store the 'accepted' flag (EVC)) som_driversel_dmi_1 (Await driver selection) Gateways: XOR between S3/S10 $som_validate_evc_1$ $som_openconn_rtm_1$ $som_retry_dmi_2$ XOR between $som_openconn_rtm_1$ $som_retry_dmi_2$ $som_checkpos_rbc_1$ $som_giveup_evc_1$ XOR between $som_checkpos_rbc_1$ $som_checktrain_rbc_1$ $som_storepos_rbc_1$ XOR between $som_checktrain_rbc_1$ $som_storepos_rbc_1$ $som_storevalacc_rbc_1$ XOR between $som_storeacc_evc_1$ $som_giveup_evc_1$

 $som_driversel_dmi_1$

From S3 the process allows the system to contact the RBC and go through all the checks that need to be done in order to later allow the train to transition to the FS mode. As seen in the state diagram, it is also possible that the level is 0, 1 or NTC, which triggers the Give up activity that models the transition to the driver mode selection. Also, a *Retry* activity is modeled if the use is interested in retrying to connect to the RBC if the connection initially fails. The activities Store the 'valid' and 'accepted' flag and Store the 'accepted' flag (RBC) are modeled to consider the instances where the train or the position encounter some problems while being assessed.

Tasks: som_openconn_rtm_1 (Open the connection) $som_checkpos_rbc_1$ (Check the position) textbfsom_checktrain_rbc_1 (Check the train) $som_storepos_rbc_1$ (Store the valid position) $som_storevalacc_rbc_1$ (Store the 'valid' and 'accepted' flag (RBC) $som_storeacc_evc_1$ (Store the 'accepted' flag (EVC)) $som_driversel_dmi_1$ (Await driver selection) Gateways: The logic is basically the same as the XOR between one explained for the transition $som_validate_evc_1$ S3/S10, with the difference that the S4/S10 $som_openconn_rtm_1$ Give upactivity is not taken in account since, to get to S4, the $som_retry_dmi_2$ XOR between level must be 2 or 3. $som_openconn_rtm_1$ $som_retry_dmi_2$ $som_checkpos_rbc_1$ $som_giveup_evc_1$ XOR between $som_checkpos_rbc_1$ $som_checktrain_rbc_1$ $som_storepos_rbc_1$ XOR between $som_checktrain_rbc_1$ $som_storepos_rbc_1$ $som_storevalacc_rbc_1$ XOR between $som_storeacc_evc_1$ $som_giveup_evc_1$

 $som_driversel_dmi_1$

| S10/S12 | Tasks: som_inserttraindata_dmi_1 | The transitions S10/S12 and S12/S13 |
|---------|--|--|
| | (Insert train data and number) | are collapsed in a single activity. |
| S12/S13 | Tasks: som_inserttraindata_dmi_1 (Insert train data and number) | The transitions S10/S12 and S12/S13 are collapsed in a single activity. |
| S13/S10 | Tasks: som_checkrbcsess_rtm_1 (Check RBC session) | If the session is dropped an anomaly is notified to the user. The process then continues from the selection by the driver of the mode, as seen in the state diagram. |
| S13/S11 | Tasks: som_checkrbcsess_rtm_1 (Check RBC session) | If the session is still up the process continues. |
| S11/S20 | Tasks: som_selstart_dmi_1 (Select 'Start') | If the user selects 'Start', he/she has also acknowledged the data transmitted by the RBC. |
| S20/S24 | Tasks: som_checklev_evc_1 (Check level) Gateways: XOR between som_selstart_dmi_1 som_sendMAreq_rtm_1 som_checklev_evc_1 | The XOR gateway models the fact that if the level is NTC, 0 or 1, a check on these values must be made to yield the correct mode proposal. |
| S20/S21 | Tasks: som_sendMAreq_rtm_1 (Send MA Request) Gateways: XOR between som_selstart_dmi_1 som_sendMAreq_rtm_1 som_checklev_evc_1 | |

| | Tasks: | |
|---------|---|---|
| S21/S24 | som_checktrainroute_rbc_1 (Check the route for the train) som_checkval_rbc_1 (Check the 'valid' flag) som_grantSR_rbc_1 (Grant SR mode) som_awaitack_dmi_2 (Await driver acknowledgement) Gateways: XOR between som_checkval_rbc_1 som_grantSR_rbc_1 som_grantOS_rbc_1 som_grantFS_rbc_1 | The transition is subjected to the check against the already cited parameters that look for the route of the train and the validity of the position of the train traced by the RBC. If some anomalies in the position and the train route are detected, the RBC proposes the SR mode to the driver. |
| S21/S25 | Tasks: som_checktrainroute_rbc_1 (Check the route for the train) som_checkval_rbc_1 (Check the 'valid' flag) som_grantOS_rbc_1 (Grant OS mode) som_awaitack_dmi_1 (Await driver acknowledgement) Gateways: XOR between som_checkval_rbc_1 som_grantSR_rbc_1 som_grantFS_rbc_1 som_grantFS_rbc_1 | The logic is the same as the one seen in S21/S24, with the difference that the RBC proposes the OS mode in this case. |

| S20/S22 | Tasks: | | |
|---------|-------------------------|--|--|
| | som_checklev_evc_1 | This transition is performed when the stored level is NTC. | |
| | (Check level) | | |
| | som_grantSN_evc_1 | | |
| | (Grant SN) | | |
| | Gateways: | | |
| | XOR between | | |
| | $som_checklev_evc_1$ | | |
| | $som_grantSR_evc_1$ | | |
| | $som_grantUN_evc_1$ | | |
| | $som_grantSN_evc_1$ | | |
| | Tasks: | This transition is performed when the stored level is 0. | |
| | $som_checklev_evc_1$ | | |
| | (Check level) | | |
| | $som_grantUN_evc_1$ | | |
| | (Grant UN) | | |
| S20/S23 | Gateways: | | |
| | XOR between | | |
| | $som_checklev_evc_1$ | | |
| | $som_grantSR_evc_1$ | | |
| | $som_grantUN_evc_1$ | | |
| | $som_grantSN_evc_1$ | | |

Table 1: Mapping between the state transitions and the modeled activities and gateways