

Pyrit: Polynomial Ring Transforms for Fast Erasure Coding

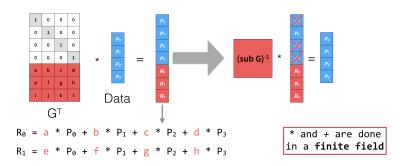
Some parts of this work have been patented

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- 1 Use case and context
- 2 Ring transforms
- 3 An example
- 4 Results
- Conclusion

Erasure codes for network coding: a remainder [1]



Coding operations
erasure codes are matrix vector product over a finite field

[1] J. S. Plank, K. M. Greenan, and E. L. Miller. A Complete Treatment of Software Implementations of Finite Field Arithmetic for Erasure Coding Applications. Tech. rep. UT-CS-13-717. University of Tennessee, 2013.

The context

As operations in a finite field are complex, we do operations into a specific **ring**

- By using fast transforms, we move from a finite field structure into a simpler structure called ring
- In the ring, operations are much easier.
- We need to go back from the ring structure to the field using the reverse transforms

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- 3 An example
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Objective: fast encoding/decoding operations

$$\mathbb{F}_{2^{\mathsf{w}}}: \qquad (\alpha_0, \ldots, \alpha_{k-1}) \quad imes \left(egin{array}{cccc} \gamma_{0,0} & \ldots & \gamma_{0,n-1} \\ \vdots & \ddots & \vdots \\ \gamma_{k-1,0} & \ldots & \gamma_{k-1,n-1} \end{array}
ight)$$

 $R_{2,w+1}$:

Objective : fast encoding/decoding operations

$$\mathbb{F}_{2^{w}}: \qquad (\alpha_{0}, \ldots, \alpha_{k-1}) \quad \times \left(\begin{array}{ccc} \gamma_{0,0} & \ldots & \gamma_{0,n-1} \\ \vdots & \ddots & \vdots \\ \gamma_{k-1,0} & \ldots & \gamma_{k-1,n-1} \end{array}\right)$$

 $R_{2,w+1}: (a_0,\ldots,a_{k-1})$

Objective: fast encoding/decoding operations

$$\mathbb{F}_{2^w}: \qquad (lpha_0,\ldots,lpha_{k-1}) \quad imes \left(egin{array}{cccc} \gamma_{0,0} & \ldots & \gamma_{0,n-1} \ dots & \ddots & dots \ \gamma_{k-1,0} & \ldots & \gamma_{k-1,n-1} \end{array}
ight) \ & \qquad \downarrow \ R_{2,w+1}: \quad (a_0,\ldots,a_{k-1}) \quad imes \left(egin{array}{cccc} g_{0,0} & \ldots & g_{0,n-1} \ dots & \ddots & dots \ g_{k-1,0} & \ldots & g_{k-1,n-1} \end{array}
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$$\mathbb{F}_{2^{w}}: \qquad (\alpha_{0}, \ldots, \alpha_{k-1}) \qquad \times \left(\begin{array}{ccc} \gamma_{0,0} & \ldots & \gamma_{0,n-1} \\ \vdots & \ddots & \vdots \\ \gamma_{k-1,0} & \ldots & \gamma_{k-1,n-1} \end{array} \right) \qquad = (\beta_{0}, \ldots, \beta_{n-1})$$

$$\downarrow \qquad \qquad \qquad \downarrow \qquad \qquad \uparrow \uparrow$$

$$R_{2,w+1}: \qquad (a_{0}, \ldots, a_{k-1}) \qquad \times \left(\begin{array}{ccc} g_{0,0} & \ldots & g_{0,n-1} \\ \vdots & \ddots & \vdots \\ g_{k-1,0} & \ldots & g_{k-1,n-1} \end{array} \right) \qquad = (b_{0}, \ldots, b_{n-1})$$

Transforms between the field and the ring

To optimize the coding operations, we consider several transforms:

- The *embedding transform* [2]: **very fast** to transform **finite field** elements to **ring elements**
- The parity transform: very fast to transform finite field elements to ring elements
- The sparse transform: very efficient to reduce the complexity of the operations in the ring. Will choose the sparsest element in the ring corresponding to the field element.

^[2] Toshiya Itoh and Shigeo Tsujii. "Structure of parallel multipliers for a class of fields $GF(2^m)$ ". In: Information and Computation 83.1 (1989), pp. 21 –40.

Final scheme [3]

$$\mathbb{F}_{2^{\mathsf{w}}}: \qquad (\alpha_{0}, \dots, \alpha_{k-1}) \quad \times \left(\begin{array}{ccc} \gamma_{0,0} & \dots & \gamma_{0,n-1} \\ \vdots & \ddots & \vdots \\ \gamma_{k-1,0} & \dots & \gamma_{k-1,n-1} \end{array} \right) = (\beta_{0}, \dots, \beta_{n-1})$$

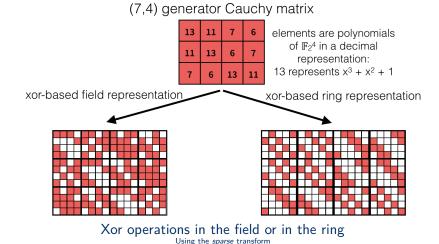
$$\downarrow Emb \text{ or } Par \qquad \qquad \downarrow Sparse \qquad \qquad \uparrow Emb^{-1} \text{ or } Par^{-1}$$

$$R_{2,w+1}: \quad (a_{0}, \dots, a_{k-1}) \quad \times \left(\begin{array}{ccc} g_{0,0} & \dots & g_{0,n-1} \\ \vdots & \ddots & \vdots \\ g_{k-1,0} & \dots & g_{k-1,n-1} \end{array} \right) = (b_{0}, \dots, b_{n-1})$$

^[3] J. Detchart and J. Lacan. "Fast Xor-based Erasure Coding based on Polynomial Ring Transforms". In: ISIT17.

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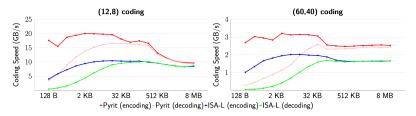
The sparse transform



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Implementation Results

Comparison with the fastest known implementation: Intel ISA-L [4]



Coding Speeds (in GB/s) for several data block lengths CPU: Intel Core i5-6500 (Skylake architecture) @3.20 GHz

- Encoding and decoding: up to 2X faster
- Works well with small codes (GF(16), GF(64))

^{[4] &}quot;ISA-L: Intel Storage Acceleration library". In: https://github.com/01org/isa-I.

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- 3 An example
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- **5** Conclusion

Conclusion

- Reduces the coding and decoding complexities
- Easy to implement (just a set of xor operations)
- Allows the use of uncommon fields like GF(64) rather than GF(16) or GF(256)
- GF(64) is a good compromise between capacity corrections and coding speed (fits perfectly in Tetrys)

Thank you!