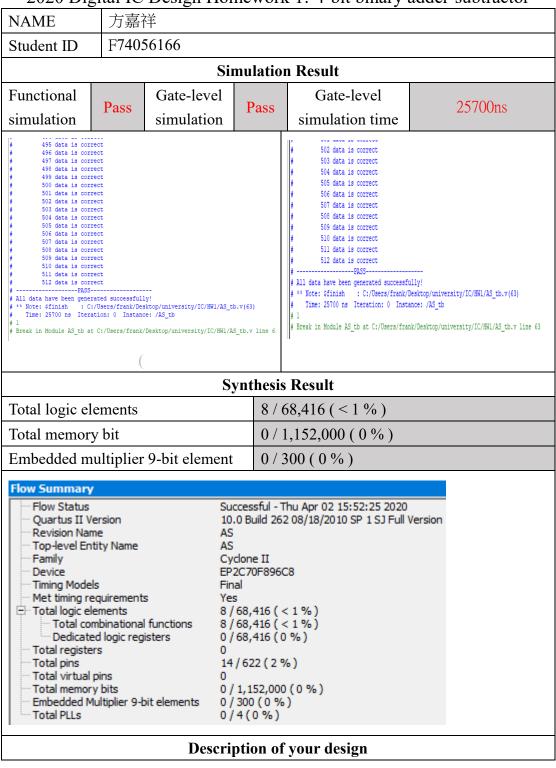
2020 Digital IC Design Homework 1: 4-bit binary adder-subtractor



這次作業中,我將 full adder 獨立出來成一個 module,名叫 AS,在測試 4-bit binary adder-subtractor 時就可以直接呼叫,這可以讓 code 可讀性高些,其餘的部分就是照著作業上 block 的圖加上對應的邏輯閘。

Scoring = (Total logic elements + total memory bit + 9*embedded multiplier 9-bit element) × (gate-level simulation time in \underline{ns})