**CMPE-260 Digital System Design**

**Lab Exercise 6**

**Pipelined MIPS Processor**

By submitting this report, I attest that its contents are wholly my individual writing about this exercise and that they reflect the submitted code. I further acknowledge that permitted collaboration for this exercise consists only of discussions of concepts with course staff and fellow students. Other than code provided by the instructor for this exercise, all code was developed by me.

**Frank Andes**

Frank Andes

Performed 3/23

Submitted 3/27

Lab Section 4

Instructor: Richard Cliver

TAs: Colin Vo, Robbie, Henry