```
module m8_1e_testbench();

   reg input1;
   reg [7:0] input_bus;
   reg [2:0] input_bus2;
   wire output1;

   m8_1e   UUT   ( .in(input_bus), .sel(input_bus2), .e(input1), .o(output1));

// below is the "stimuli," the values for the inputs
// be sure to select a range of inputs that will fully exercise your design

    initial
    begin

        //-------------  Current Time:  0ns
        input1=1'b1;
        input_bus = 8'b10101010;
        input_bus2 = 3'b000;
    #100;  //This advances time by 100 units (ns in this case)
        // -------------  Current Time:  100ns
        input_bus2 = 3'b001;
        #100; // -------------  Current Time:  200ns
        input_bus2 = 3'b010;
        #100; // -------------  Current Time:  300ns
        input_bus2 = 3'b011;
        #100; // -------------  Current Time:  400ns
        input_bus2 = 3'b100;
        #100; // -------------  Current Time:  500ns
        input_bus2 = 3'b101;
        #100; // -------------  Current Time:  600ns
        input_bus2 = 3'b110;
        #100; // -------------  Current Time:  700ns
        input_bus2 = 3'b111;
        #100; // -------------  Current Time:  800ns
        input_bus2 = 3'b001;
        input1=1'b0;
    end
endmodule
```