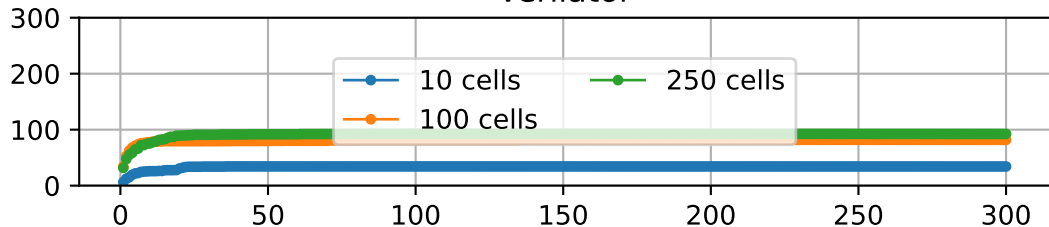
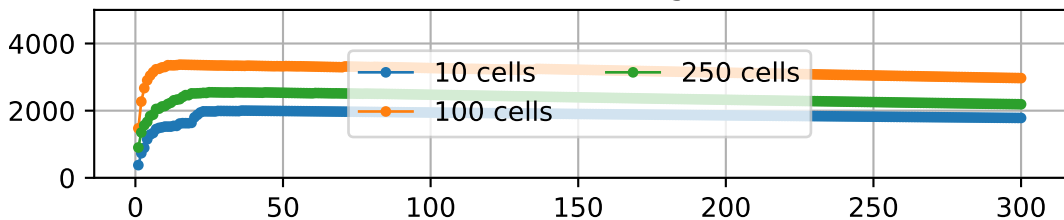


Cell output bit toggles per second

Verilator



Icarus Verilog



CXXRTL

