# Repaso de C

# Estructura de Computadores

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#### Bibliografía:

Apéndice C. Digital Design and Computer Architecture. ARM edition. S. Harris y D.M. Harris.



### Operadores (de mayor a menor prioridad)

Category	Operator	Description	
Unary	++	post-increment	
		post-decrement	
	&	memory address of a variable	
	~	bitwise NOT	
	!	Boolean NOT	
	_	negation	
	++	pre-increment	
		pre-decrement	
	(type)	casts a variable to (type)	
	sizeof()	size of a variable or type in bytes	
Multiplicative	*	multiplication	
	/	division	
	%	modulo	



## **Operadores (cont)**

Additive	+	addition	
	_	subtraction	
Bitwise Shift	<<	bitshift left	
	>>	bitshift right	
Relational	==	equals	
	!=	not equals	
	<	less than	
	>	greater than	
	<=	less than or equal	
	>=	greater than or equal	



# **Operadores (cont 2)**

Bitwise	&	& bitwise AND	
	٨	bitwise XOR	
		bitwise OR	
Logical	&&	&& Boolean AND	
		Boolean OR	
Ternary	?:	ternary operator	
Assignment	=	assignment	
	+=	addition and assignment	
	-=	subtraction and assignment	
	*=	multiplication and assignment	
	/=	division and assignment	
	%=	modulo and assignment	
	>>=	bitwise right-shift and assignment	
	<<=	bitwise left-shift and assignment	
	<b>&amp;</b> =	bitwise AND and assignment	
	=	bitwise OR and assignment	
	^=	bitwise XOR and assignment	



#### **Variables**

- Una variable tiene:
  - Tipo
  - Nombre
  - Valor
  - Ubicación en memoria
- Distinguimos entre:
  - Globales
  - Locales



# Tipos de datos primitivos

Type	Size (bits)	Minimum	Maximum
char	8	$-2^{-7} = -128$	$2^7 - 1 = 127$
unsigned char	8	0	$2^8 - 1 = 255$
short	16	$-2^{15} = -32,768$	$2^{15} - 1 = 32,767$
unsigned short	16	0	$2^{16} - 1 = 65,535$
long	32	$-2^{31} = -2,147,483,648$	$2^{31} - 1 = 2,147,483,647$
unsigned long	32	0	$2^{32} - 1 = 4,294,967,295$
long long	64	$-2^{63}$	$2^{63}-1$
unsigned long	64	0	$2^{64} - 1$
int	machine-dependent		
unsigned int	machine-dependent		
float	32	±2 <sup>-126</sup>	±2 <sup>127</sup>
double	64	$\pm 2^{-1023}$	±2 <sup>1022</sup>



#### Representación de números en punto flotante

IEEE-754: Define la representación de números de 32 bits (*float*) y 64 bits (*double*). Para 32 bits:

S Exp Mantissa 
$$\equiv (-1)^{S} \times (1.Mantissa) \times (2^{Exp-127})$$

- Ejemplos:
  - 0 10000001 1000 ........ 000 =  $(-1)^0$  x (1.1000 000) x  $(2^{129} 127)$  = +1.1 x  $2^2$  =  $(110)_2$  =  $(6)_{10}$
  - 0 10000101 0000 ...... 000 =  $(-1)^0$  x (1.0000 000) x  $(2^{133} 12^7)$  = +1.0 x  $2^6$  =  $(1000000)_2$  =  $(64)_{10}$
  - 1 10000000 0100 ...... 000 =  $(-1)^1$  x (1.0100....000) x (2<sup>128-127</sup>) = -1.010 x 2<sup>1</sup> =  $-(10.10)_2$  =  $-(2.5)_{10}$



Sección 5.3.2. Digital Design and Computer Architecture. ARM edition. S. Harris y D.M. Harris.

### Otros tipos de datos

- Punteros
- Arrays
- Cadenas de caracteres
- Estructuras
- Creación de tipos propios usando typedef.



#### Acceso a estructuras básicas de datos

Tipo C	Número de bits	
char	8	
short	16	
int	32	

Los mismo tamaños se aplican para la versión unsigned

Ejemplo. Declaramos cuatro variables:

> – char a; int b; short int c; int d;



