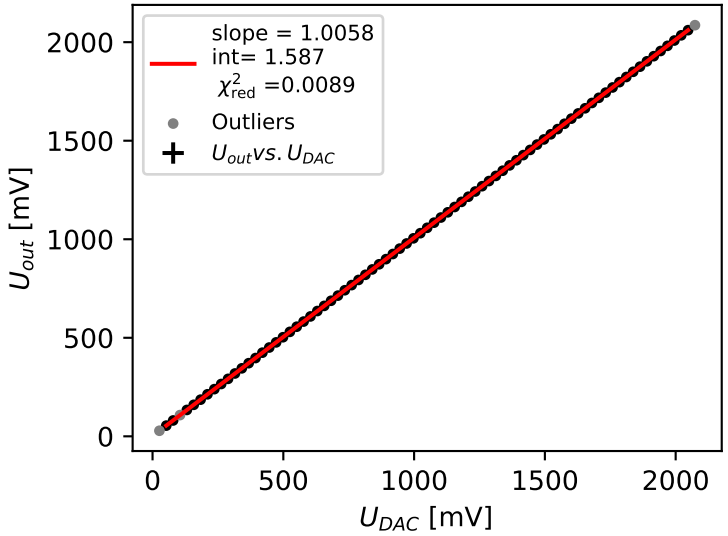
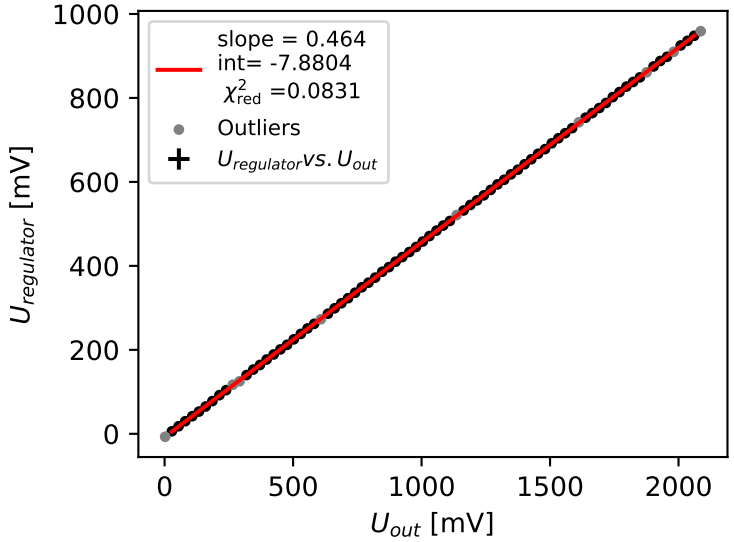
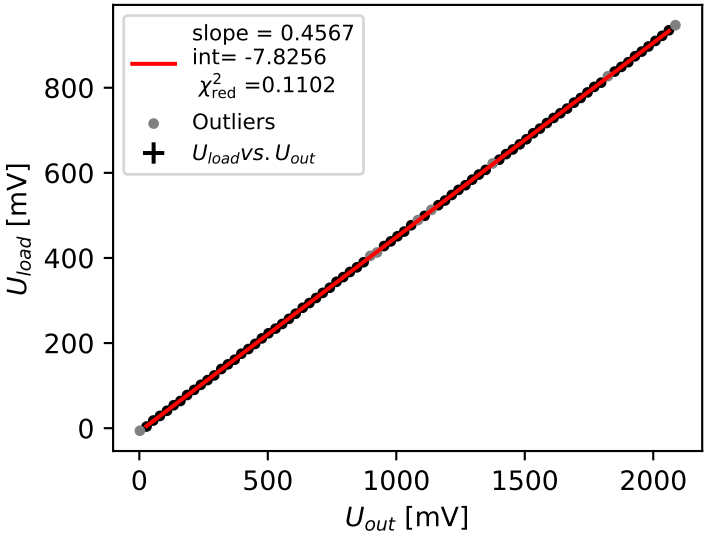
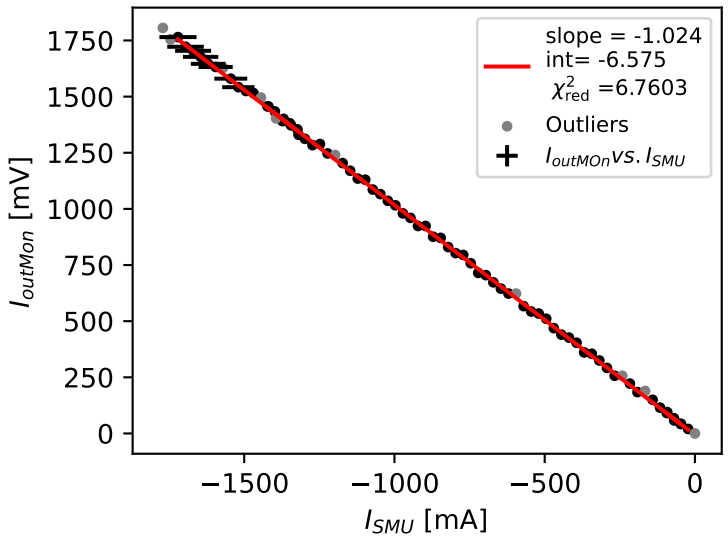
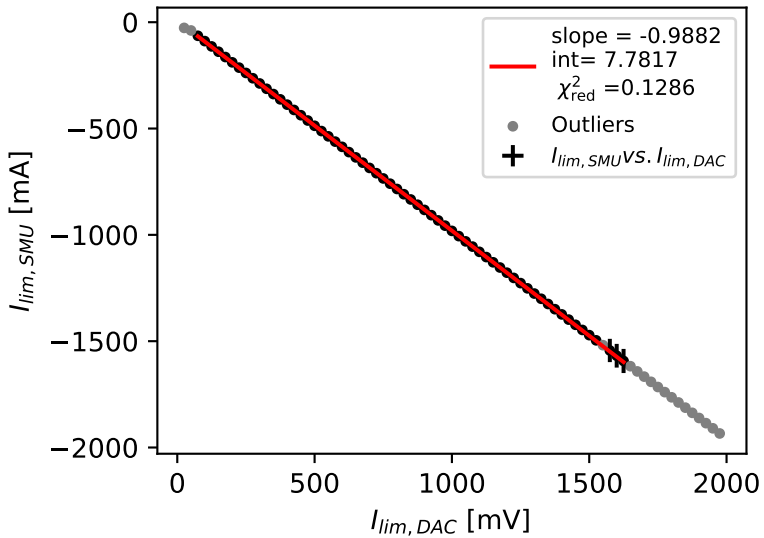




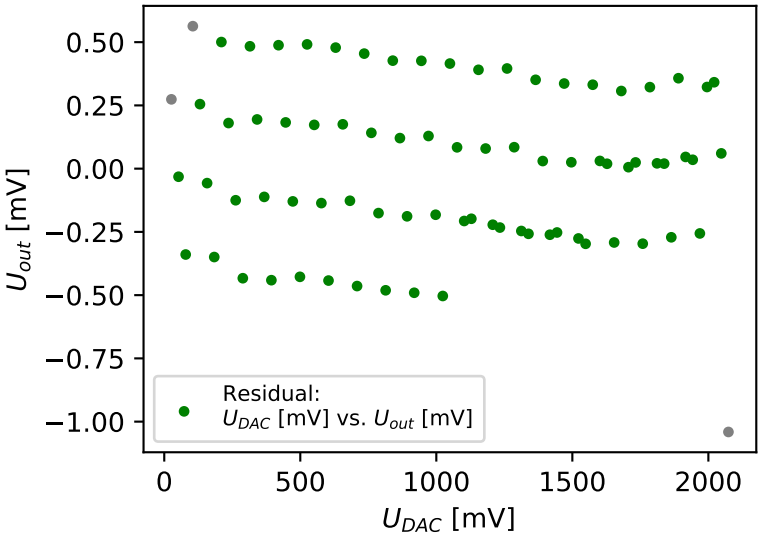


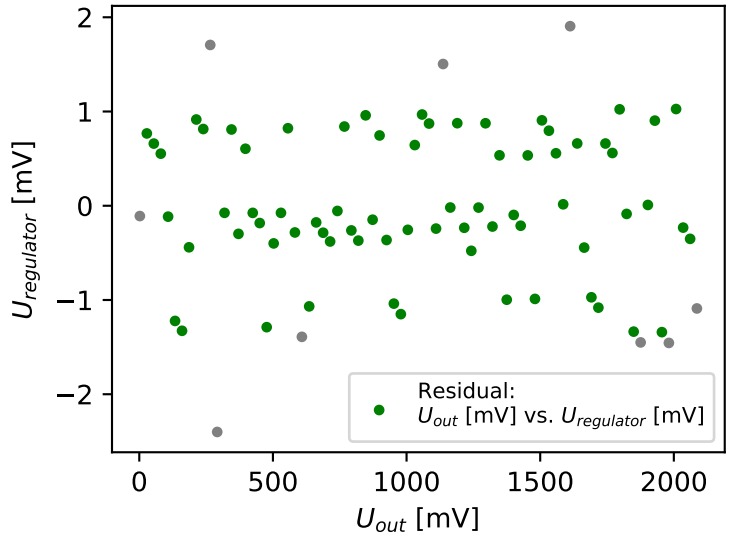


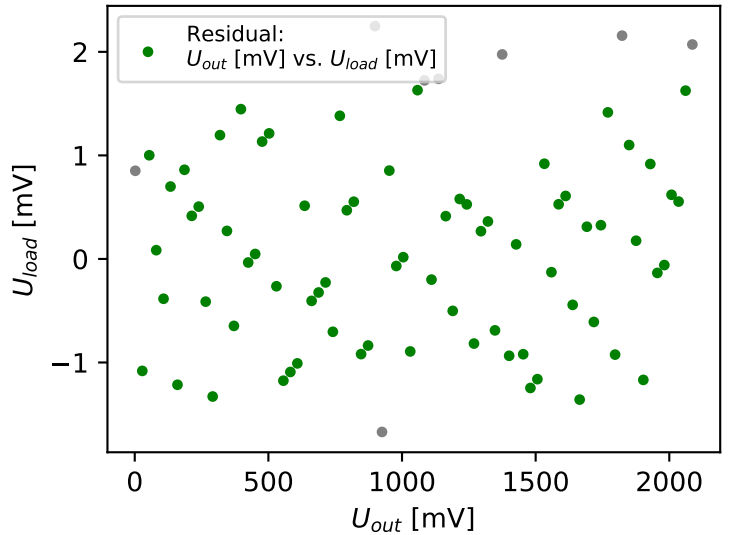


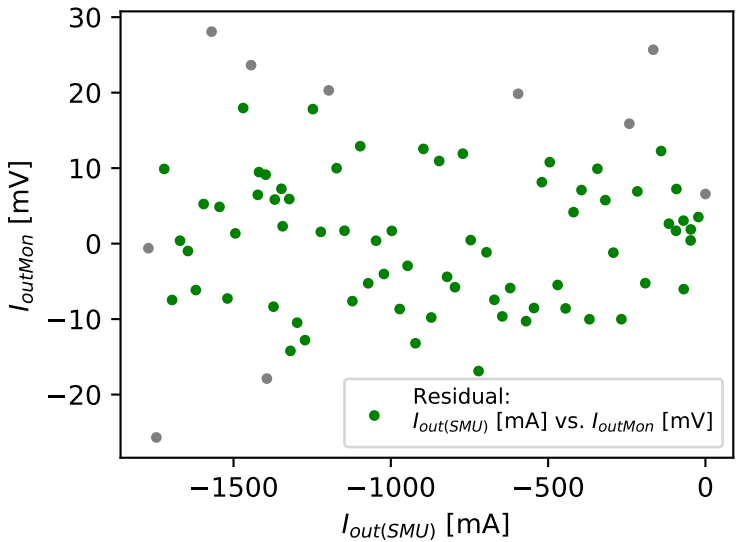
Number of deleted points :

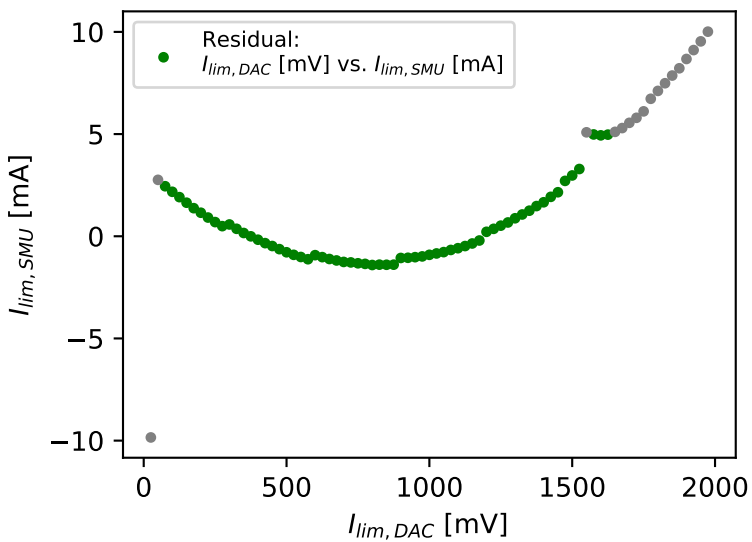
0) U_{out} vs. U_{DAC} : **4**
1) $U_{regulator}$ vs. U_{out} : **9**
2) U_{load} vs. U_{out}: **8**
3) I_{outMON} vs. I_{SMU}: **10**
4) $I_{lim,SMU}$ vs. $I_{lim,DAC}$: **18**

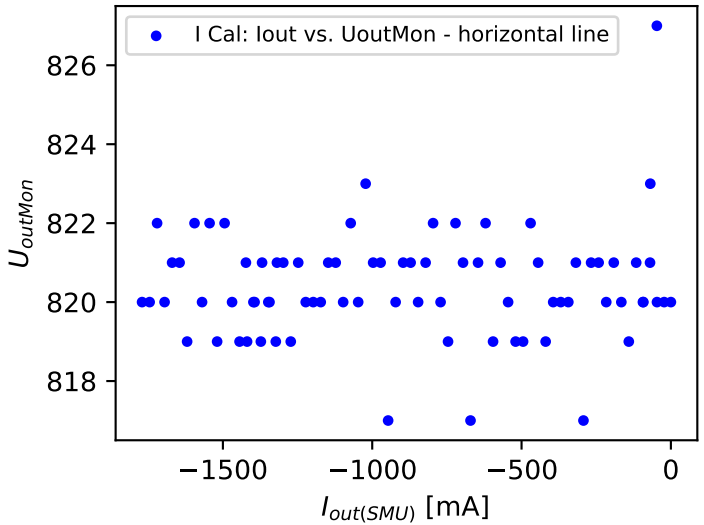

Residual:
U_{DAC} [mV] vs. U_{out} [mV]
U_{out} [mV]
U_{DAC} [mV]

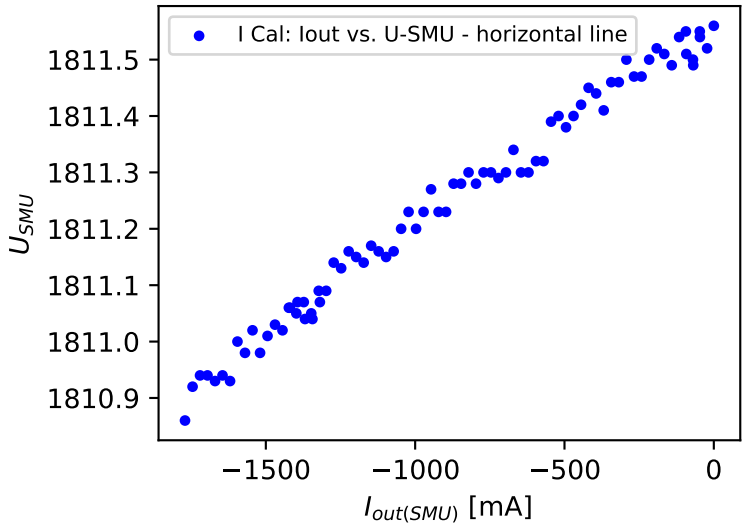

Residual:
U_{out} [mV] vs. $U_{regulator}$ [mV]
$U_{regulator}$ [mV]
U_{out} [mV]

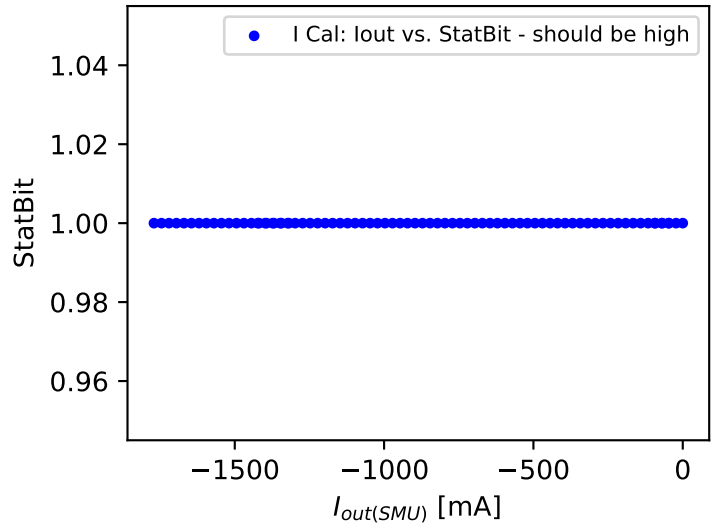

Residual:
U_{out} [mV] vs. U_{load} [mV]
U_{load} [mV]
U_{out} [mV]

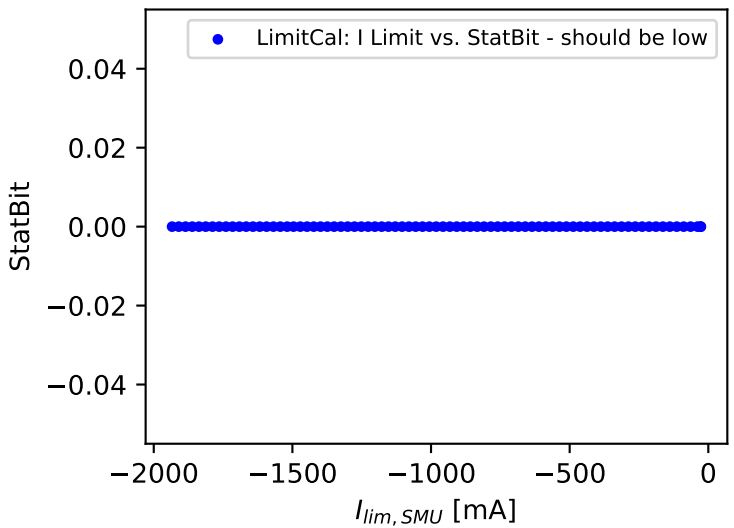

Residual:
$I_{out(SMU)}$ [mA] vs. I_{outMon} [mV]
I_{outMon} [mV]
$I_{out(SMU)}$ [mA]


Residual:
$I_{lim,DAC}$ [mV] vs. $I_{lim,SMU}$ [mA]
$I_{lim,SMU}$ [mA]
$I_{lim,DAC}$ [mV]


I Cal: Iout vs. UoutMon - horizontal line
826
824
822
820
818
U_{outMon}
−1500
−1000
−500
0
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. U-SMU - horizontal line
1811.5
1811.4
1811.3
1811.2
1811.1
1811.0
1810.9
U_{SMU}
−1500
−1000
−500
0
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. StatBit - should be high
1.04
1.02
1.00
0.98
0.96
StatBit
−1500
−1000
−500
0
$I_{out(SMU)}$ [mA]


LimitCal: I Limit vs. StatBit - should be low
0.04
0.02
0.00
−0.02
−0.04
StatBit
−2000
−1500
−1000
−500
0
$I_{lim,SMU}$ [mA]