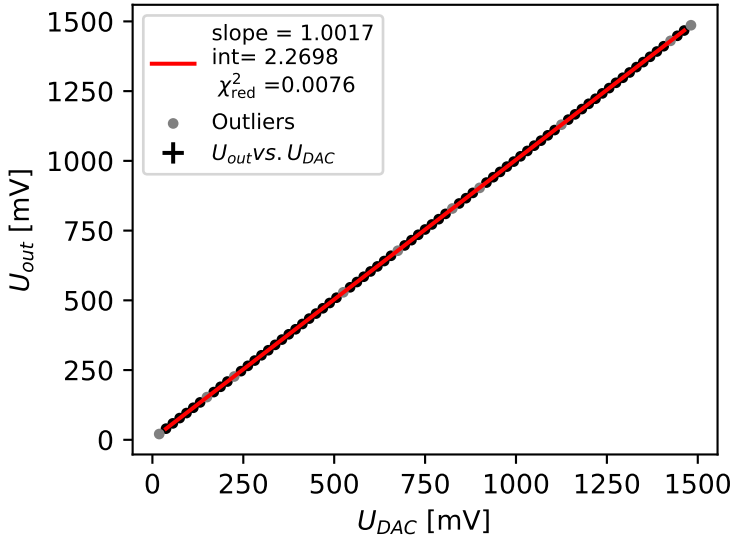
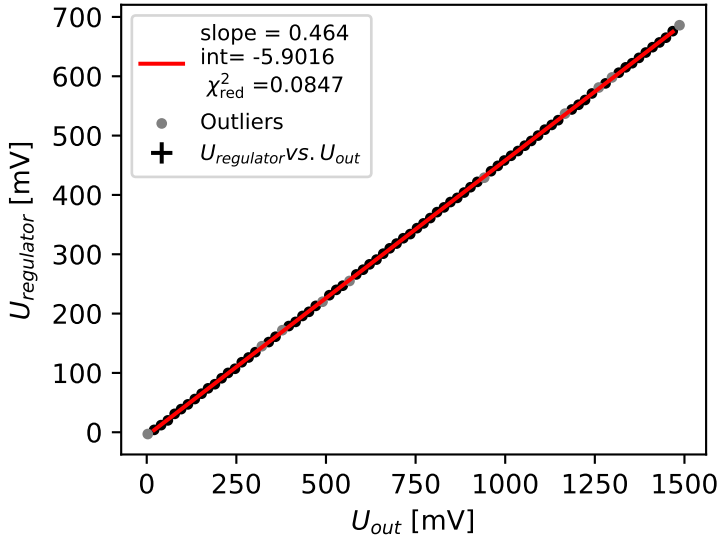
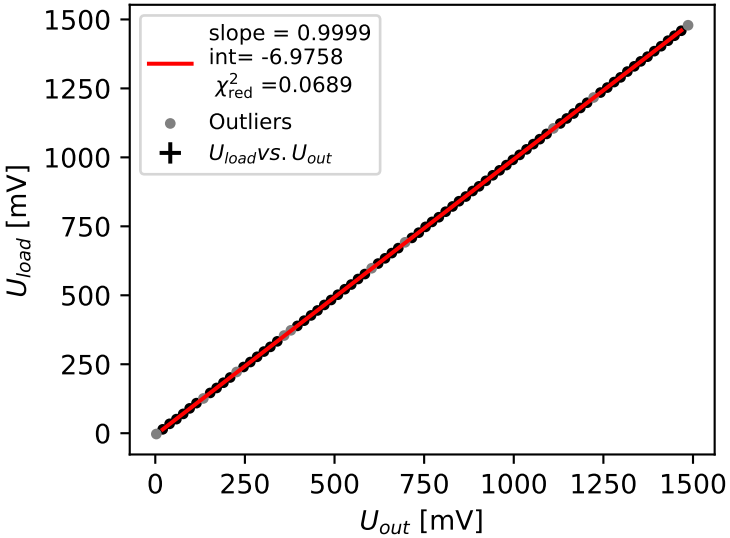
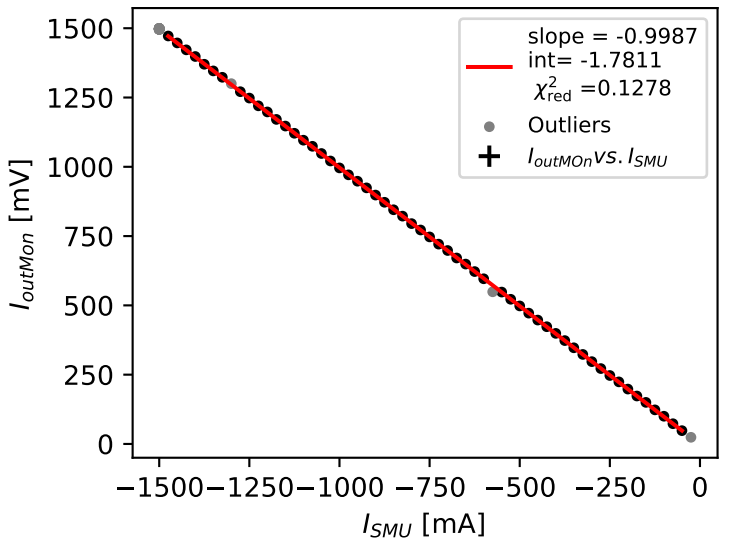
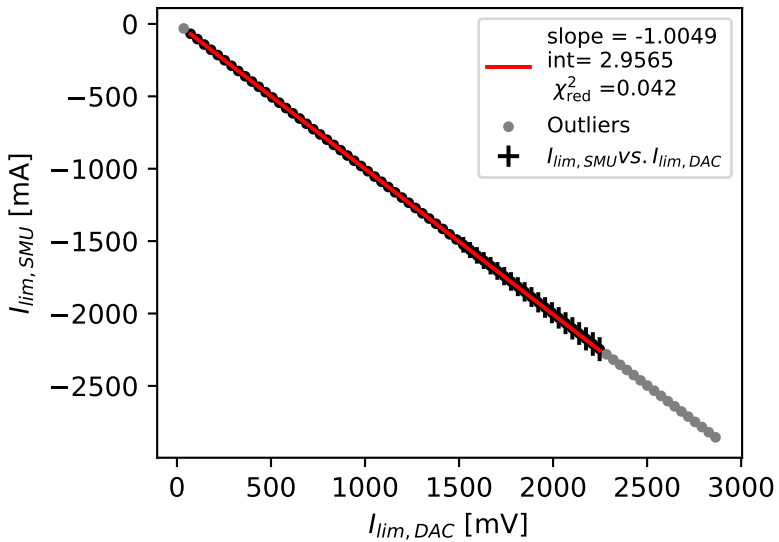




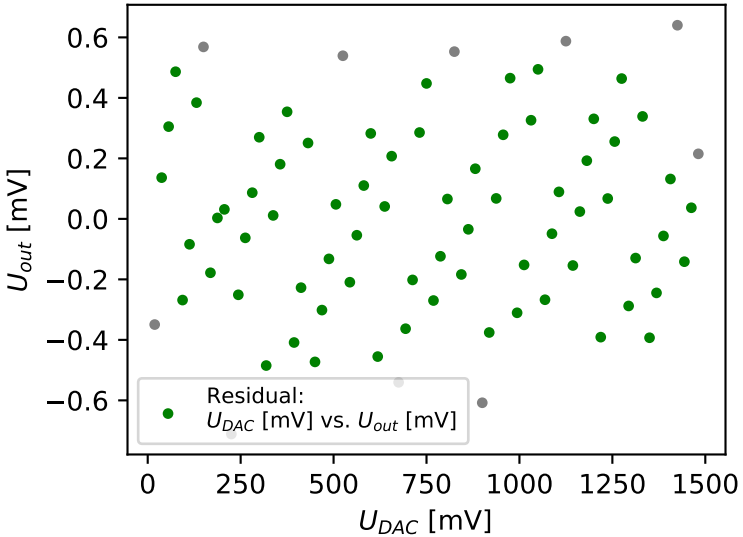


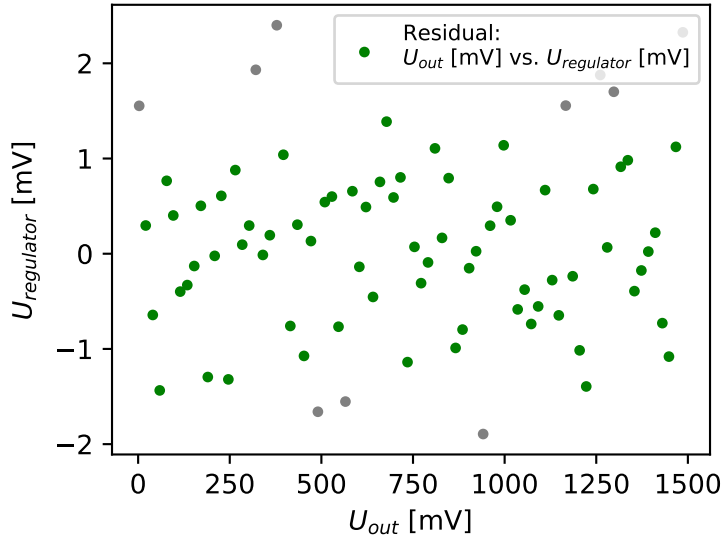


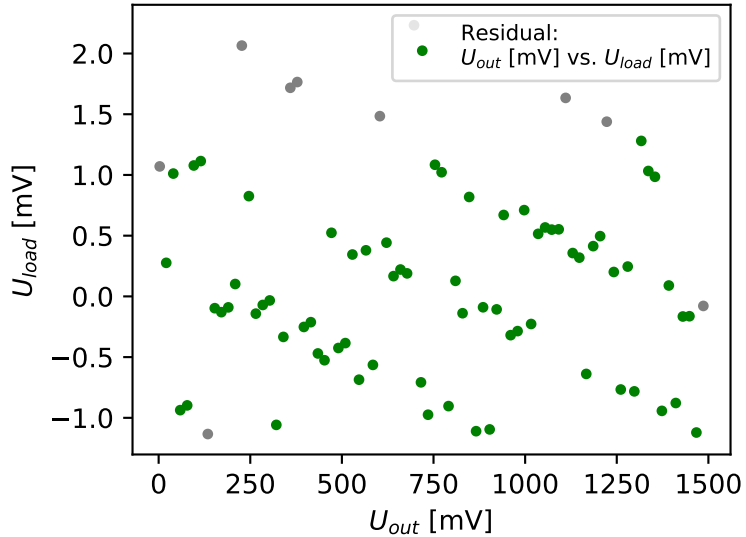


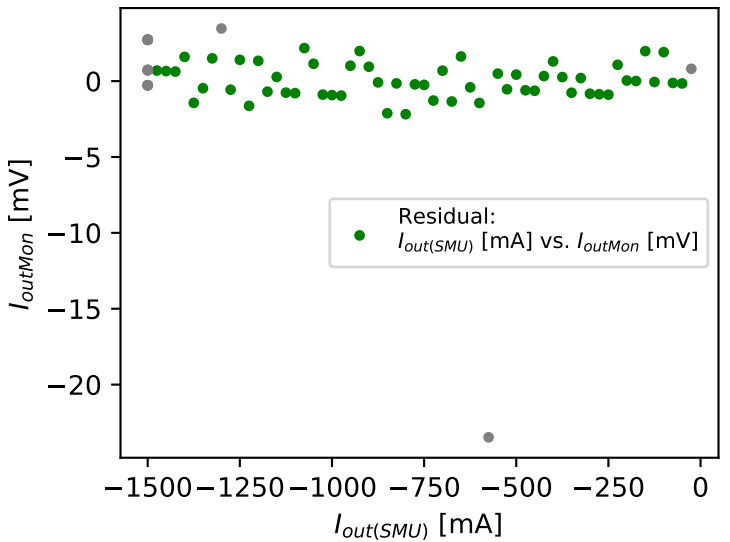
Number of deleted points :

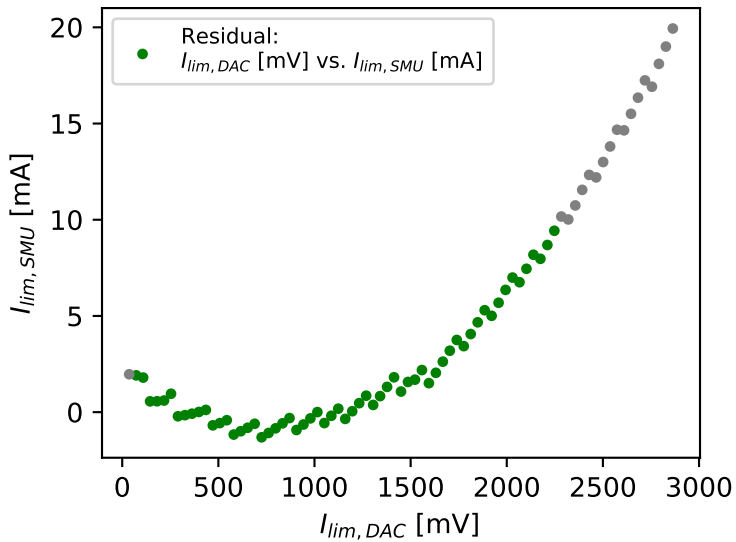
0) U_{out}vs. U_{DAC} : **11**
1) $U_{regulator}$vs. U_{out} : **10**
2) U_{load}vs. U_{out}: **10**
3) I_{outMON}vs. I_{SMU}: **24**
4) $I_{lim,SMU}$vs. $I_{lim,DAC}$: **19**

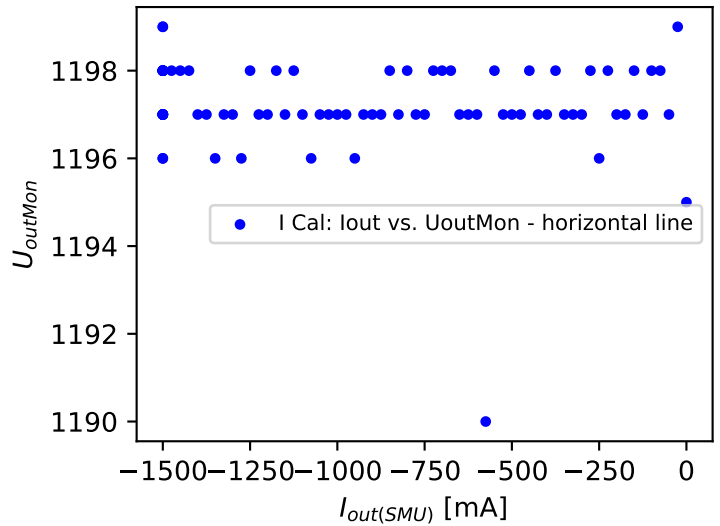

Residual:
U_{DAC} [mV] vs. U_{out} [mV]
U_{out} [mV]
U_{DAC} [mV]

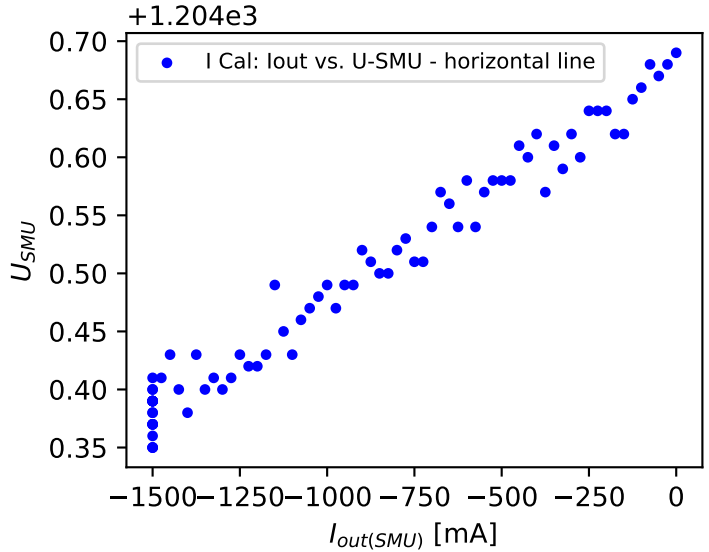

Residual:
U_{out} [mV] vs. $U_{regulator}$ [mV]
$U_{regulator}$ [mV]
U_{out} [mV]

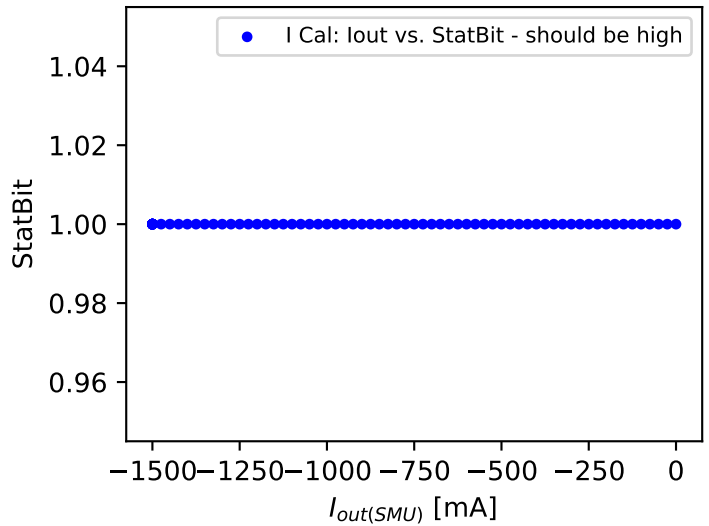

Residual:
U_{out} [mV] vs. U_{load} [mV]
U_{load} [mV]
U_{out} [mV]

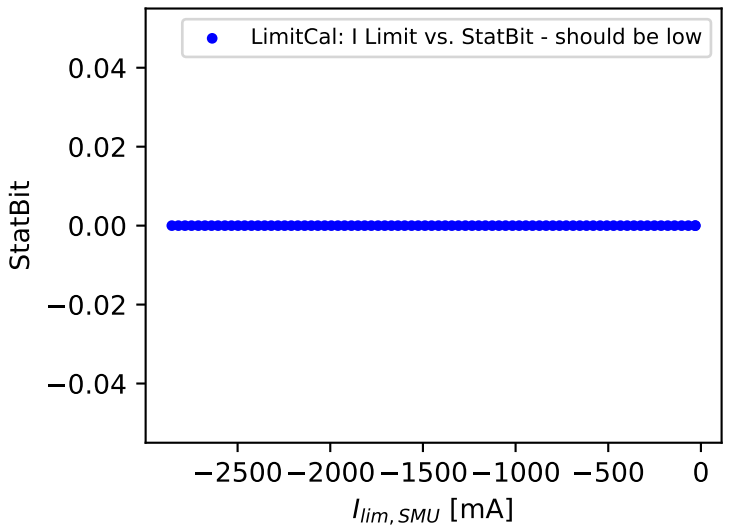

Residual:
$I_{out(SMU)}$ [mA] vs. I_{outMon} [mV]
I_{outMon} [mV]
$I_{out(SMU)}$ [mA]


Residual:
$I_{lim,DAC}$ [mV] vs. $I_{lim,SMU}$ [mA]
$I_{lim,SMU}$ [mA]
$I_{lim,DAC}$ [mV]


I Cal: Iout vs. UoutMon - horizontal line
U_{outMon}
1198
1196
1194
1192
1190
−1500 −1250 −1000 −750 −500 −250 0
$I_{out(SMU)}$ [mA]


+1.204e3
I Cal: Iout vs. U-SMU - horizontal line
U_{SMU}
0.70
0.65
0.60
0.55
0.50
0.45
0.40
0.35
−1500 −1250 −1000 −750 −500 −250 0
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. StatBit - should be high
StatBit
1.04
1.02
1.00
0.98
0.96
−1500 −1250 −1000 −750 −500 −250 0
$I_{out(SMU)}$ [mA]


LimitCal: I Limit vs. StatBit - should be low
StatBit
0.04
0.02
0.00
−0.02
−0.04
−2500 −2000 −1500 −1000 −500 0
$I_{lim,SMU}$ [mA]