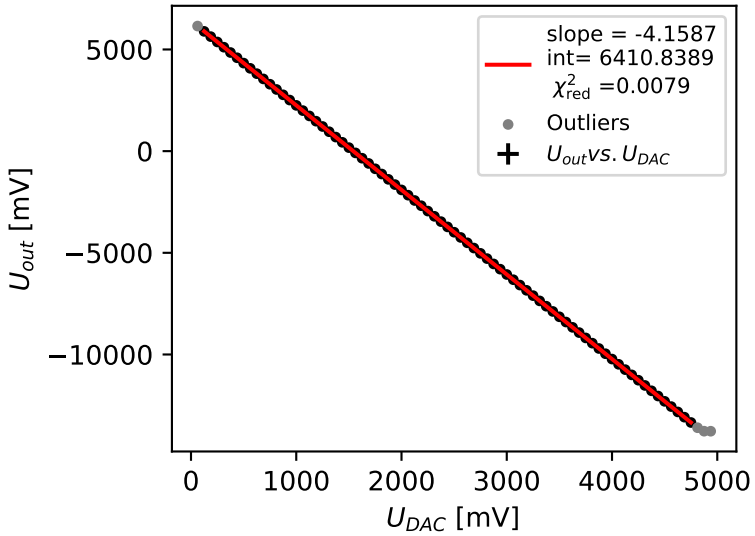
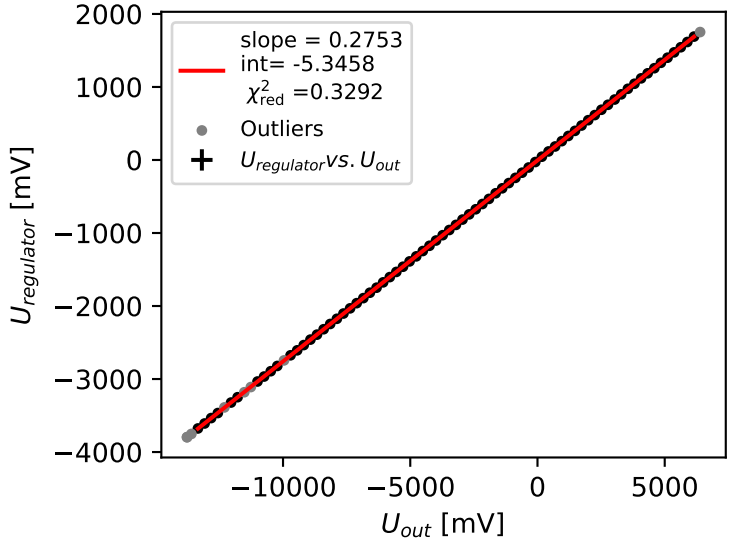
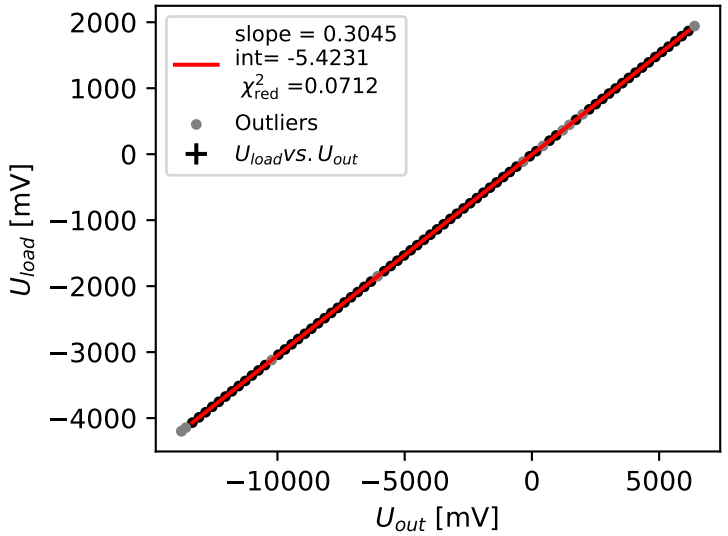
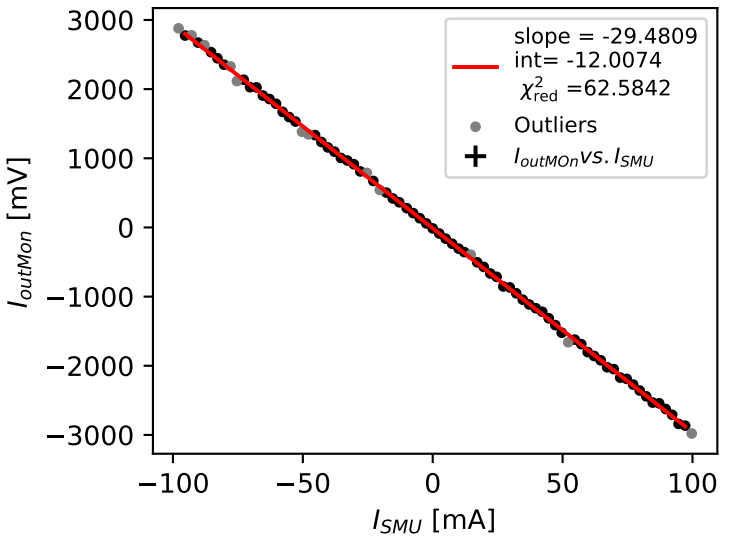
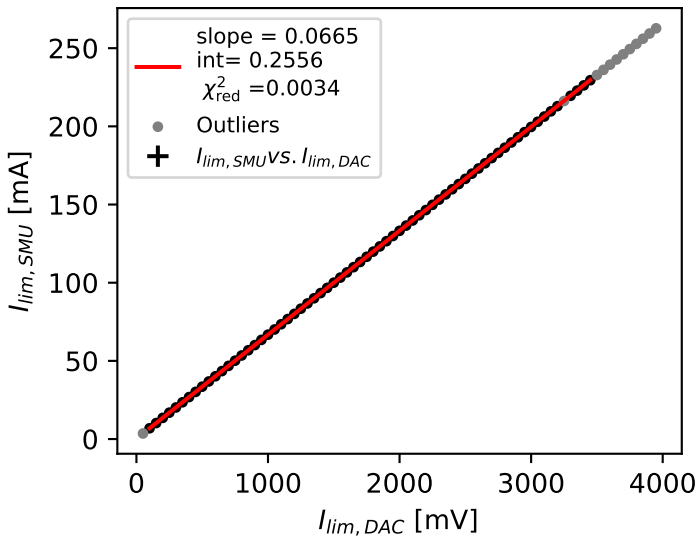


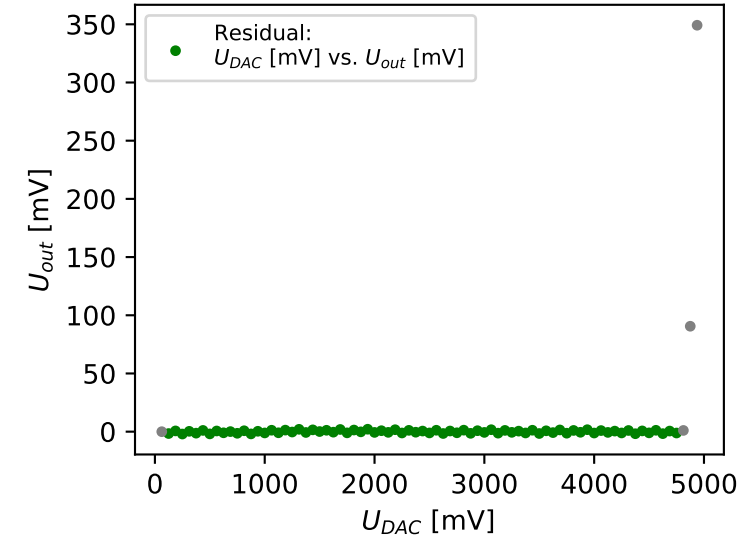



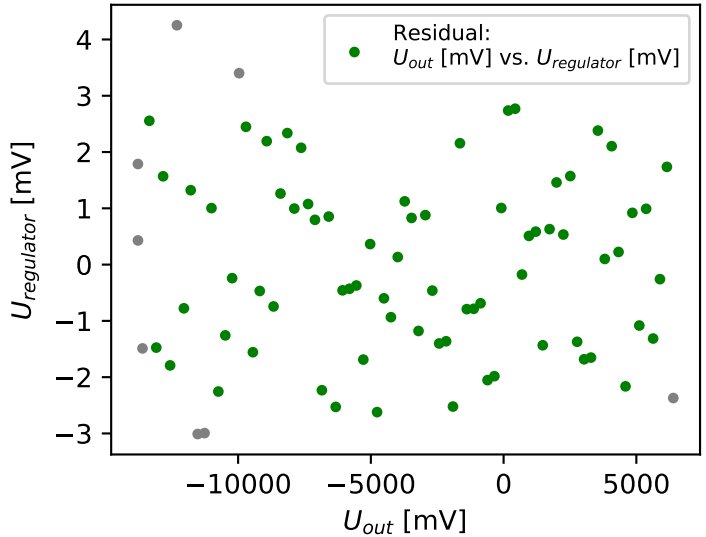



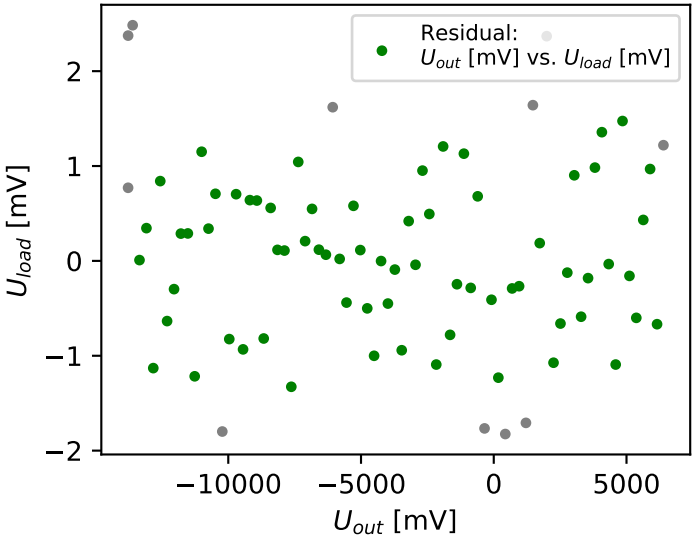



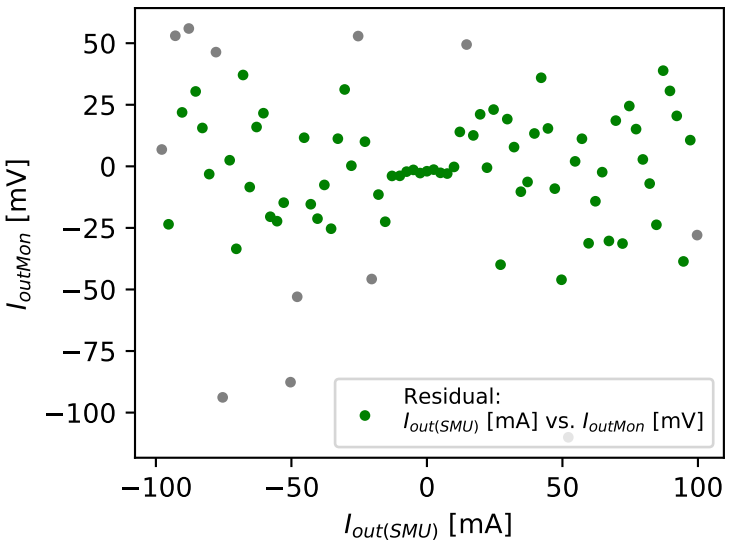

**Number of deleted points :**

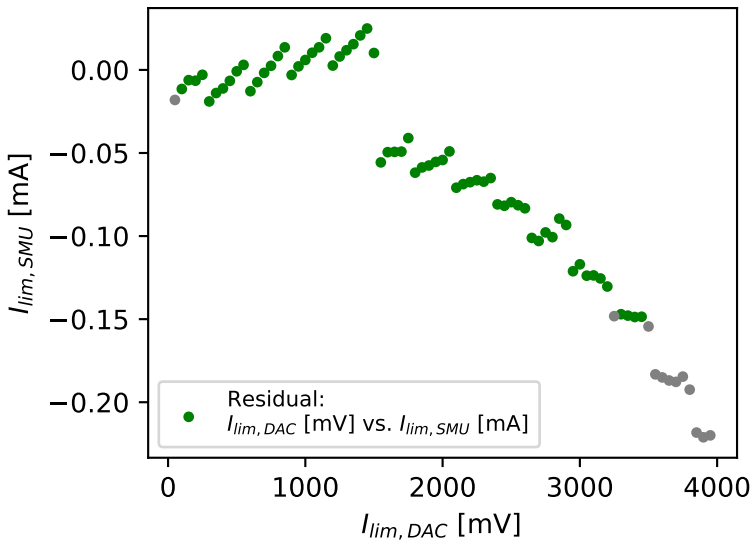
0) $U_{out}$ vs. $U_{DAC}$ : **5**
1) $U_{regulator}$ vs. $U_{out}$ : **8**
2) $U_{load}$ vs. $U_{out}$: **11**
3) $I_{outMON}$ vs. $I_{SMU}$: **12**
4) $I_{lim,SMU}$ vs. $I_{lim,DAC}$ : **13**

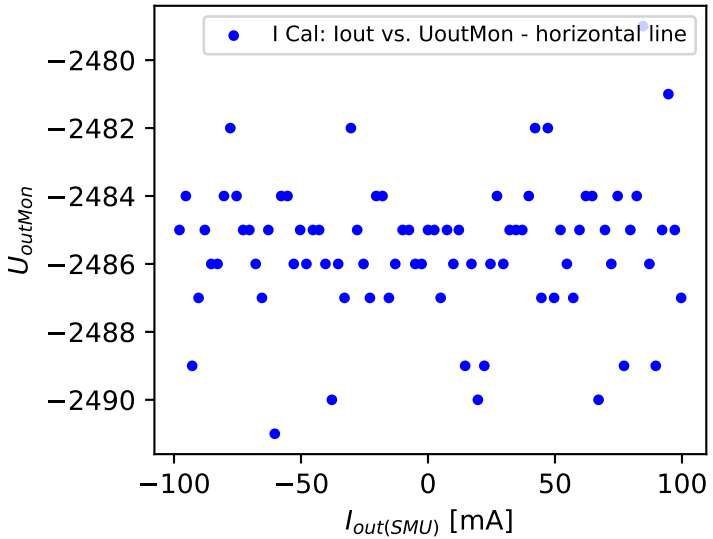

Residual:
$U_{DAC}$ [mV] vs. $U_{out}$ [mV]
$U_{out}$ [mV]
$U_{DAC}$ [mV]

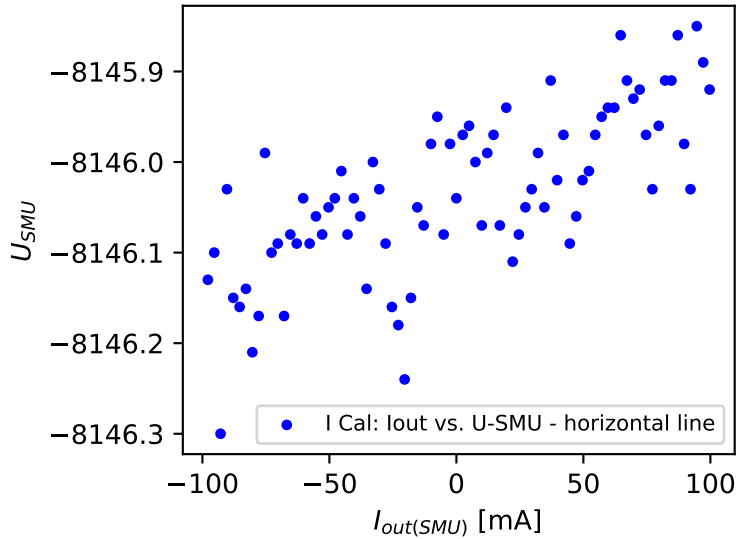

Residual:
$U_{out}$ [mV] vs. $U_{regulator}$ [mV]
$U_{regulator}$ [mV]
$U_{out}$ [mV]

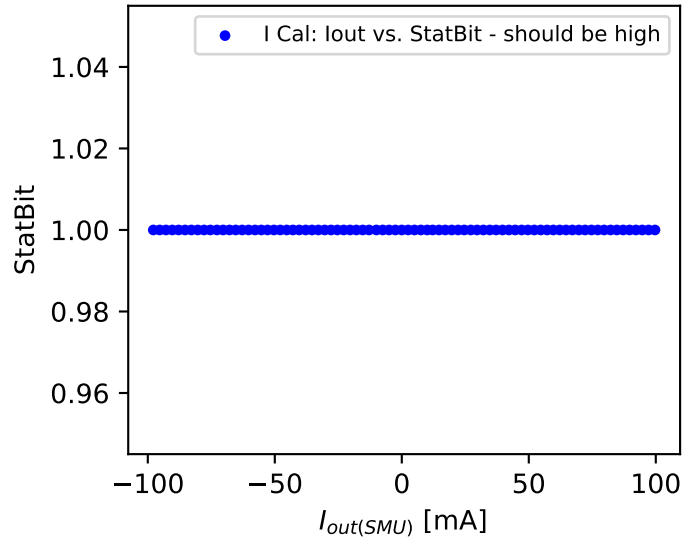

Residual:
$U_{out}$ [mV] vs. $U_{load}$ [mV]
$U_{load}$ [mV]
$U_{out}$ [mV]

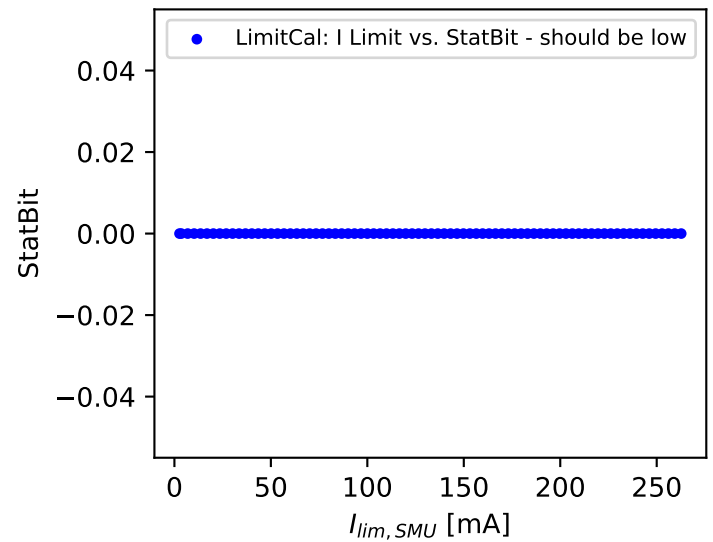

Residual:
$I_{out(SMU)}$ [mA] vs. $I_{outMon}$ [mV]
$I_{outMon}$ [mV]
$I_{out(SMU)}$ [mA]


Residual:
$I_{lim,DAC}$ [mV] vs. $I_{lim,SMU}$ [mA]
$I_{lim,SMU}$ [mA]
$I_{lim,DAC}$ [mV]


I Cal: Iout vs. UoutMon - horizontal line
−2480
−2482
−2484
−2486
−2488
−2490
$U_{outMon}$
−100
−50
0
50
100
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. U-SMU - horizontal line
−8145.9
−8146.0
−8146.1
−8146.2
−8146.3
$U_{SMU}$
−100
−50
0
50
100
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. StatBit - should be high
1.04
1.02
1.00
0.98
0.96
StatBit
−100
−50
0
50
100
$I_{out(SMU)}$ [mA]


LimitCal: I Limit vs. StatBit - should be low
0.04
0.02
0.00
−0.02
−0.04
StatBit
0
50
100
150
200
250
$I_{lim,SMU}$ [mA]