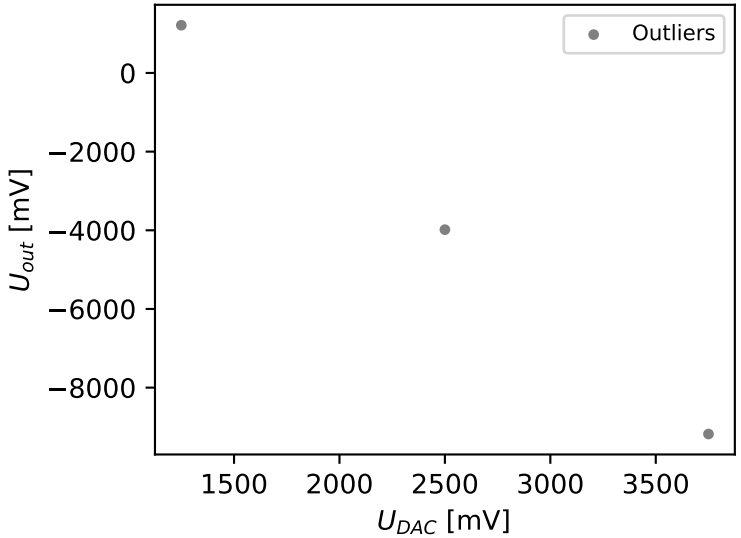
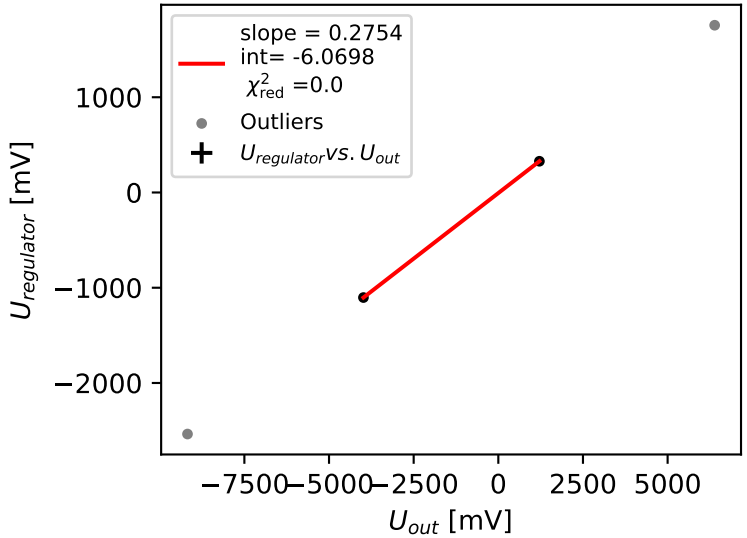
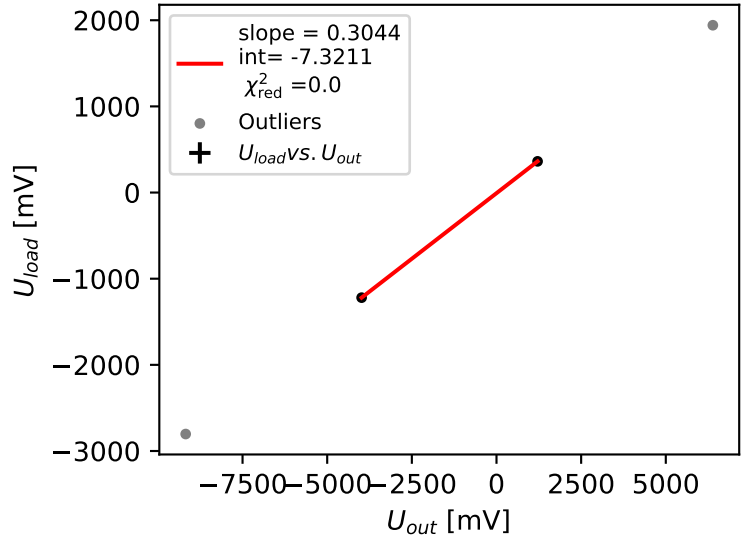
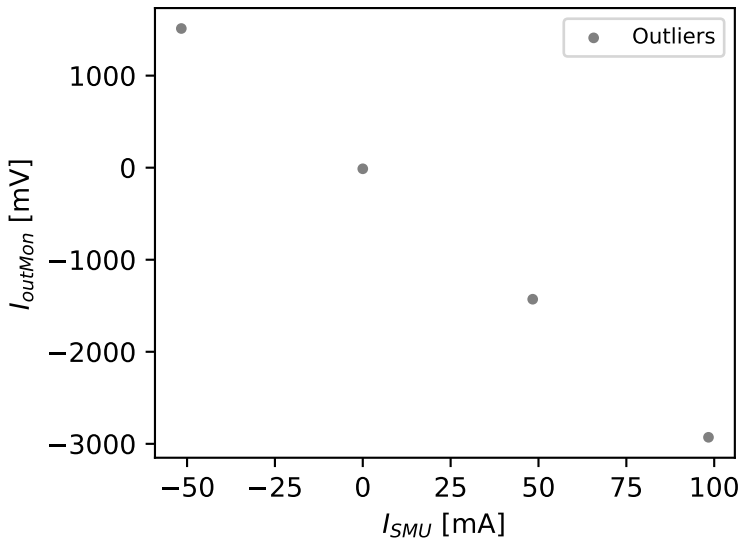
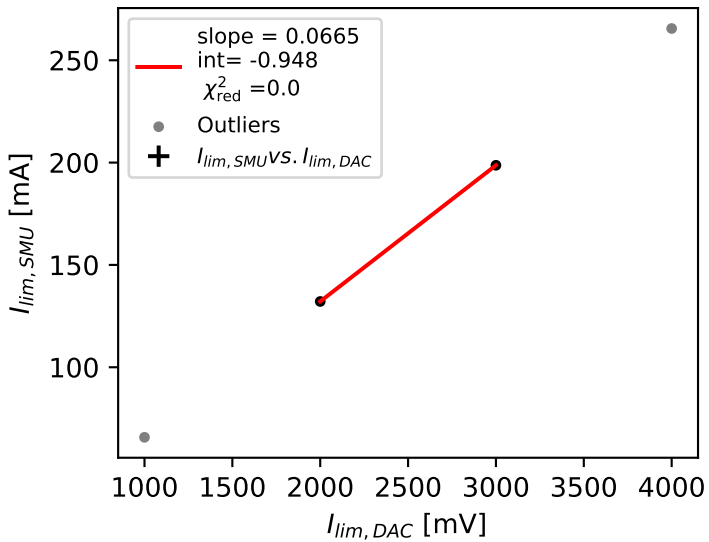




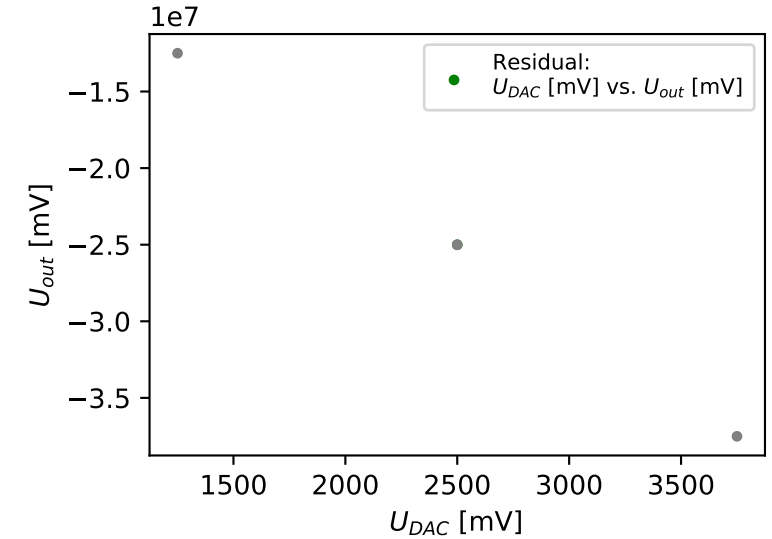


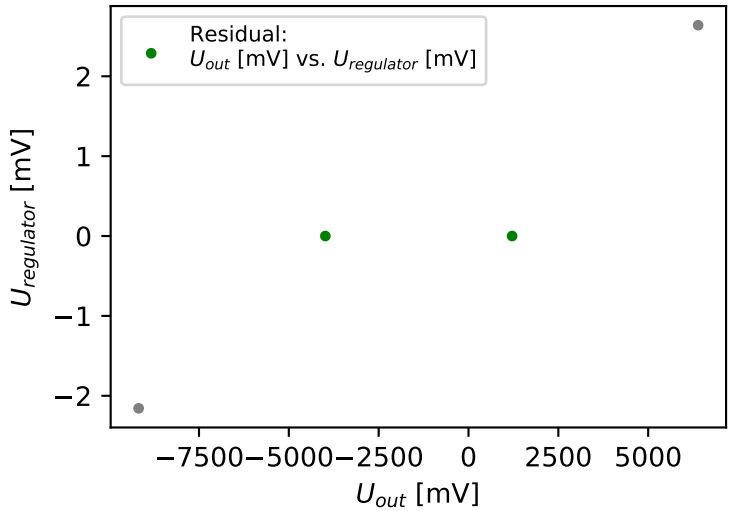


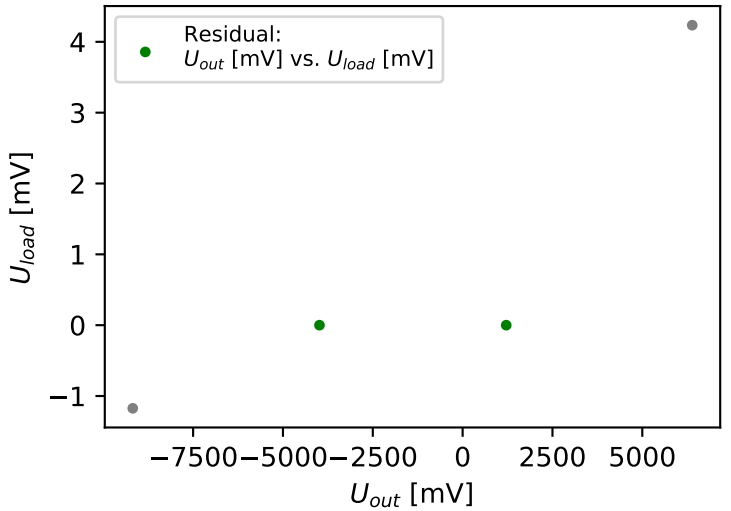


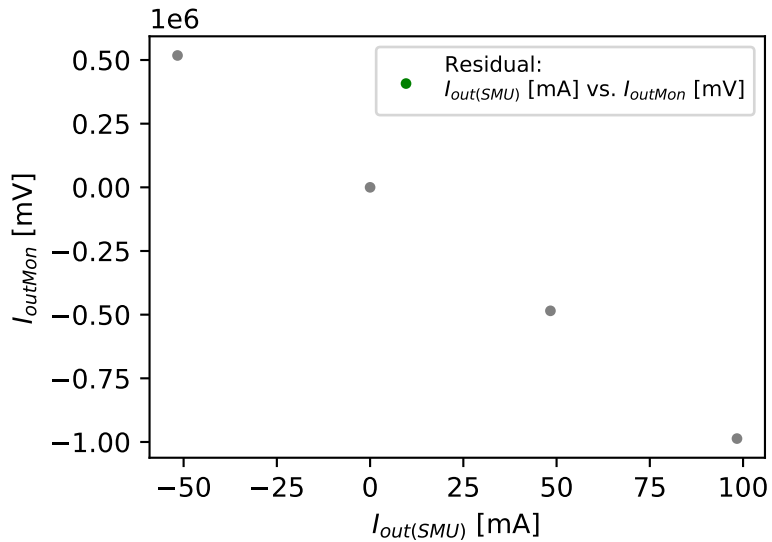
Number of deleted points :

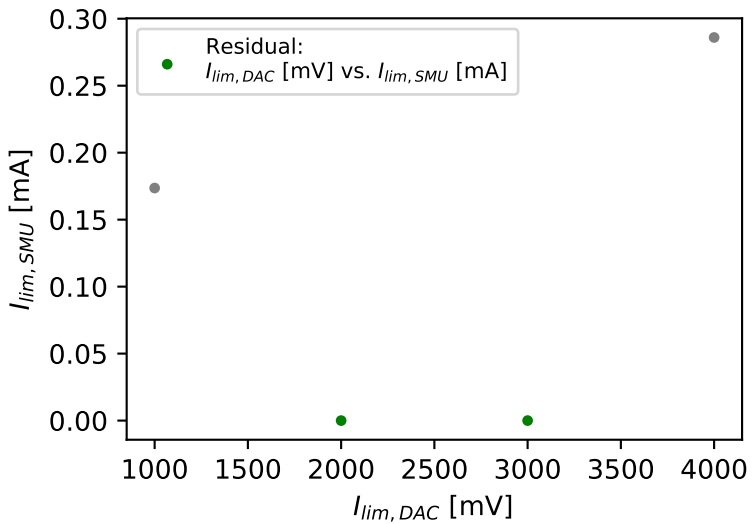
0) U_{out} vs. U_{DAC} : **3**
1) $U_{regulator}$ vs. U_{out} : **2**
2) U_{load} vs. U_{out}: **2**
3) I_{outMON} vs. I_{SMU}: **4**
4) $I_{lim,SMU}$ vs. $I_{lim,DAC}$: **2**

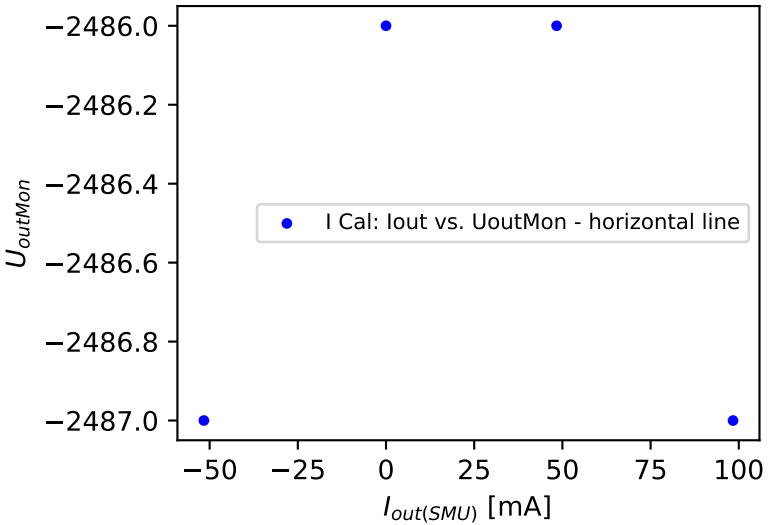

1e7
Residual:
U_{DAC} [mV] vs. U_{out} [mV]
U_{out} [mV]
U_{DAC} [mV]

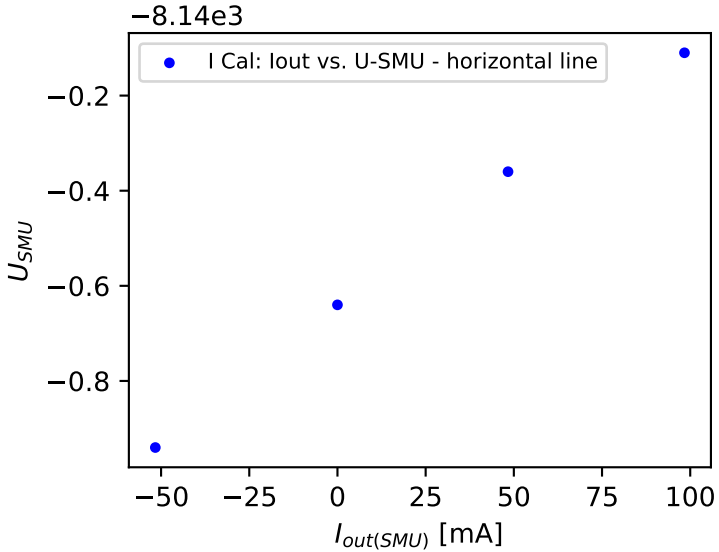

Residual:
U_{out} [mV] vs. $U_{regulator}$ [mV]
$U_{regulator}$ [mV]
U_{out} [mV]

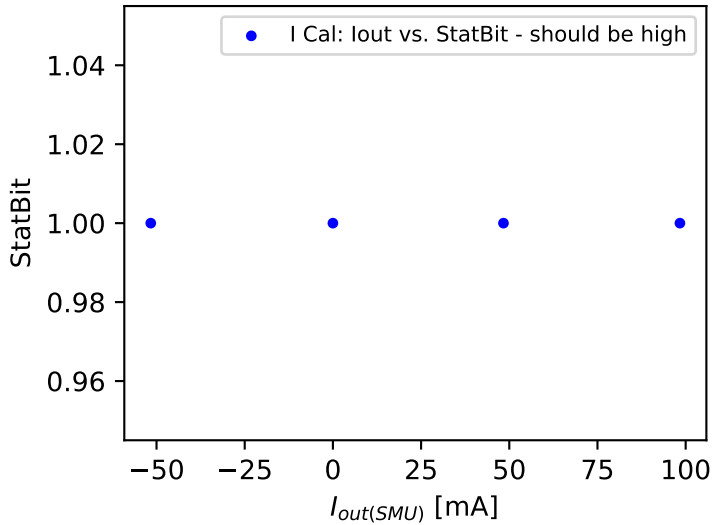

Residual:
U_{out} [mV] vs. U_{load} [mV]
U_{load} [mV]
U_{out} [mV]

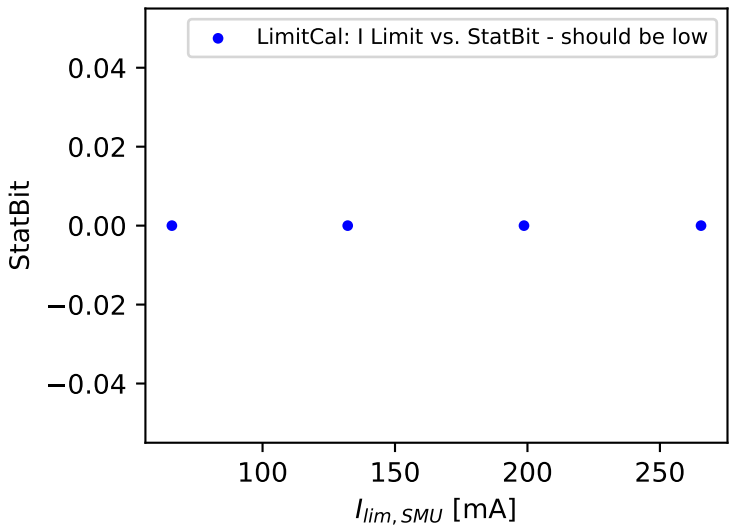

1e6
Residual:
$I_{out(SMU)}$ [mA] vs. I_{outMon} [mV]
I_{outMon} [mV]
$I_{out(SMU)}$ [mA]


Residual:
$I_{lim,DAC}$ [mV] vs. $I_{lim,SMU}$ [mA]
$I_{lim,SMU}$ [mA]
$I_{lim,DAC}$ [mV]


I Cal: Iout vs. UoutMon - horizontal line
U_{outMon}
$I_{out(SMU)}$ [mA]


−8.14e3
I Cal: Iout vs. U-SMU - horizontal line
U_{SMU}
$I_{out(SMU)}$ [mA]


I Cal: Iout vs. StatBit - should be high
StatBit
$I_{out(SMU)}$ [mA]


LimitCal: I Limit vs. StatBit - should be low
StatBit
$I_{lim,SMU}$ [mA]