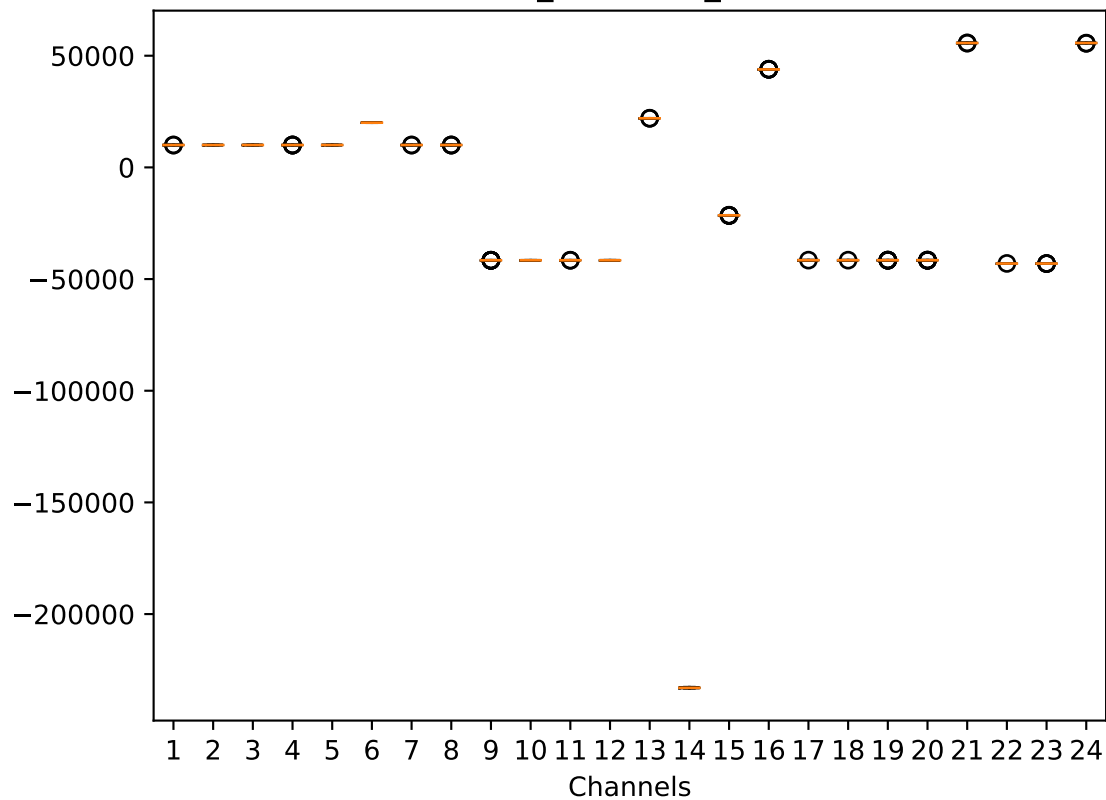
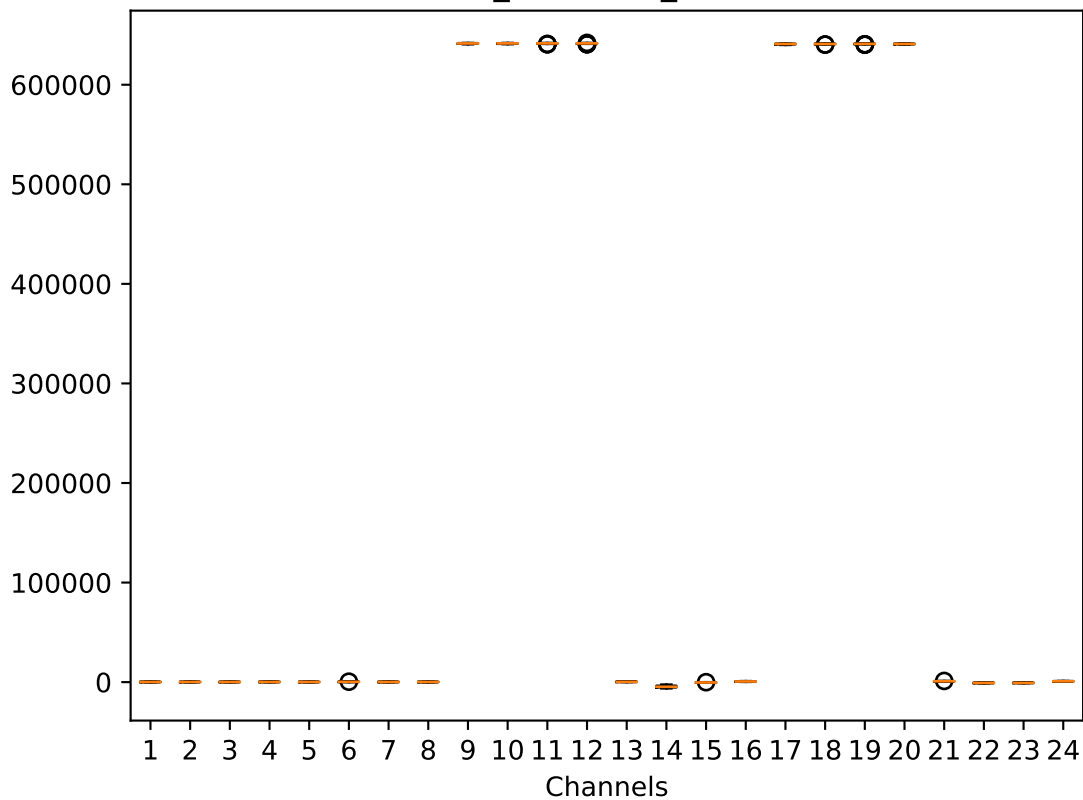


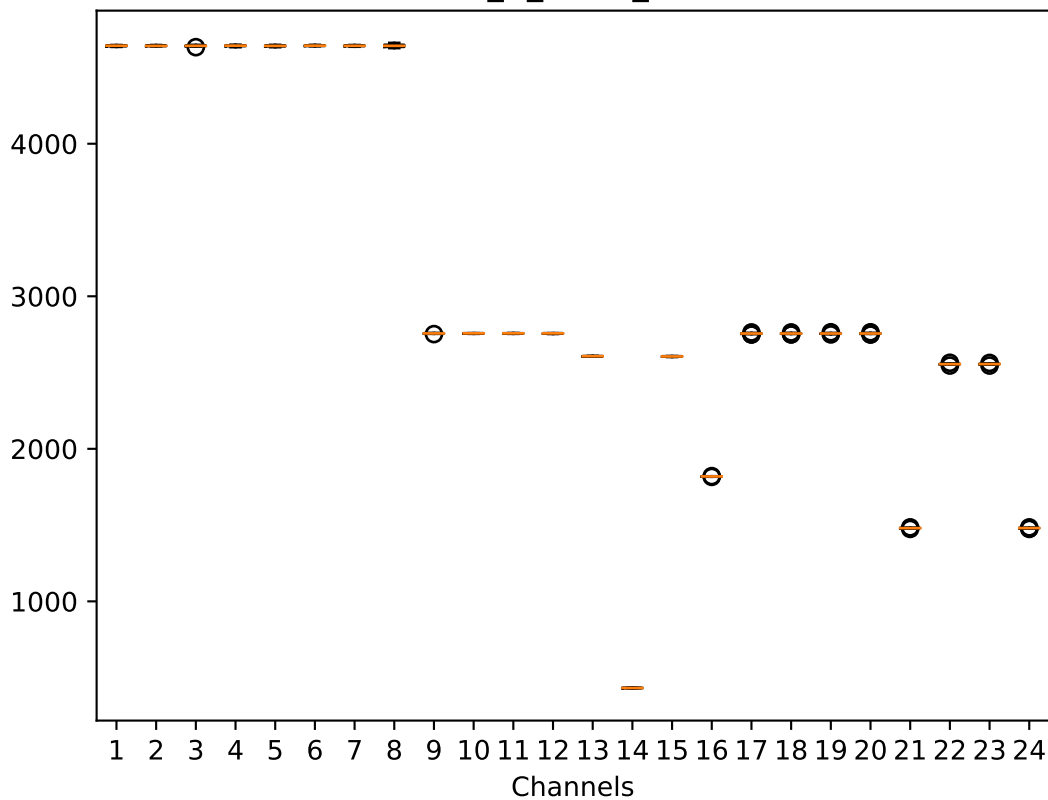
# DAC\_VOLTAGE\_GAIN



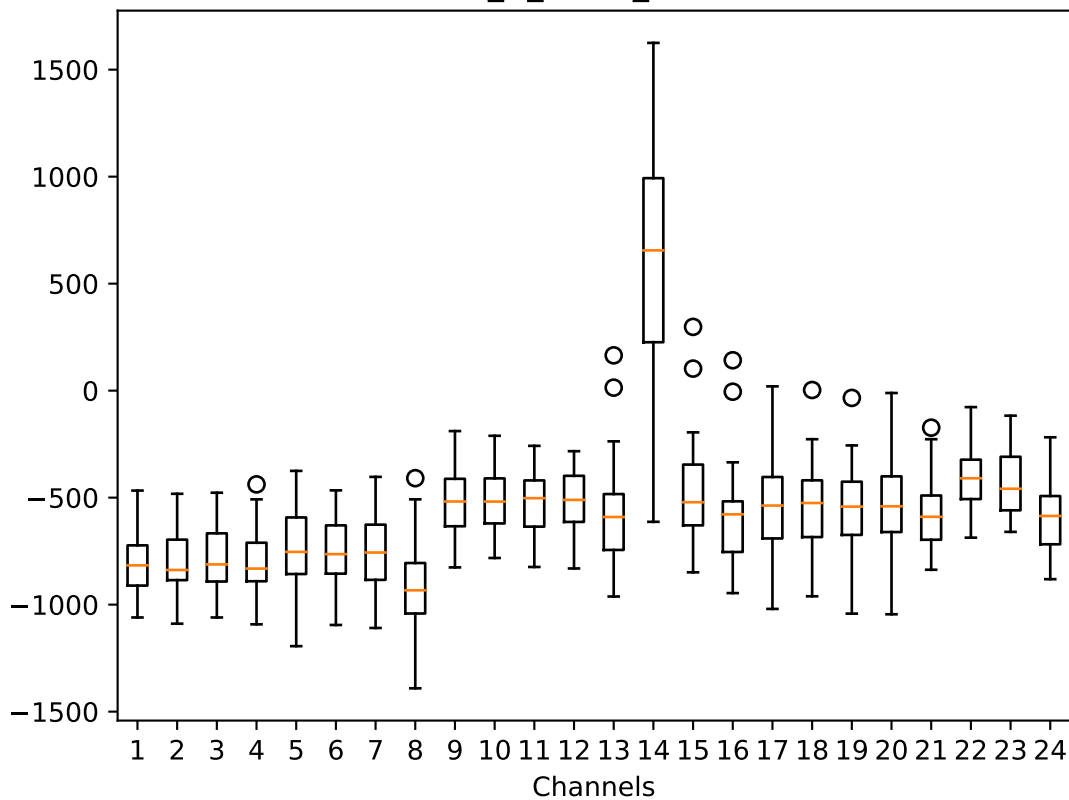
DAC\_VOLTAGE\_OFFSET



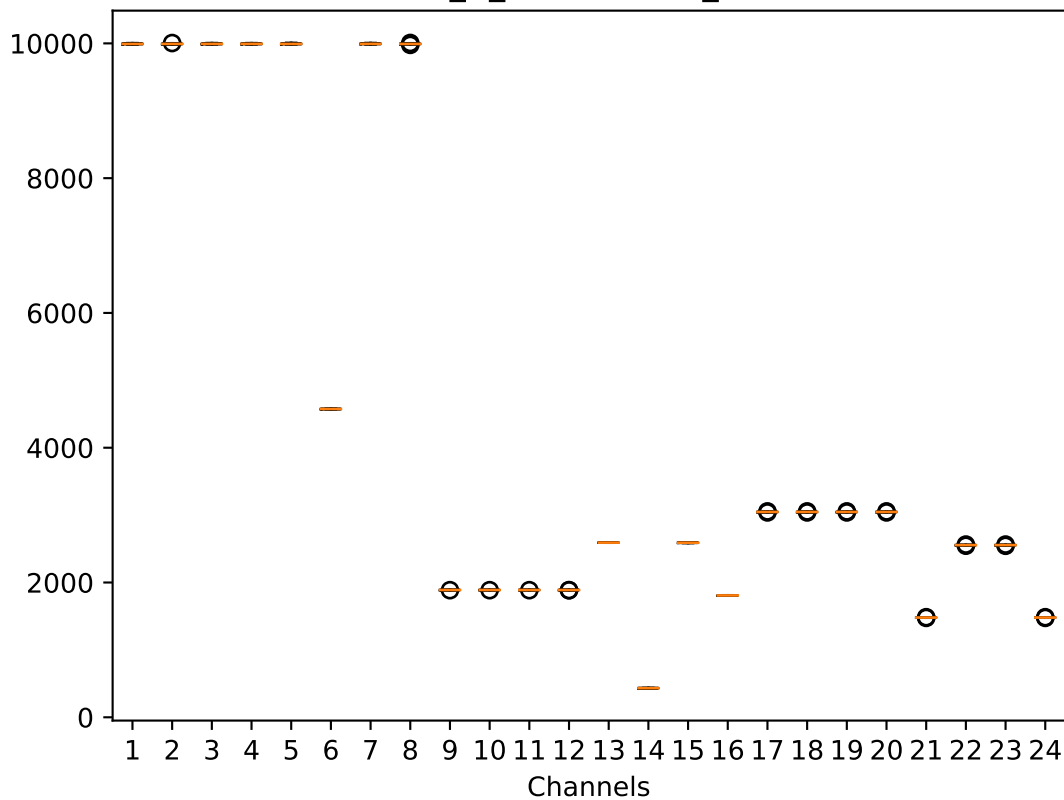
# ADC\_U\_LOAD\_GAIN



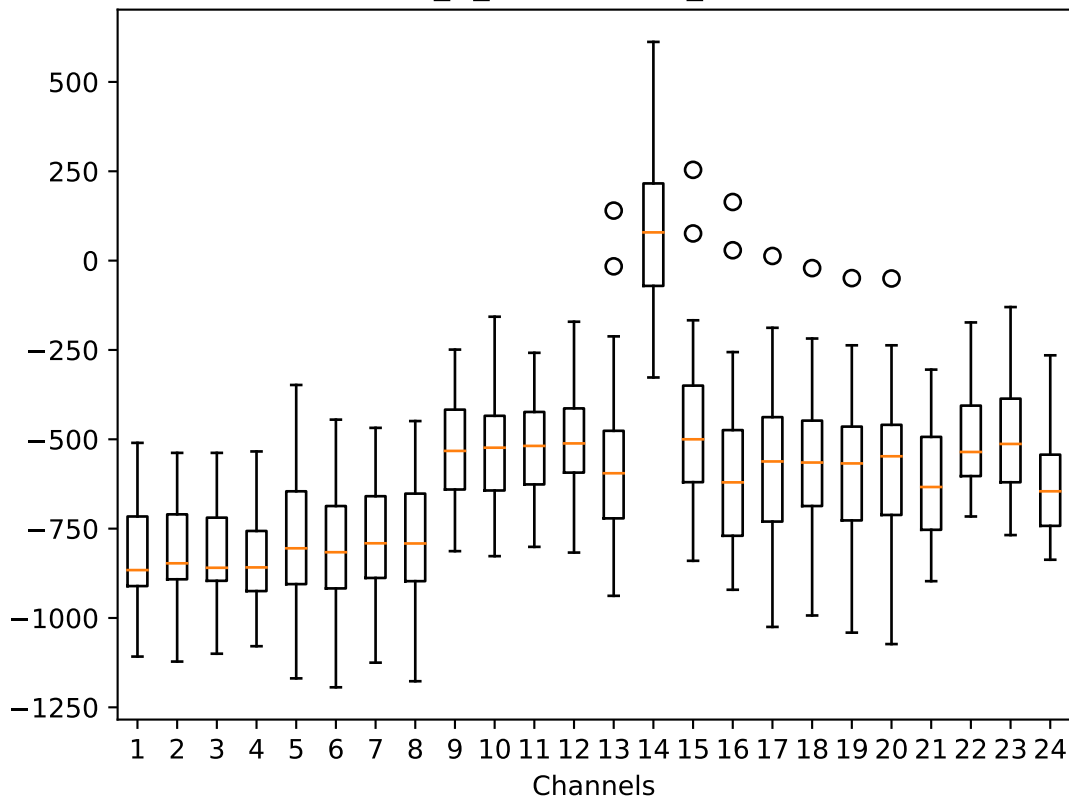
ADC\_U\_LOAD\_OFFSET



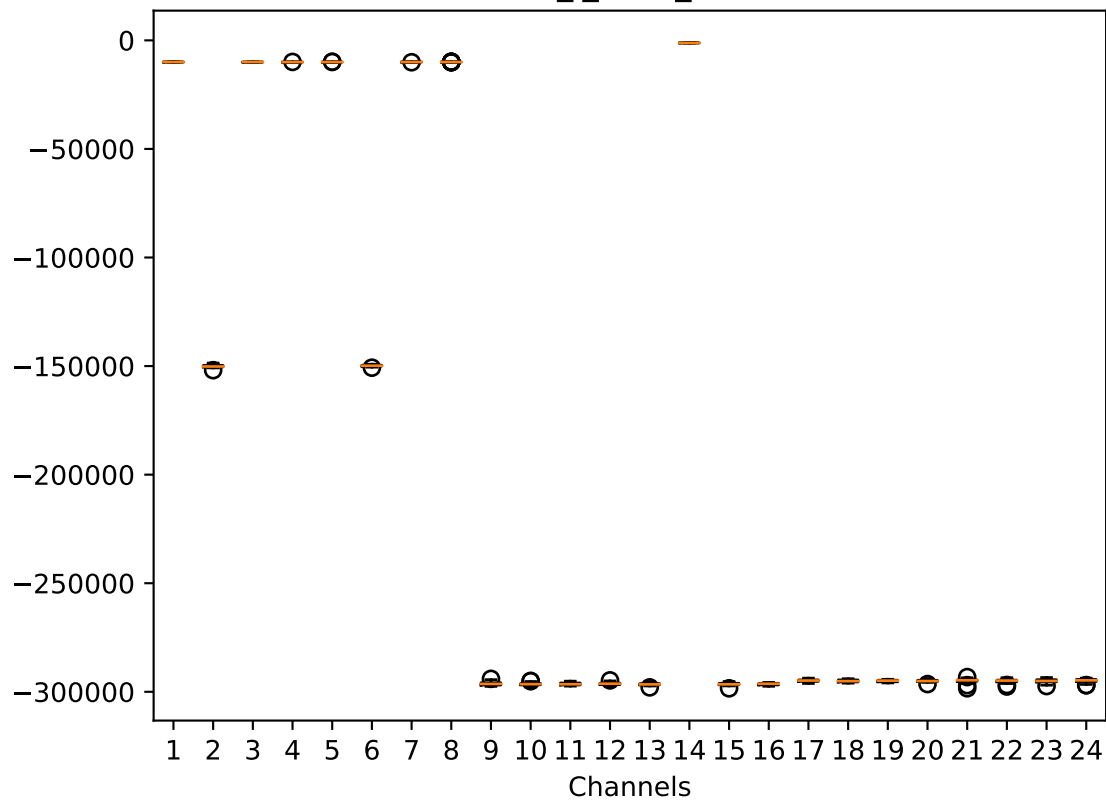
# ADC\_U\_REGULATOR\_GAIN



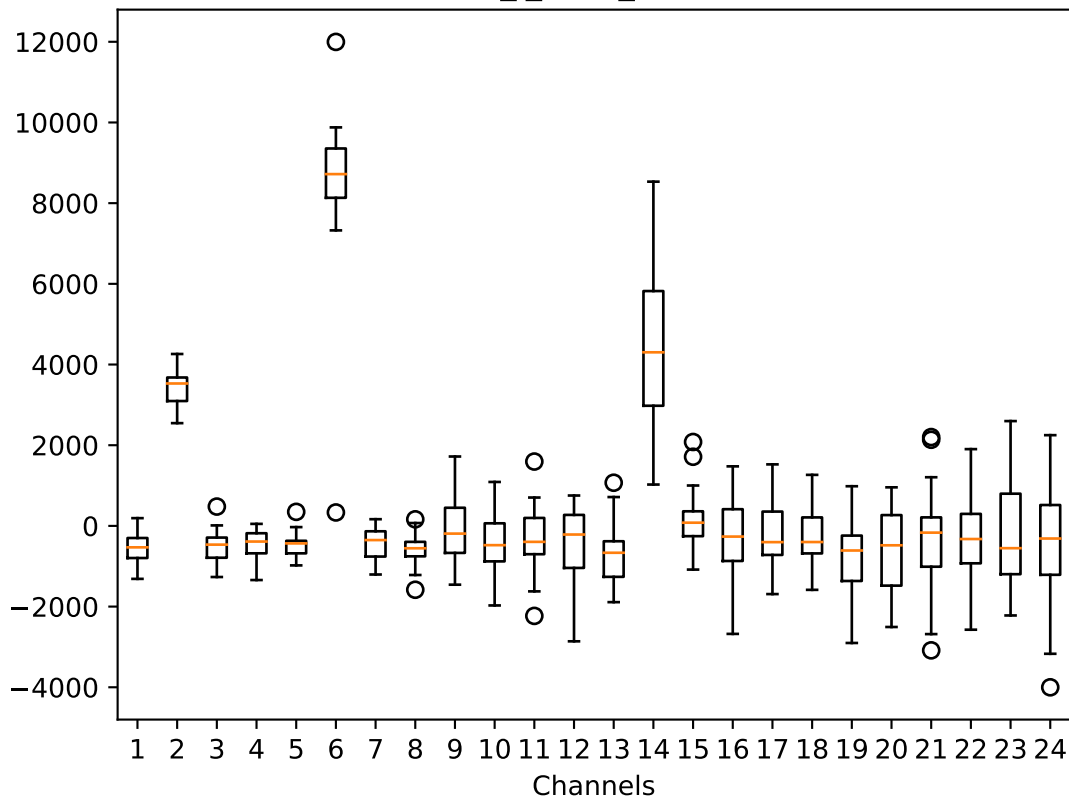
## ADC U REGULATOR OFFSET



# ADC\_I\_MON\_GAIN

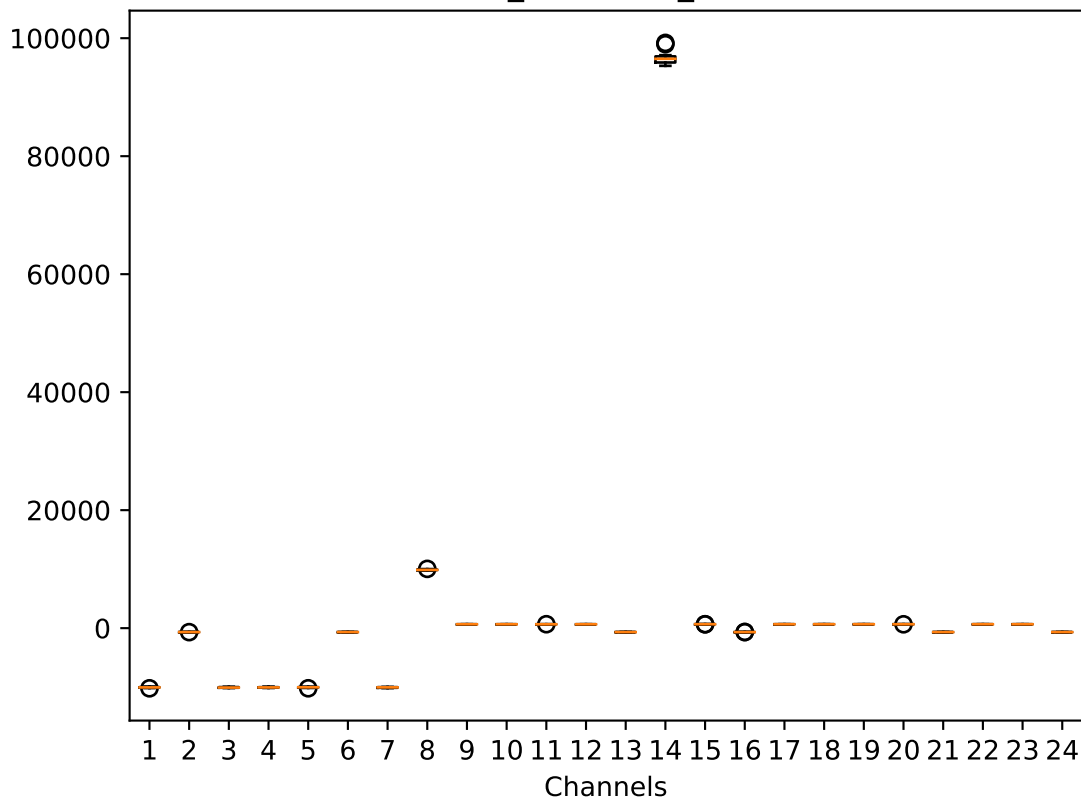


ADC\_I\_MON\_OFFSET





DAC\_CURRENT\_GAIN



# DAC\_CURRENT\_OFFSET

