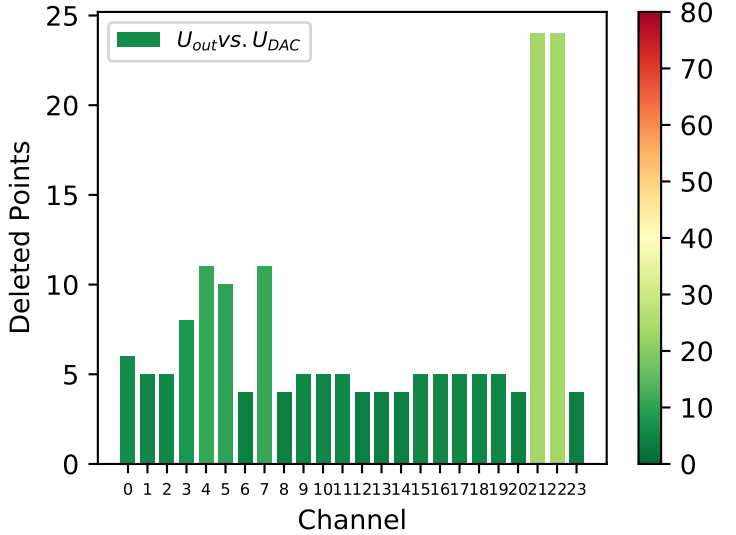
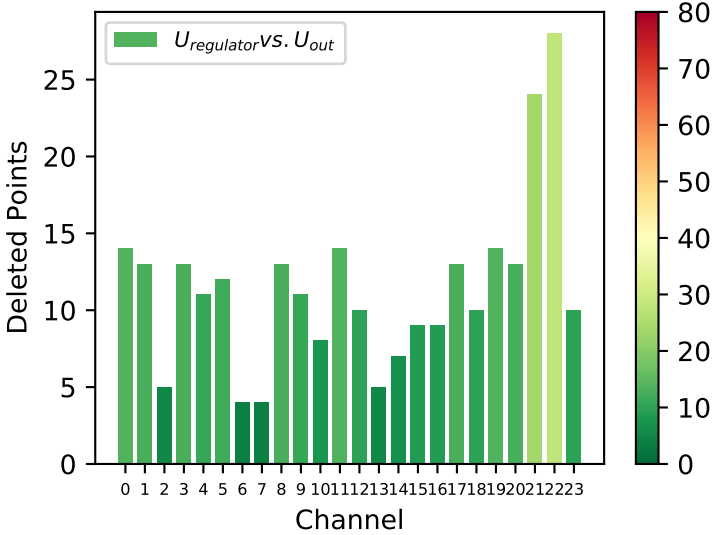
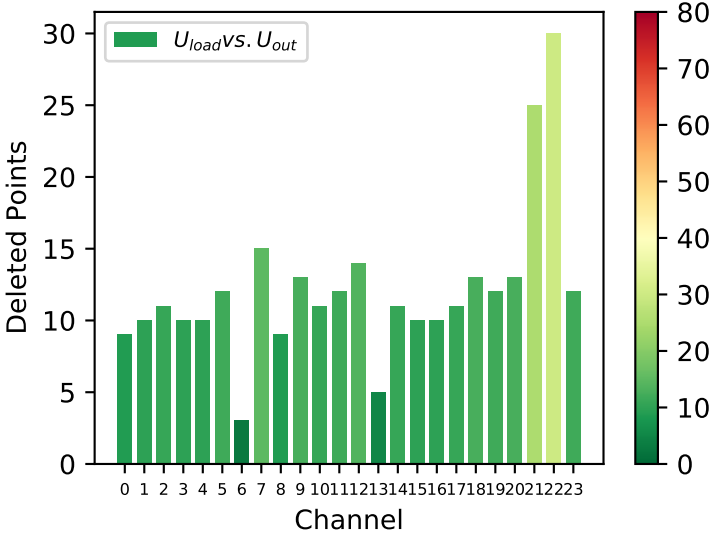
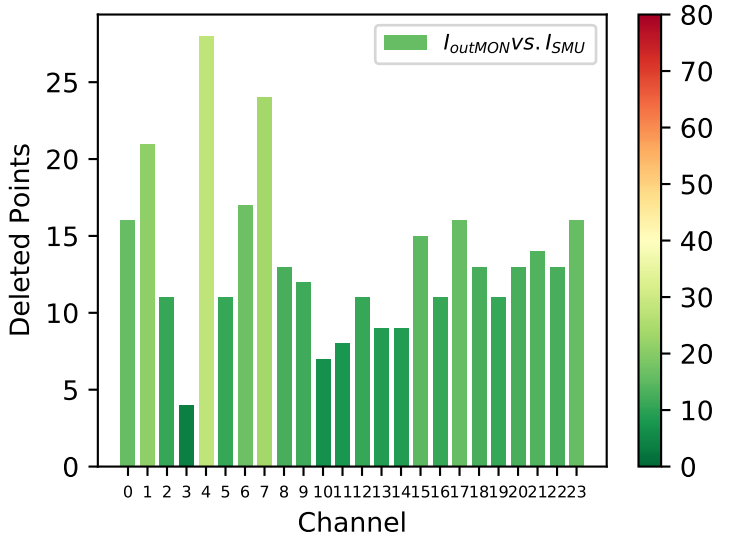
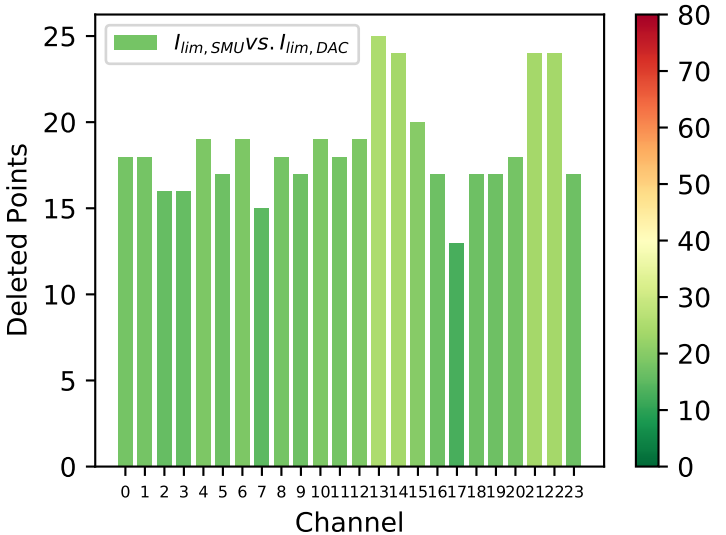

U_{out} vs. U_{DAC}
Deleted Points
Channel


$U_{regulator}$ vs. U_{out}
Deleted Points
Channel


U_{load} vs. U_{out}
Deleted Points
Channel


I_{outMON} vs. I_{SMU}
Deleted Points
Channel


$I_{lim,SMU}$ vs. $I_{lim,DAC}$
Deleted Points
Channel