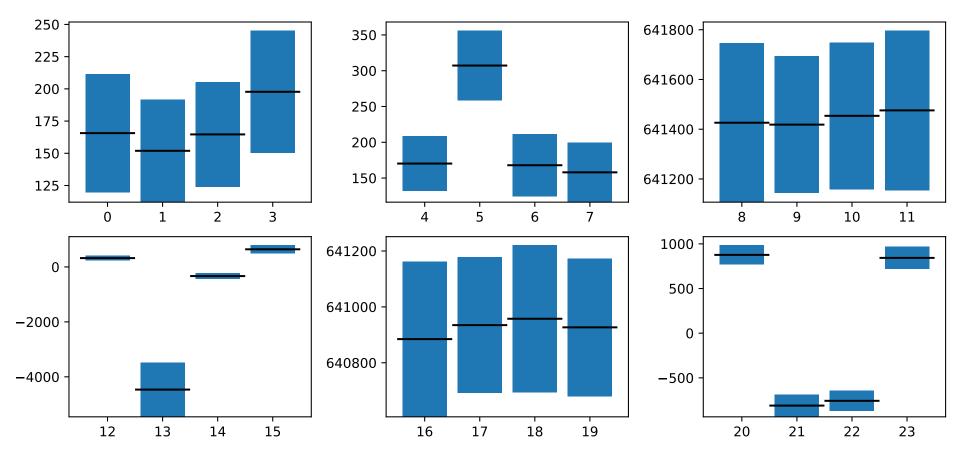
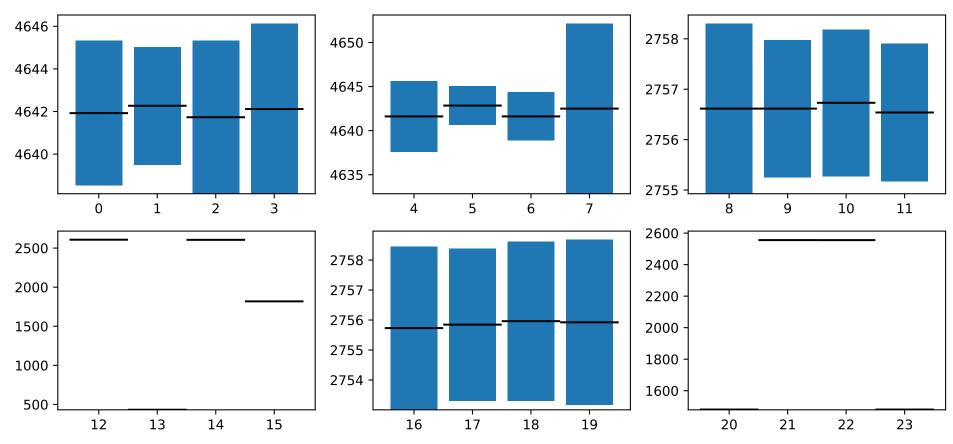
DAC_VOLTAGE_GAIN +1.001e4 20000 --41580 · 8 18000 --41585 -16000 -6 --41590 -14000 -**-**41595 -4 -12000 --41600 -2 --41605 -2 7 3 5 6 8 10 11 0 9 60000 50000 0 -40000 --41560 --50000 -20000 -41570 - -1000000 --150000 -**-**41580 --20000 --200000 --40000 -22 12 13 15 16 19 21 23 14 17 18 20

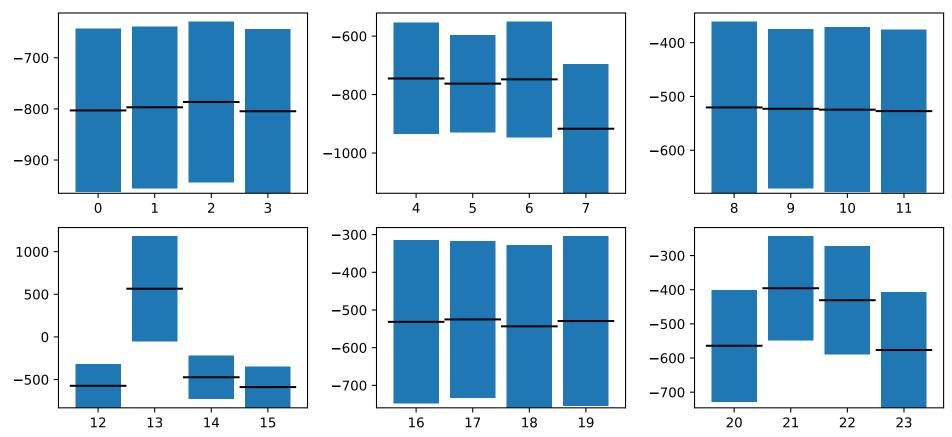
DAC_VOLTAGE_OFFSET



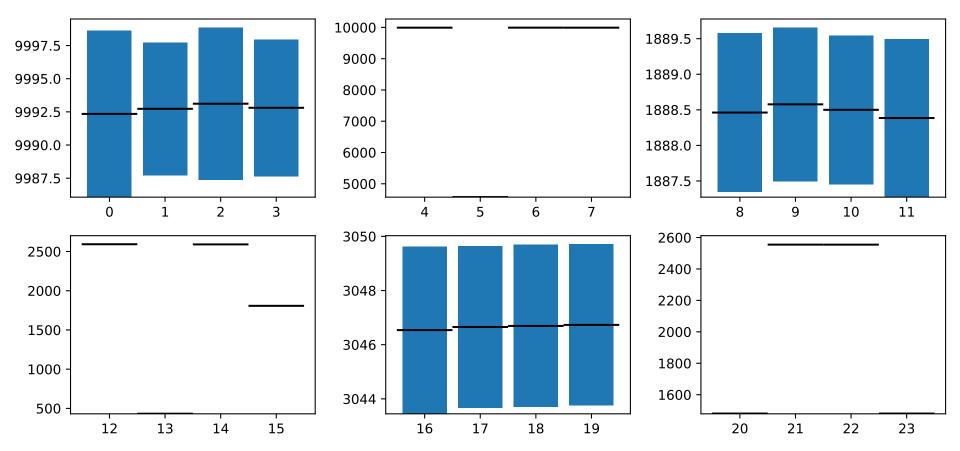
ADC_U_LOAD_GAIN



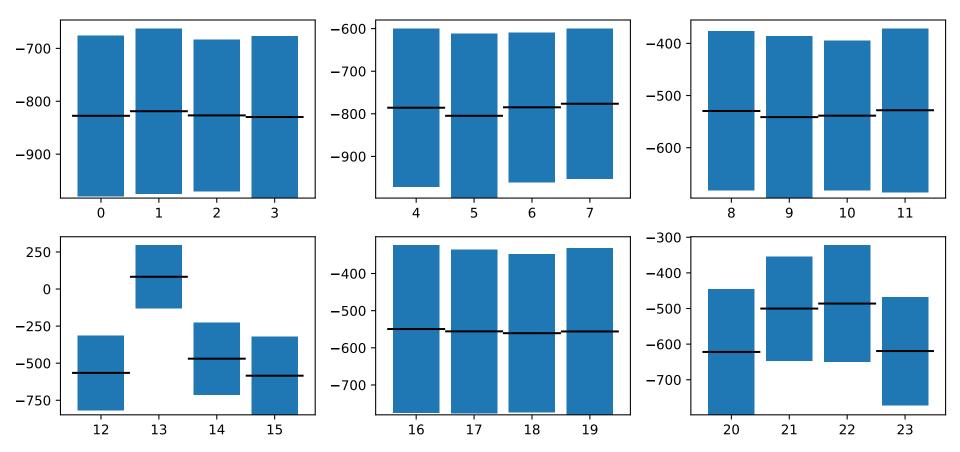
ADC_U_LOAD_OFFSET



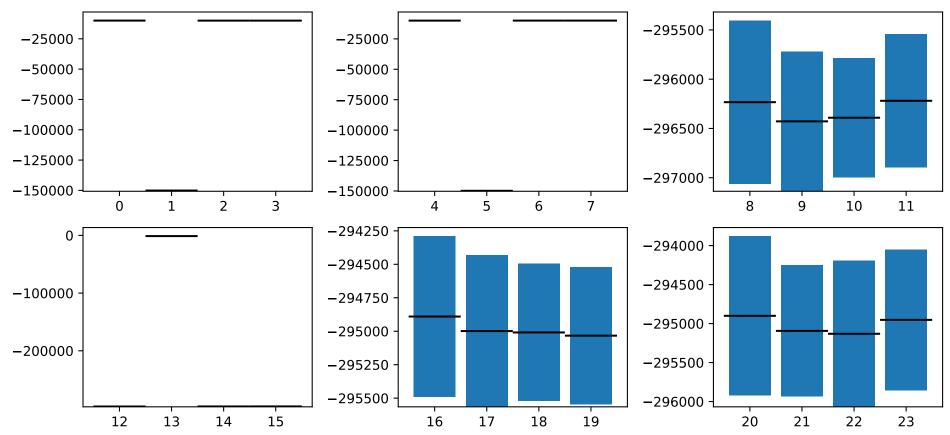
${\sf ADC_U_REGULATOR_GAIN}$



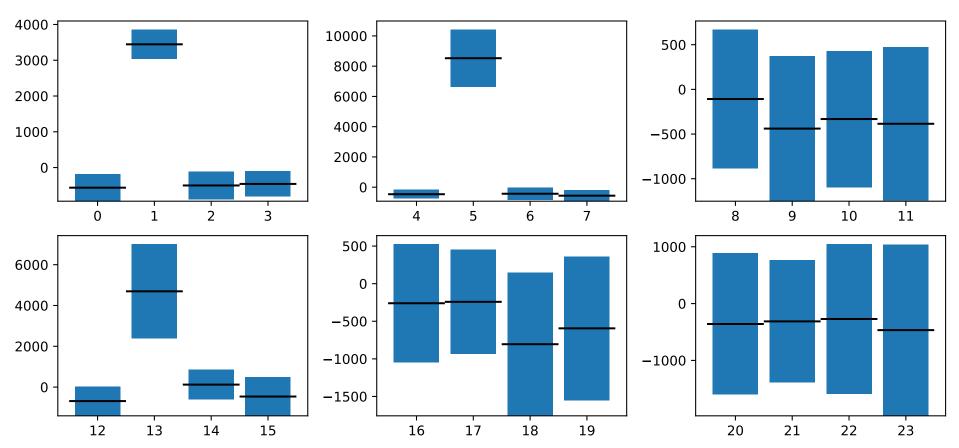
${\tt ADC_U_REGULATOR_OFFSET}$



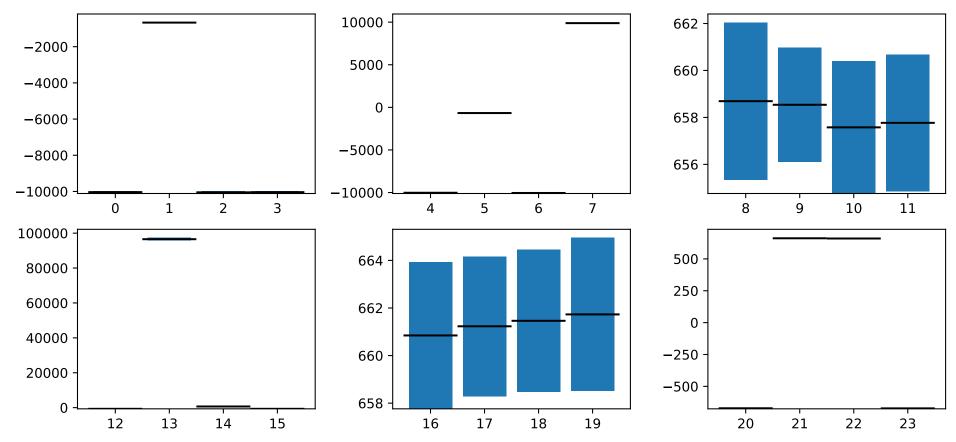
ADC_I_MON_GAIN



ADC_I_MON_OFFSET



DAC_CURRENT_GAIN



DAC_CURRENT_OFFSET

