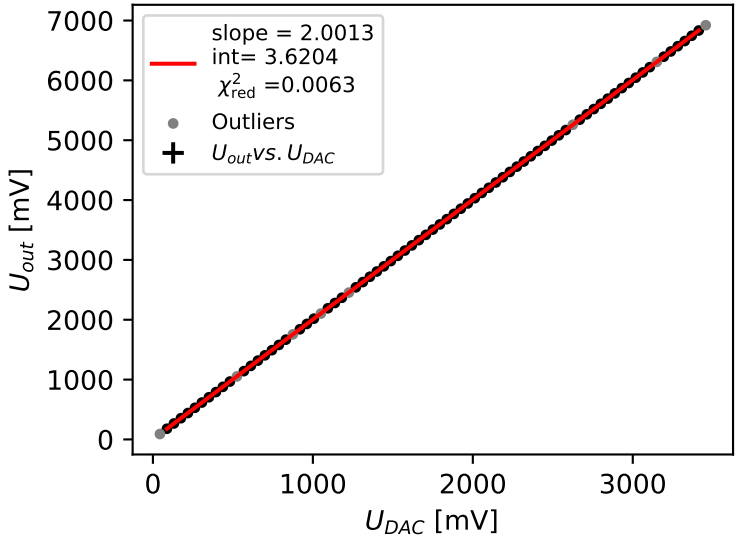
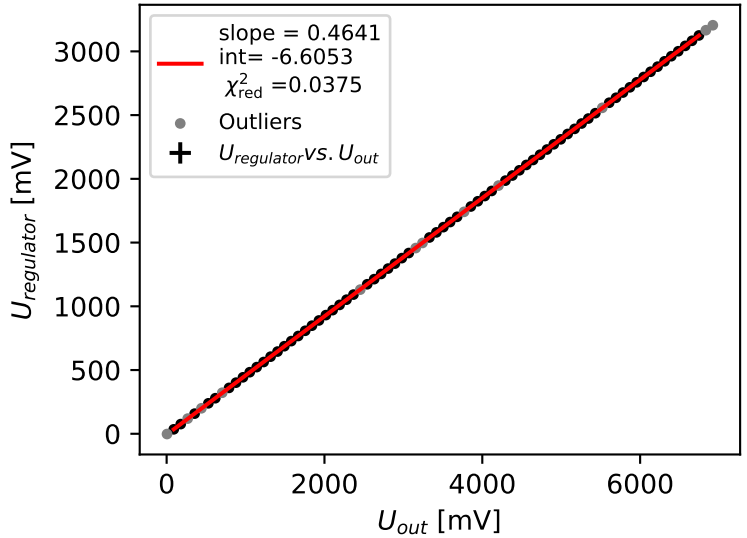
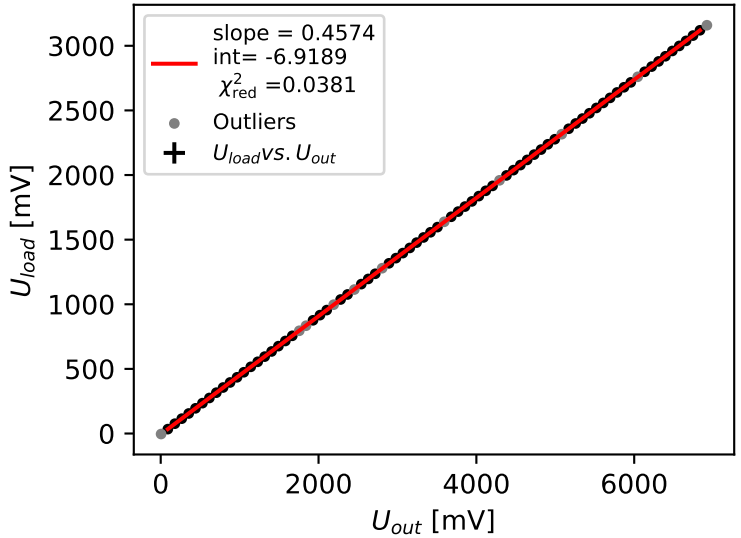
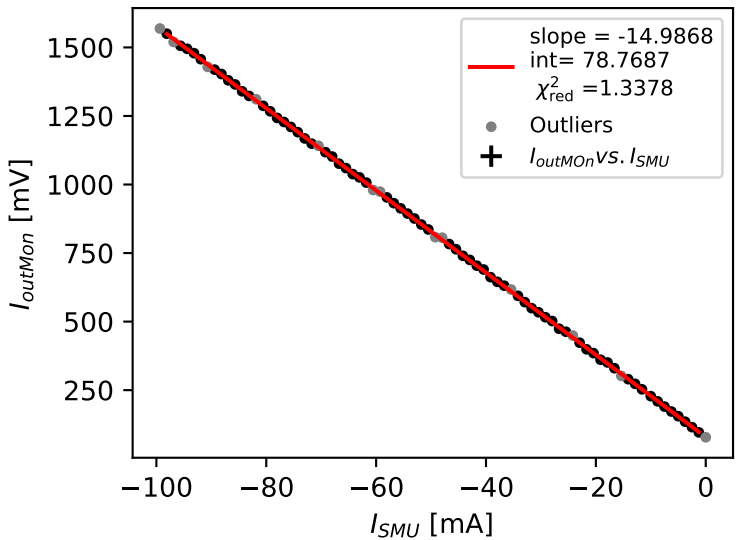
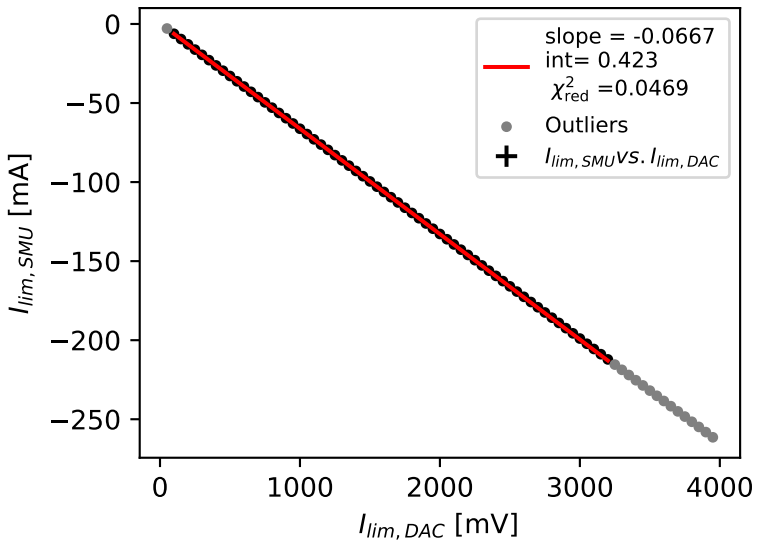




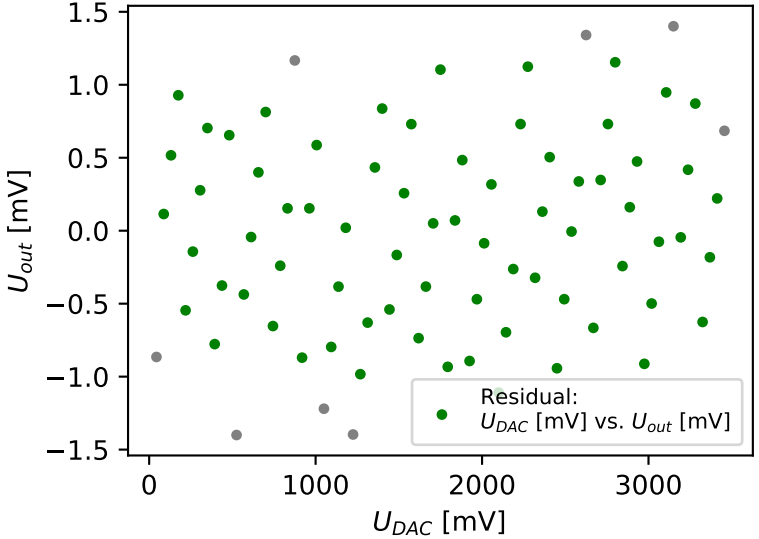


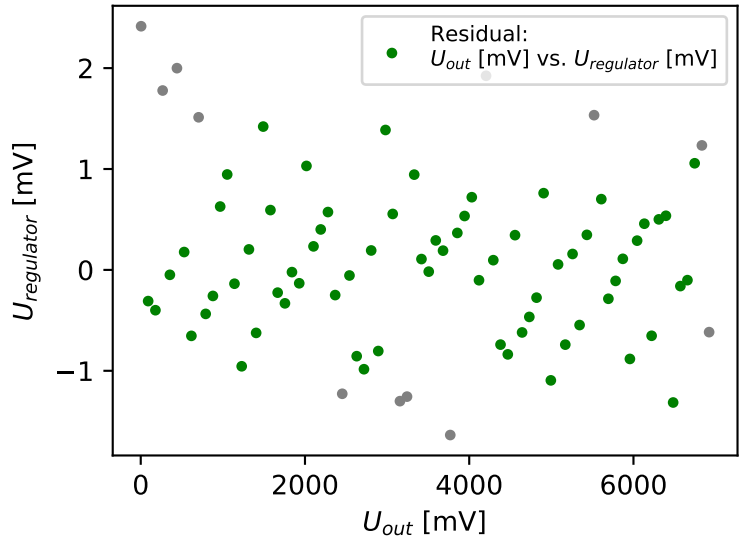


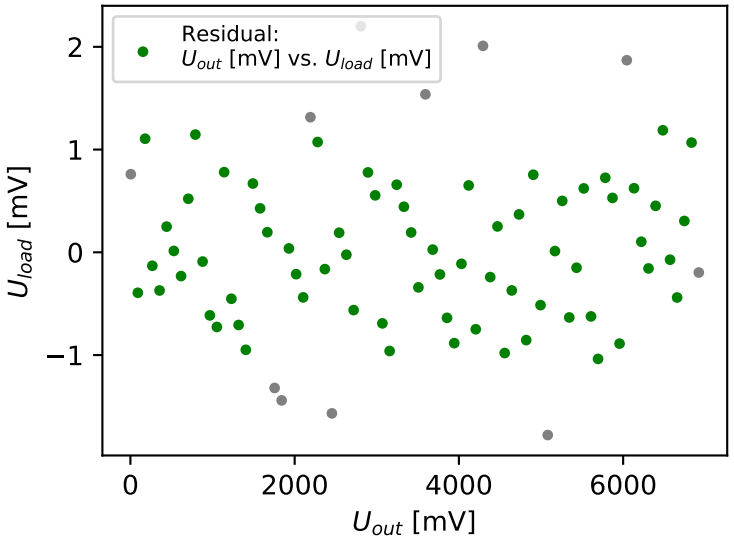


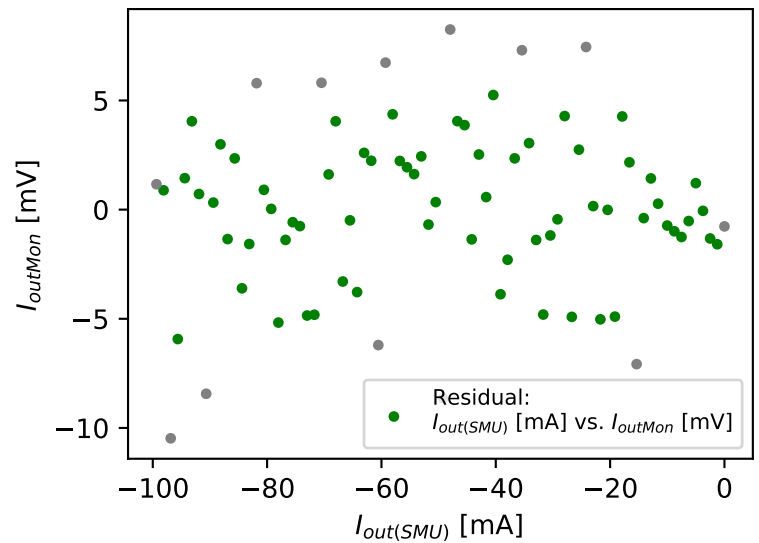
Number of deleted points :

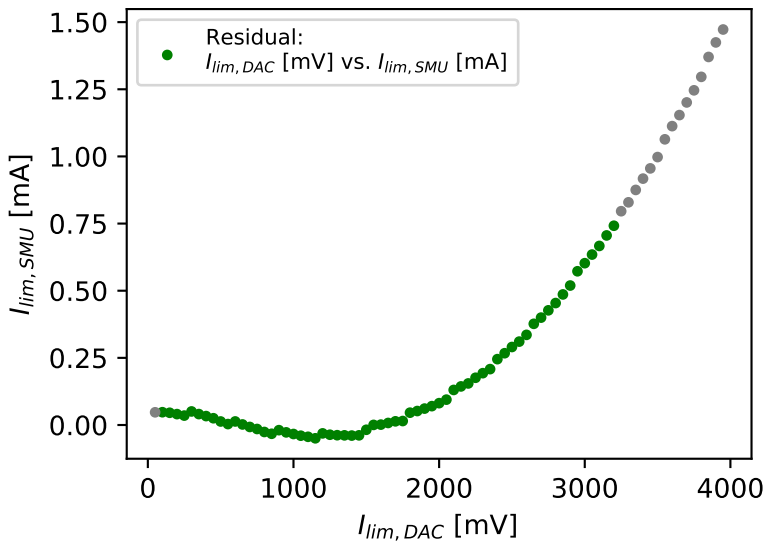
0) U_{out} vs. U_{DAC} : **9**
1) $U_{regulator}$ vs. U_{out} : **12**
2) U_{load} vs. U_{out}: **11**
3) I_{outMON} vs. I_{SMU}: **13**
4) $I_{lim,SMU}$ vs. $I_{lim,DAC}$: **17**

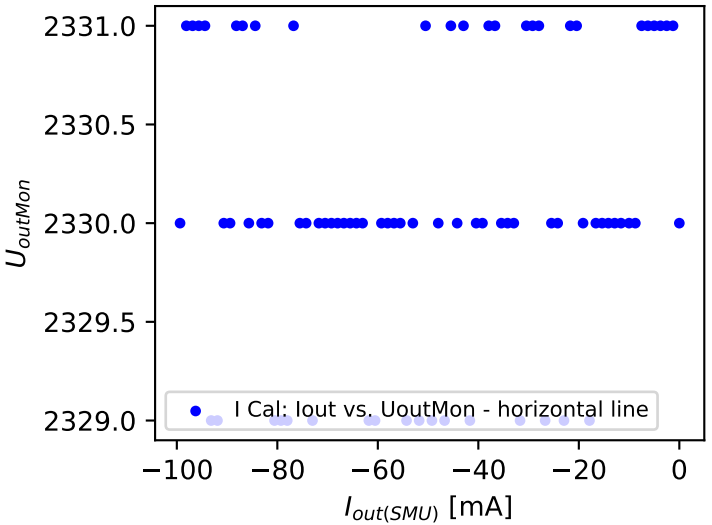

Residual:
U_{DAC} [mV] vs. U_{out} [mV]
U_{out} [mV]
U_{DAC} [mV]

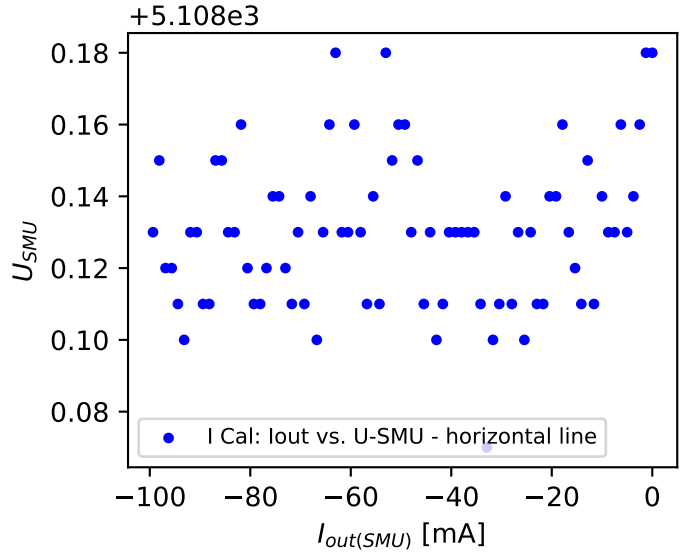

Residual:
U_{out} [mV] vs. $U_{regulator}$ [mV]
$U_{regulator}$ [mV]
U_{out} [mV]

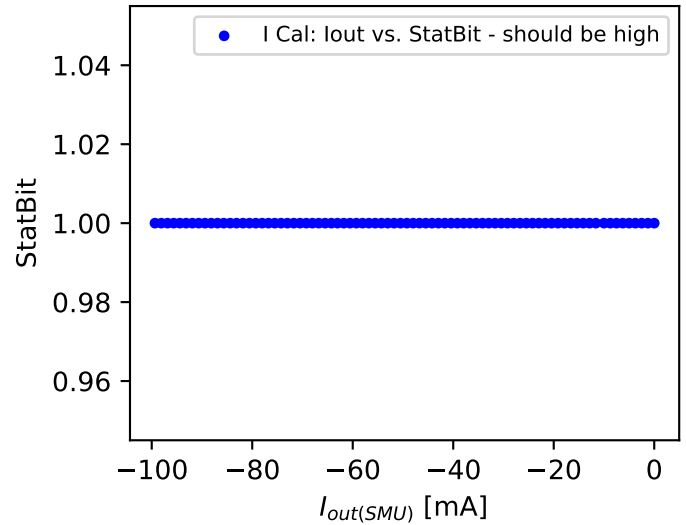

Residual:
U_{out} [mV] vs. U_{load} [mV]
U_{load} [mV]
U_{out} [mV]

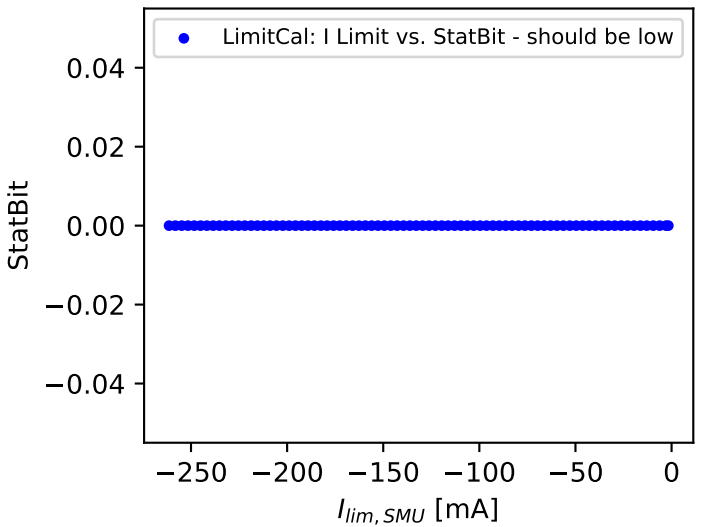

Residual:
$I_{out(SMU)}$ [mA] vs. I_{outMon} [mV]
I_{outMon} [mV]
$I_{out(SMU)}$ [mA]


Residual:
$I_{lim,DAC}$ [mV] vs. $I_{lim,SMU}$ [mA]
$I_{lim,SMU}$ [mA]
$I_{lim,DAC}$ [mV]


2331.0
2330.5
2330.0
2329.5
2329.0
U_{outMon}
I Cal: Iout vs. UoutMon - horizontal line
−100 −80 −60 −40 −20 0
$I_{out(SMU)}$ [mA]


+5.108e3
0.18
0.16
0.14
0.12
0.10
0.08
U_{SMU}
I Cal: Iout vs. U-SMU - horizontal line
−100 −80 −60 −40 −20 0
$I_{out(SMU)}$ [mA]


1.04
1.02
1.00
0.98
0.96
StatBit
I Cal: Iout vs. StatBit - should be high
−100 −80 −60 −40 −20 0
$I_{out(SMU)}$ [mA]


0.04
0.02
0.00
−0.02
−0.04
StatBit
LimitCal: I Limit vs. StatBit - should be low
−250 −200 −150 −100 −50 0
$I_{lim,SMU}$ [mA]