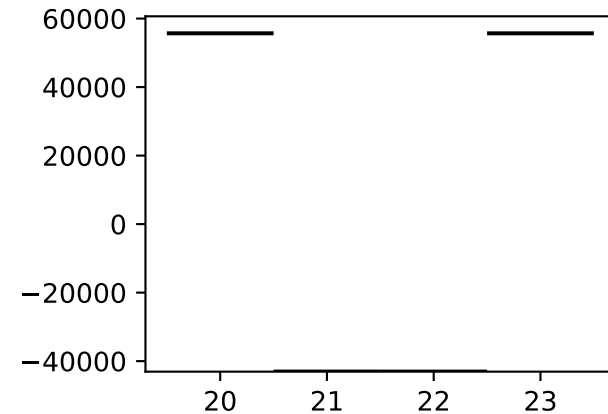
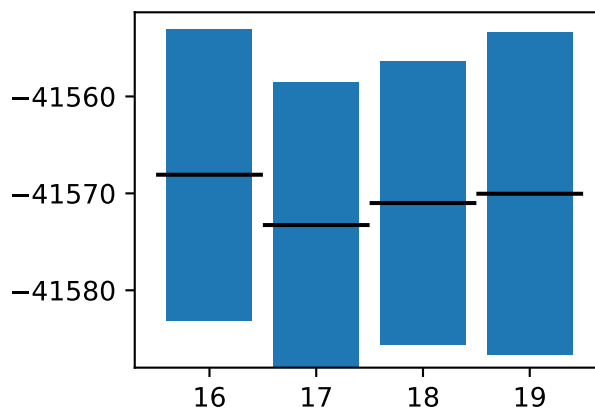
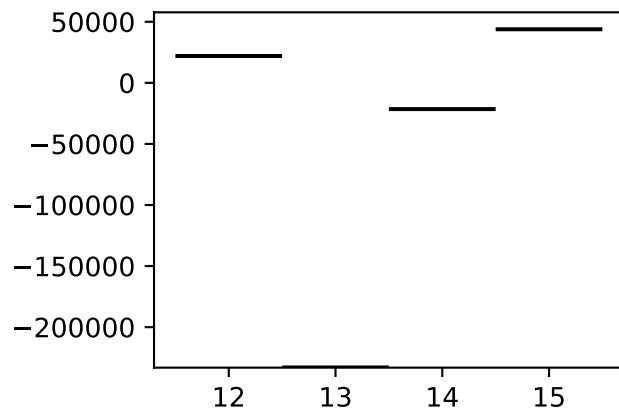
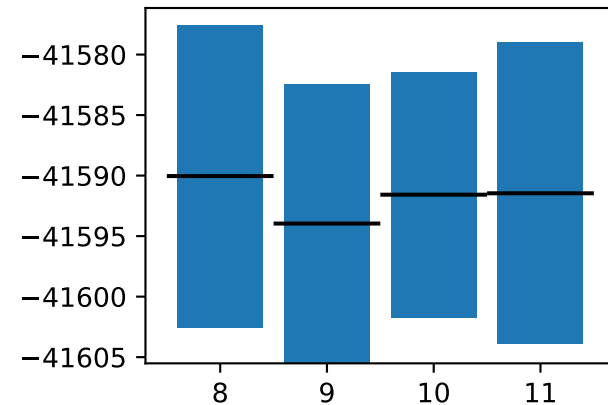
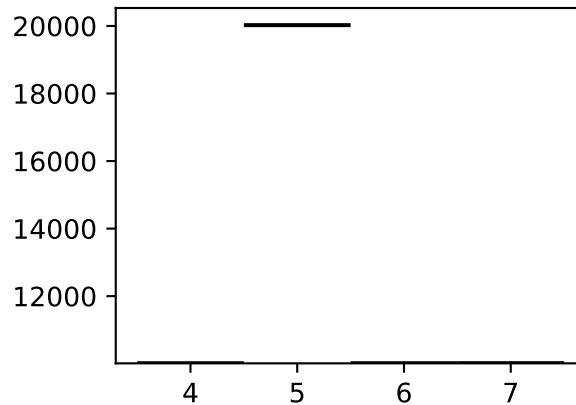
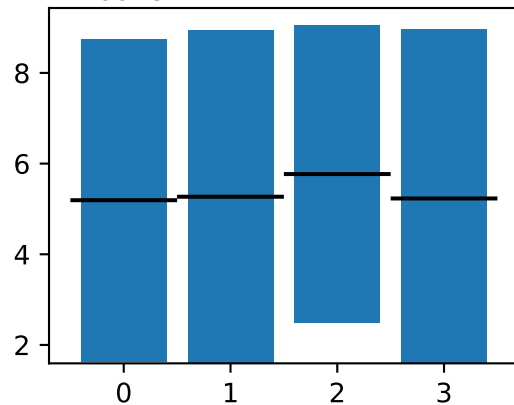
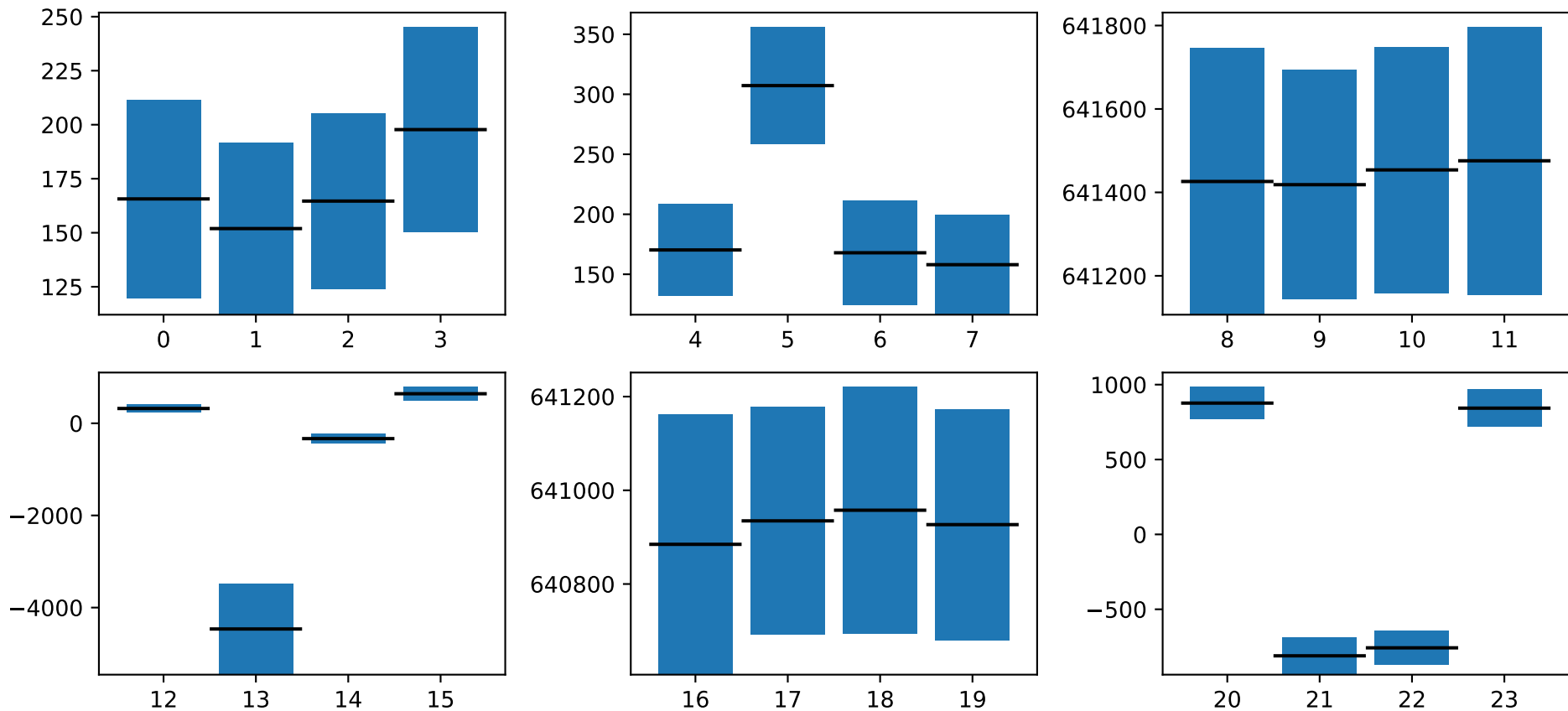


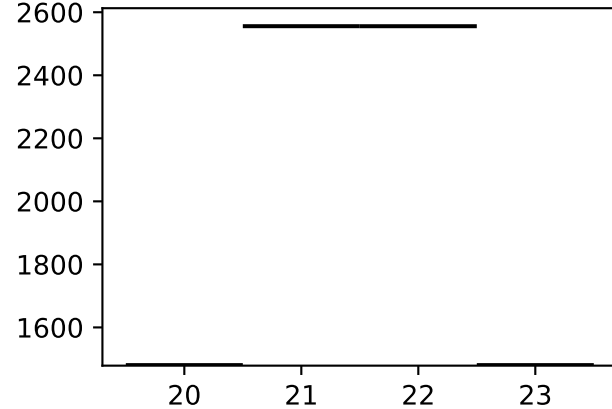
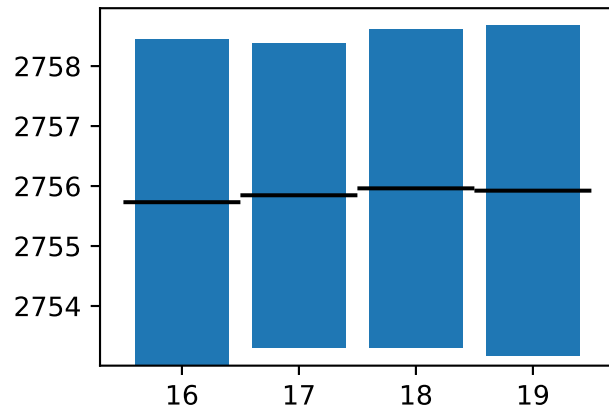
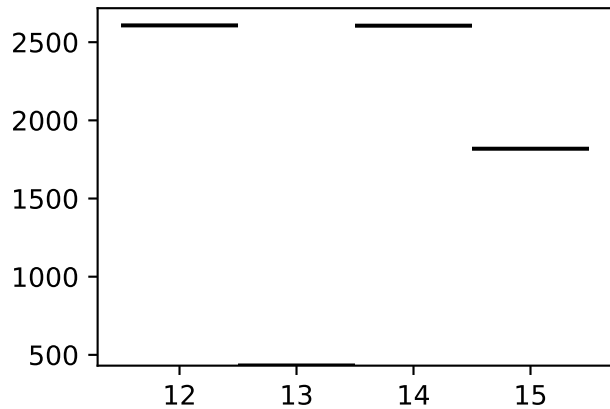
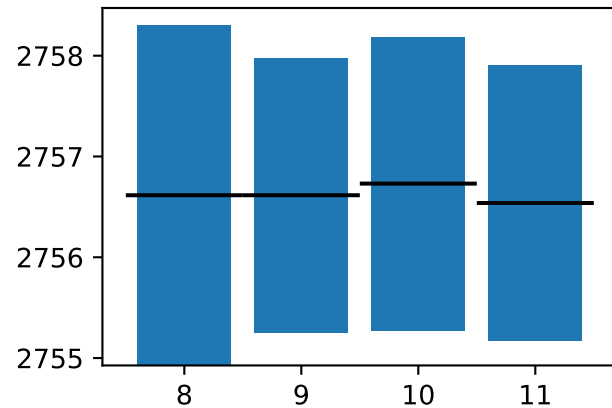
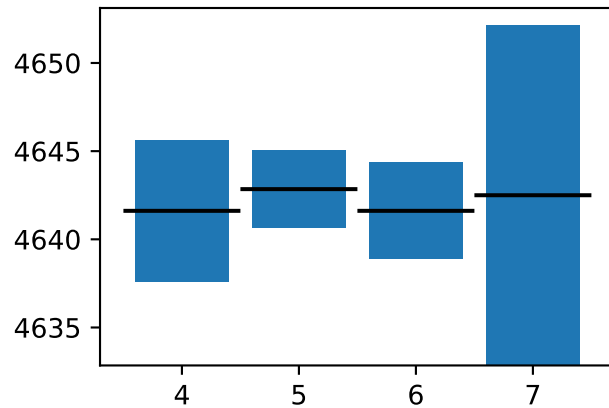
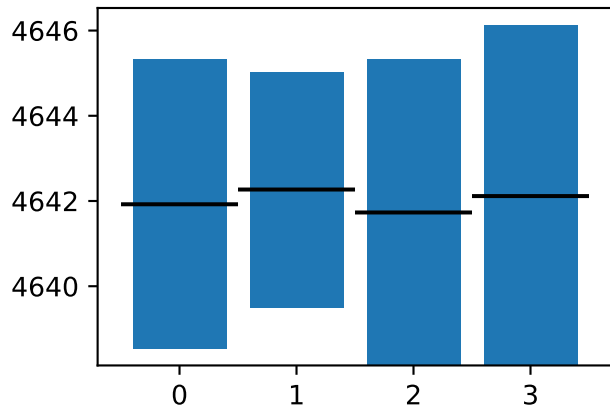
DAC_VOLTAGE_GAIN

 $+1.001e4$ 

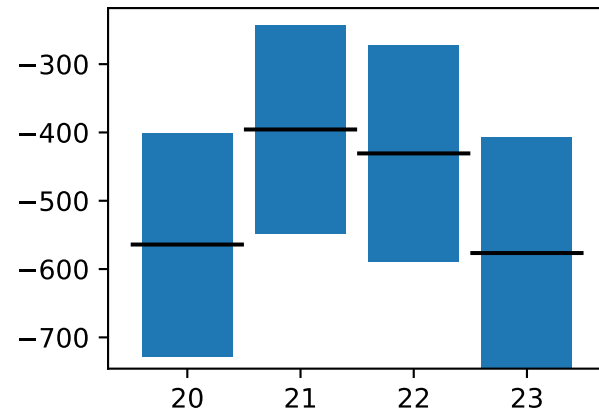
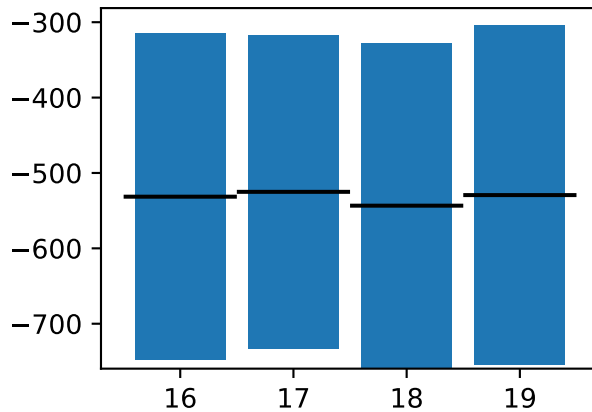
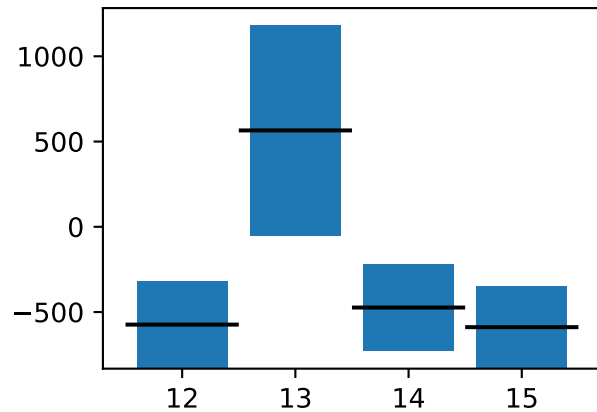
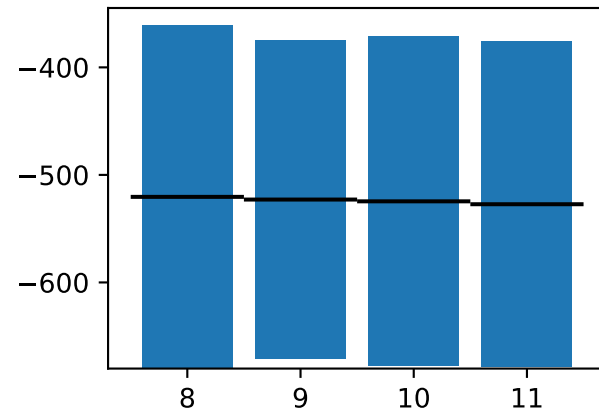
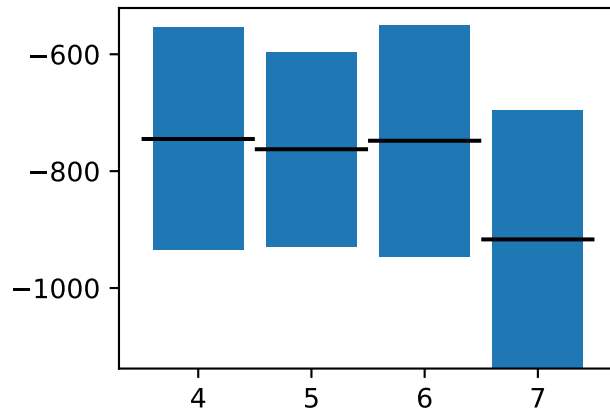
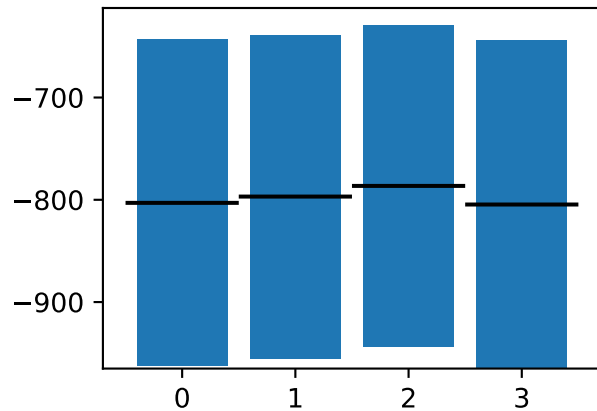
DAC_VOLTAGE_OFFSET



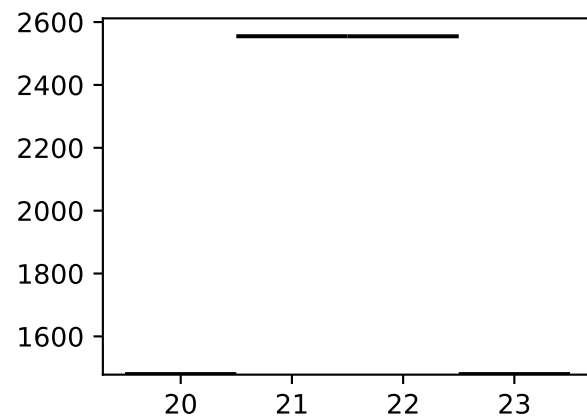
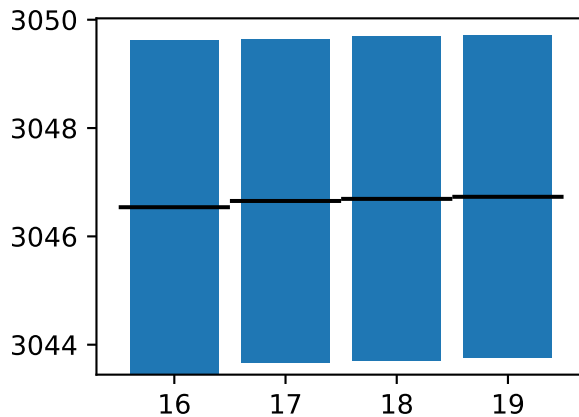
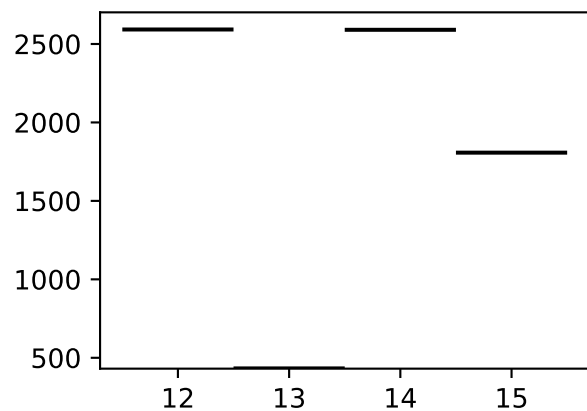
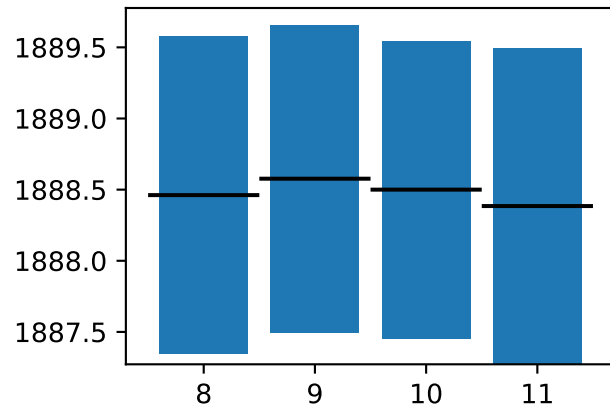
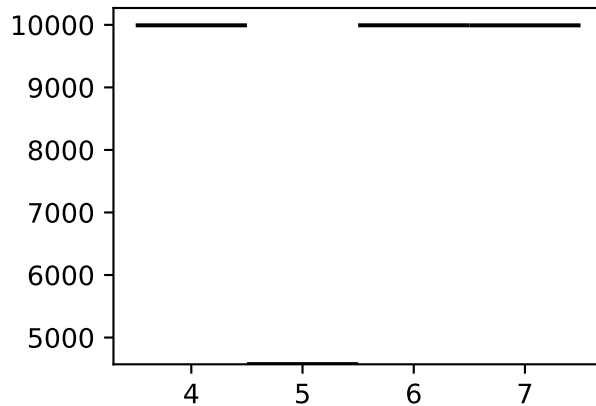
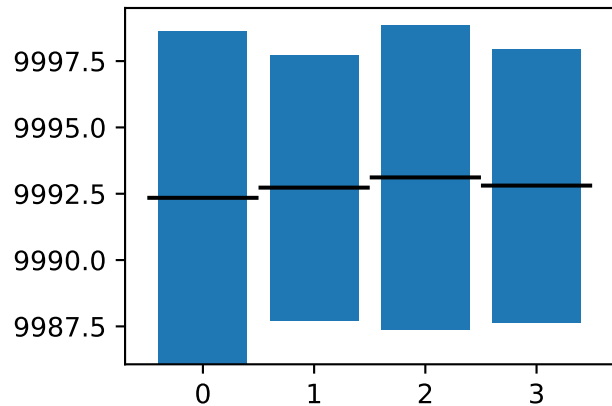
ADC_U_LOAD_GAIN



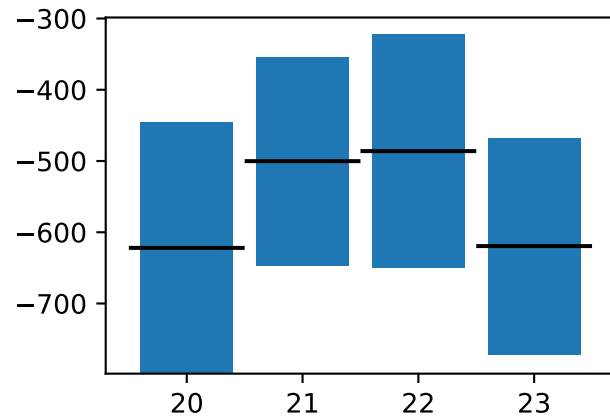
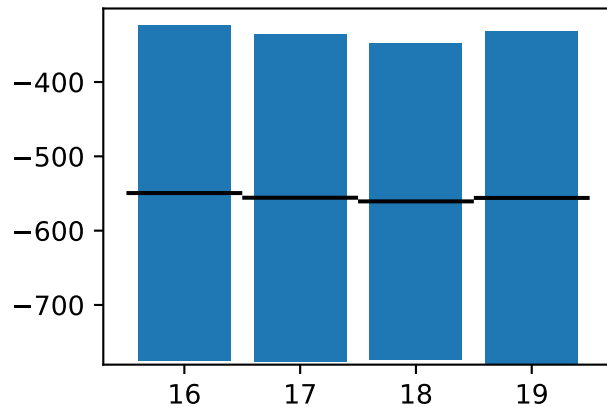
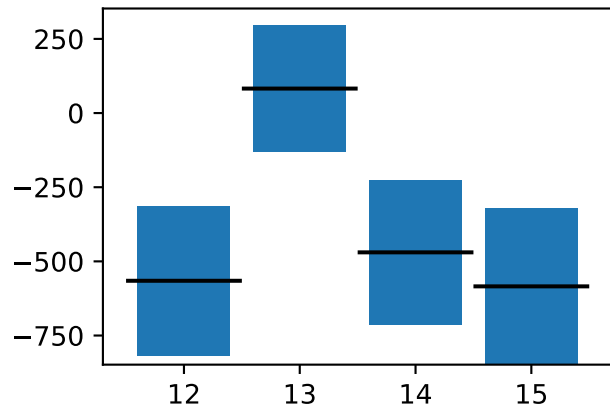
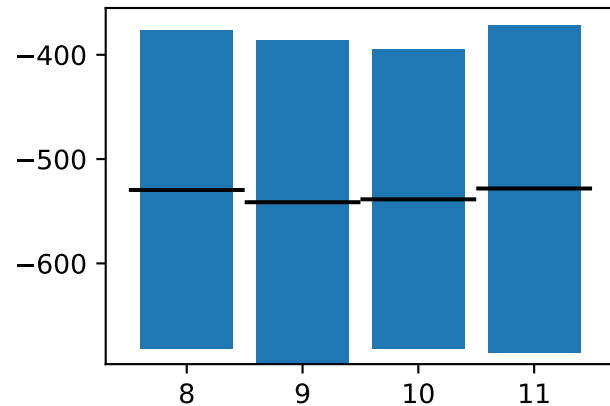
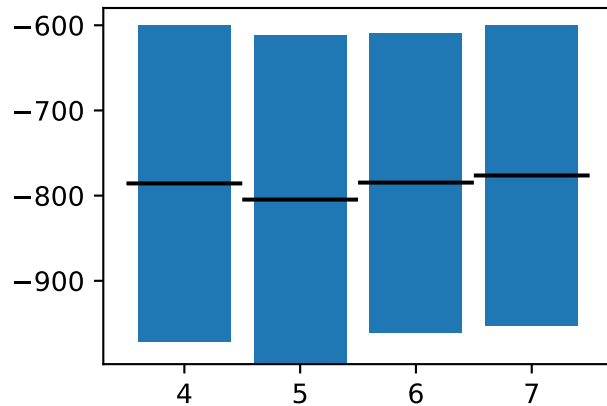
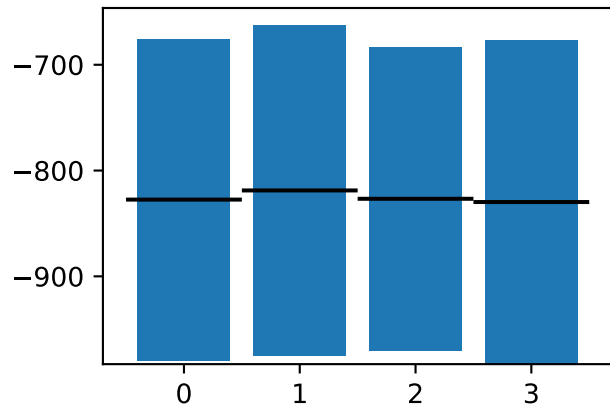
ADC_U_LOAD_OFFSET



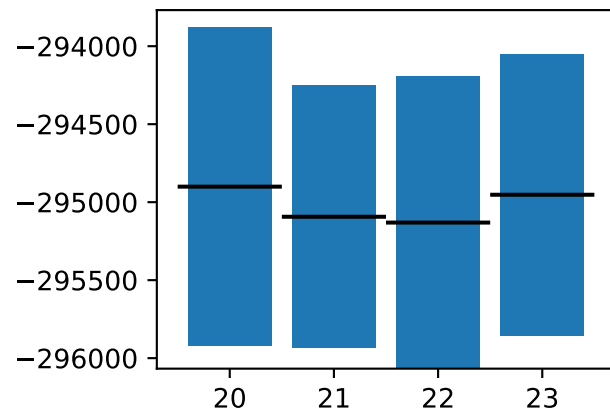
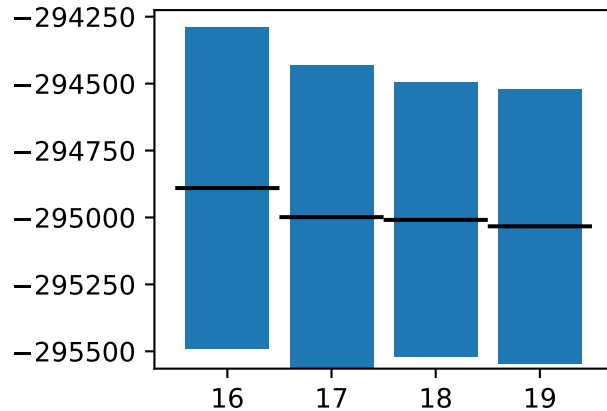
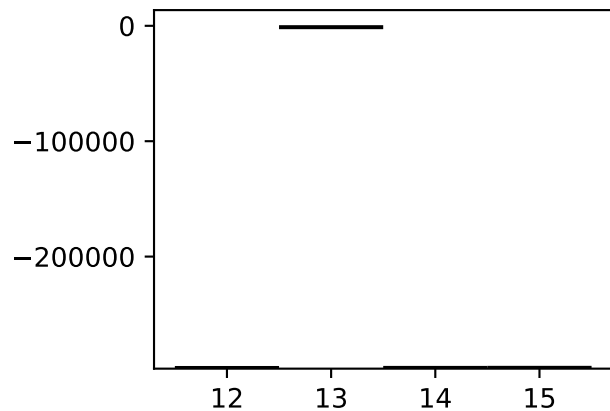
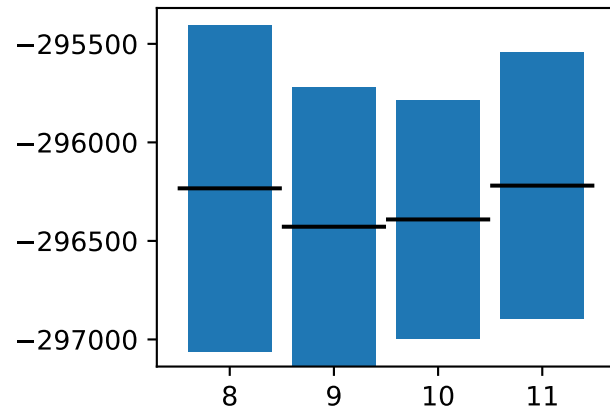
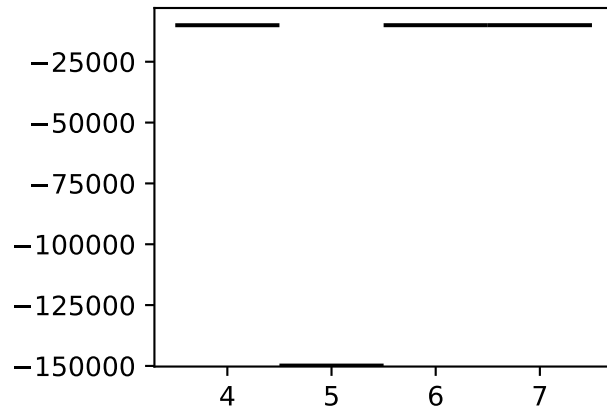
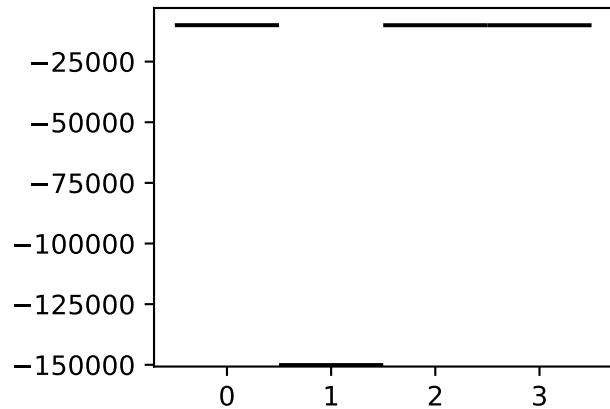
ADC_U_REGULATOR_GAIN



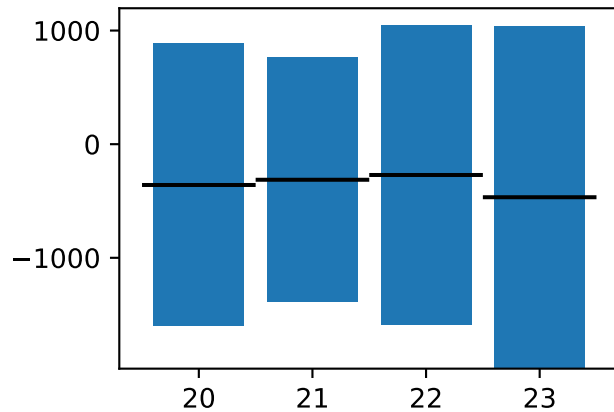
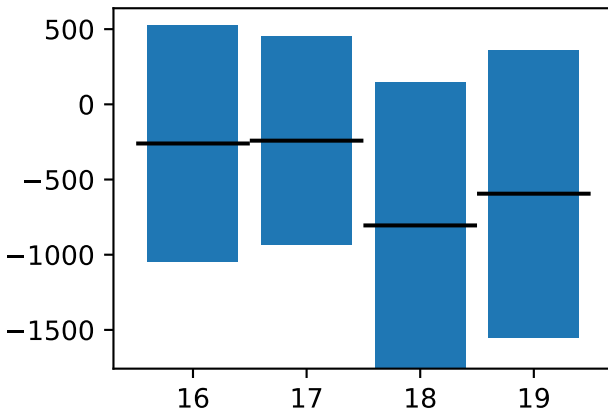
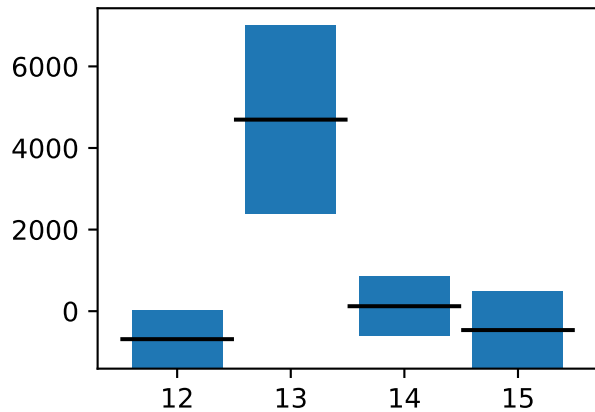
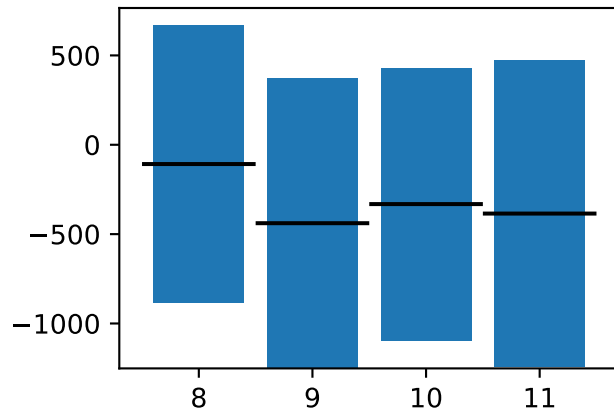
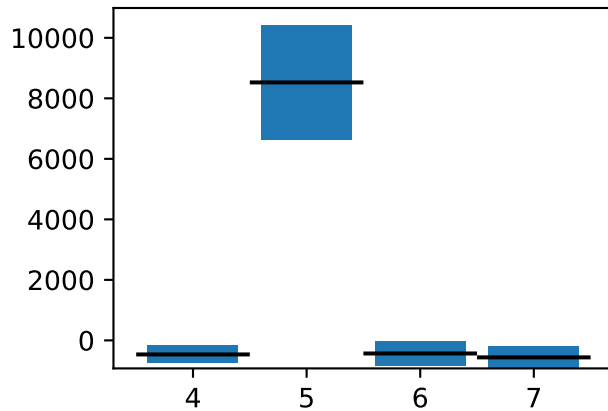
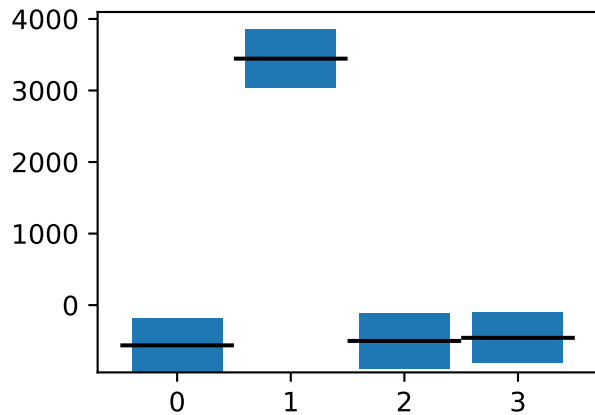
ADC_U_REGULATOR_OFFSET



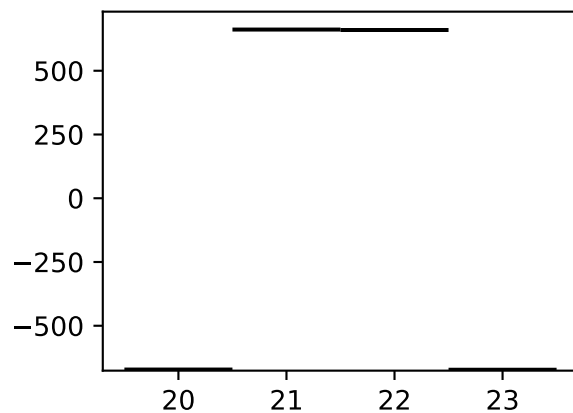
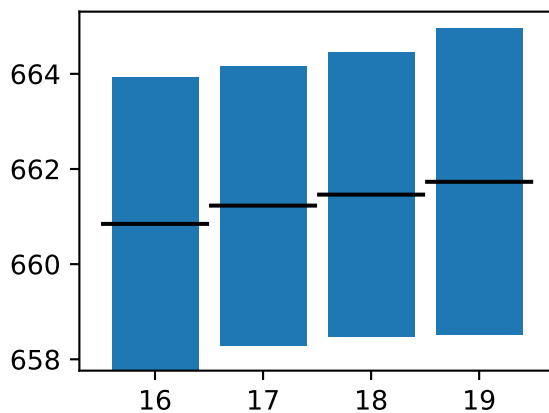
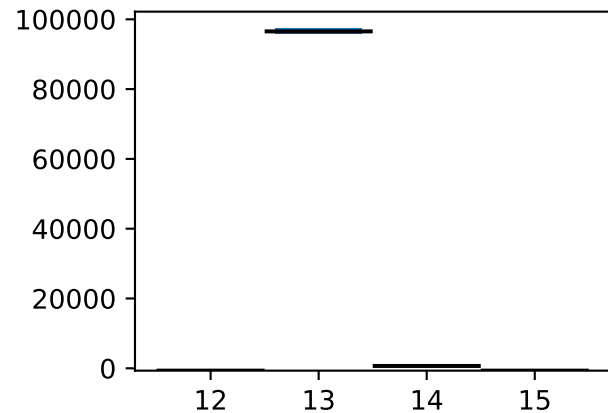
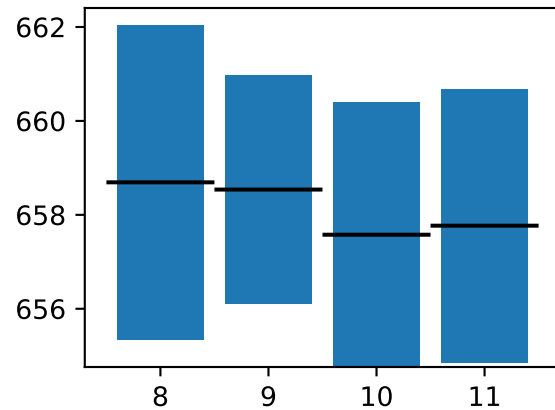
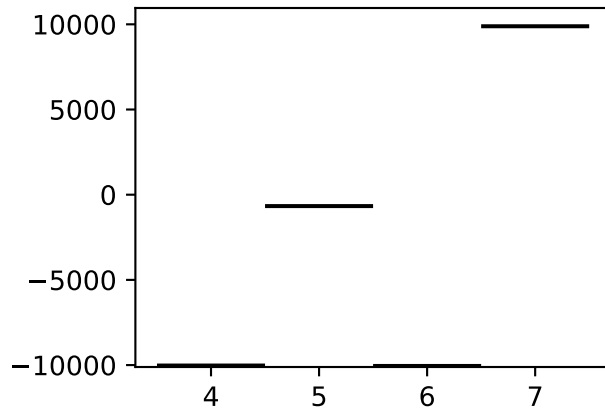
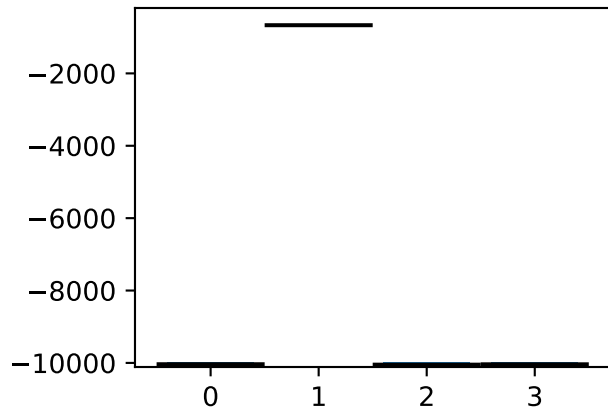
ADC_I_MON_GAIN



ADC_I_MON_OFFSET



DAC_CURRENT_GAIN



DAC_CURRENT_OFFSET

