**Memo: Current Status of Processor Milestone 2**

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Changes after Milestone 1

We changed the instruction set very heavily in this milestone. We removed the subi command completely, and changed every jump command except jr to be a pseudo instruction. More arithmetic instructions were added, and lots of simple, commonly used or wished for commands were added to the pseudo instruction set. In addition, the list of instructions and pseudo instructions were separated more clearly.

The J instruction type was changed to accommodate most j type instructions becoming pseudo instructions.

Condition Codes were added to make any non-I-type instruction into an if statement. So now it is extremely easy to make “one line if statements” by running a cmp and then adding a custom condition code in the instruction.

$sp is no longer available to the user because push and pop remove the need to directly access it. It is not one of the sixteen main instructions.

Milestone 2:

We have designed out RTL and methods to test them. We have a simple top level hand-drawn schematic diagram and ready to implement it with Xilinx. This diagram is also in our repository.