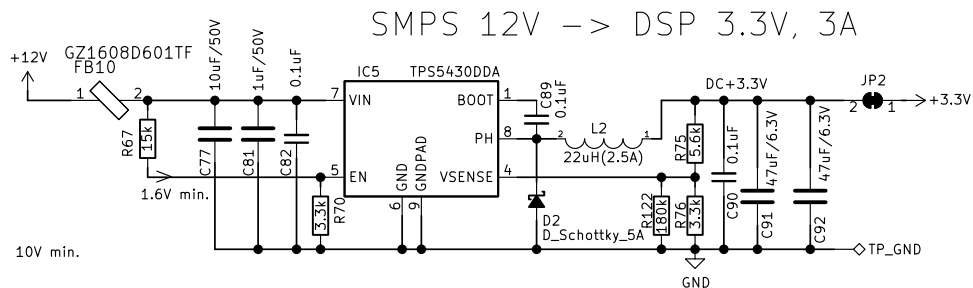
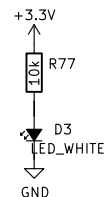


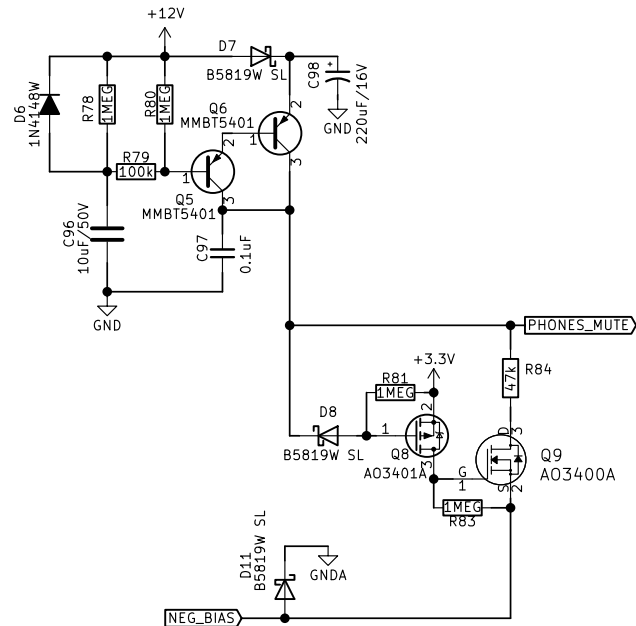
DC12V JP1 2 1 → +12V



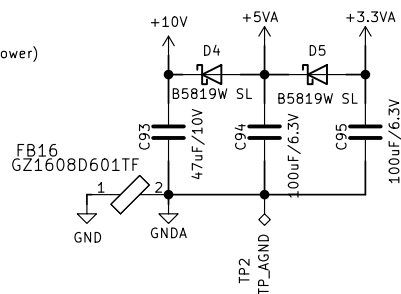
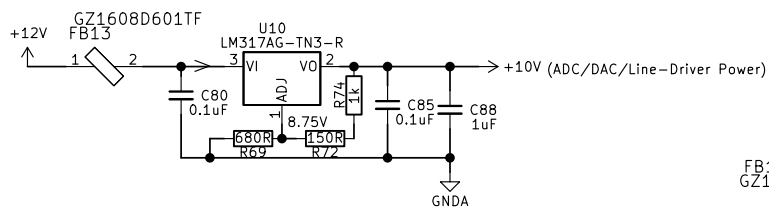
POWER INDICATOR



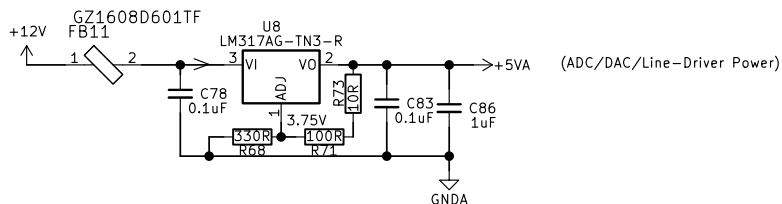
Power ON/OFF Mute Driver



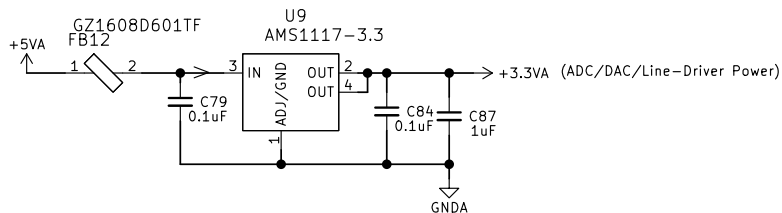
REG12V → ANALOG 10.0V



REG12V → ANALOG 5.0V



REG12V → ANALOG 3.3V



MAIN BOARD

CyberPit 2025

Sheet: /PSU/

File: PSU.sch

Title: FreeDSP-G Schematic Circuit Diagram

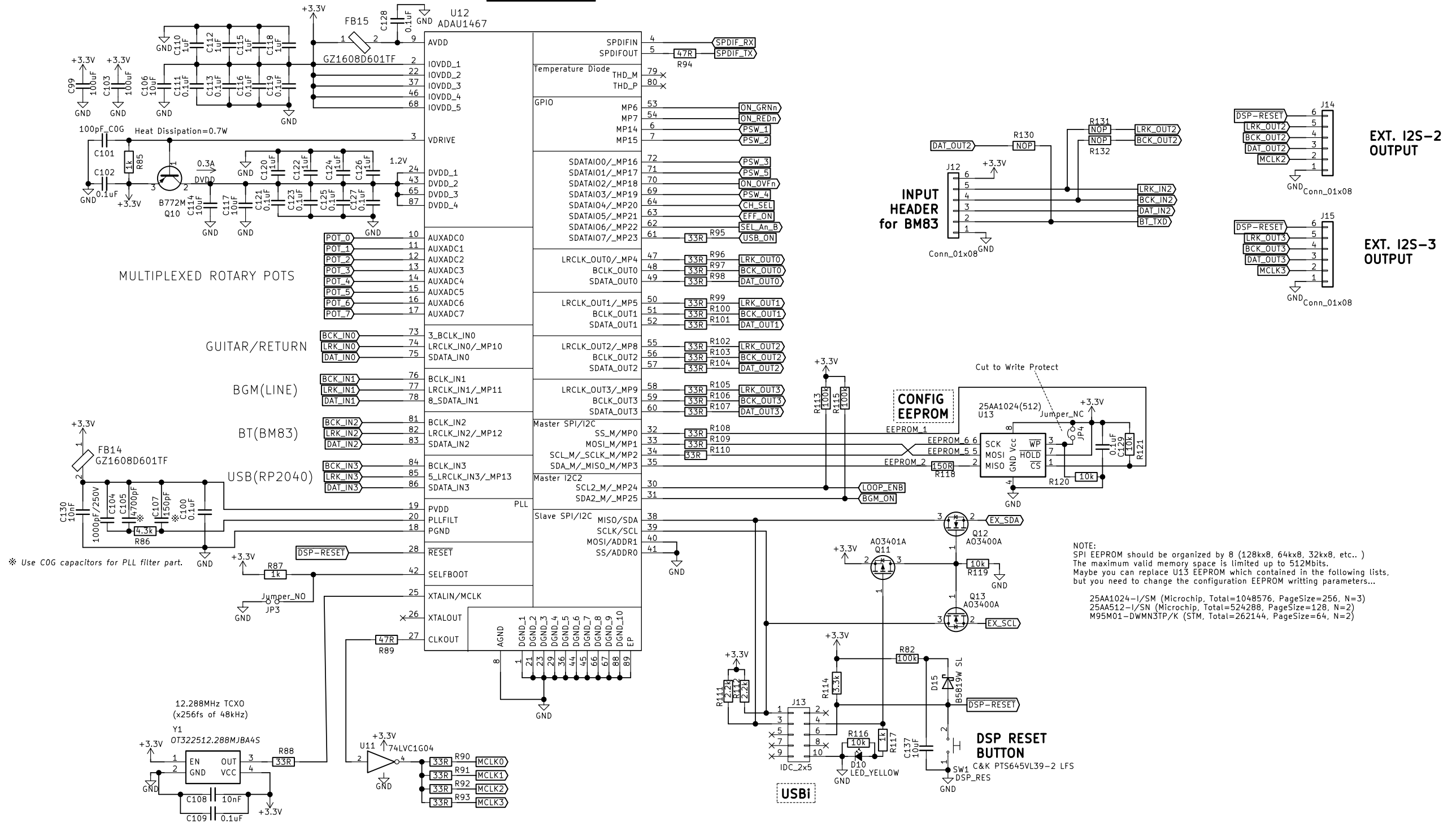
Size: A4

Date: 2025-05-19

Rev: 0.3

KiCad E.D.A. kicad (5.1.9-0-10_14)

Id: 3/5



Default: DSP's I2C slave port is connected to the XIAO RP2040.
If you Connect the USB1 then the RP2040 will offline automatically.

MAIN BOARD DSP PART		
CyberPit 2025		
Sheet: /DSP/ File: DSP.sch		
Title: FreeDSP-G Schematic Circuit Diagram		
Size: A3	Date: 2025-05-19	Rev: 0.3
KiCad E.D.A. kicad (5.1.9-0-10_14)		Id: 4/5

