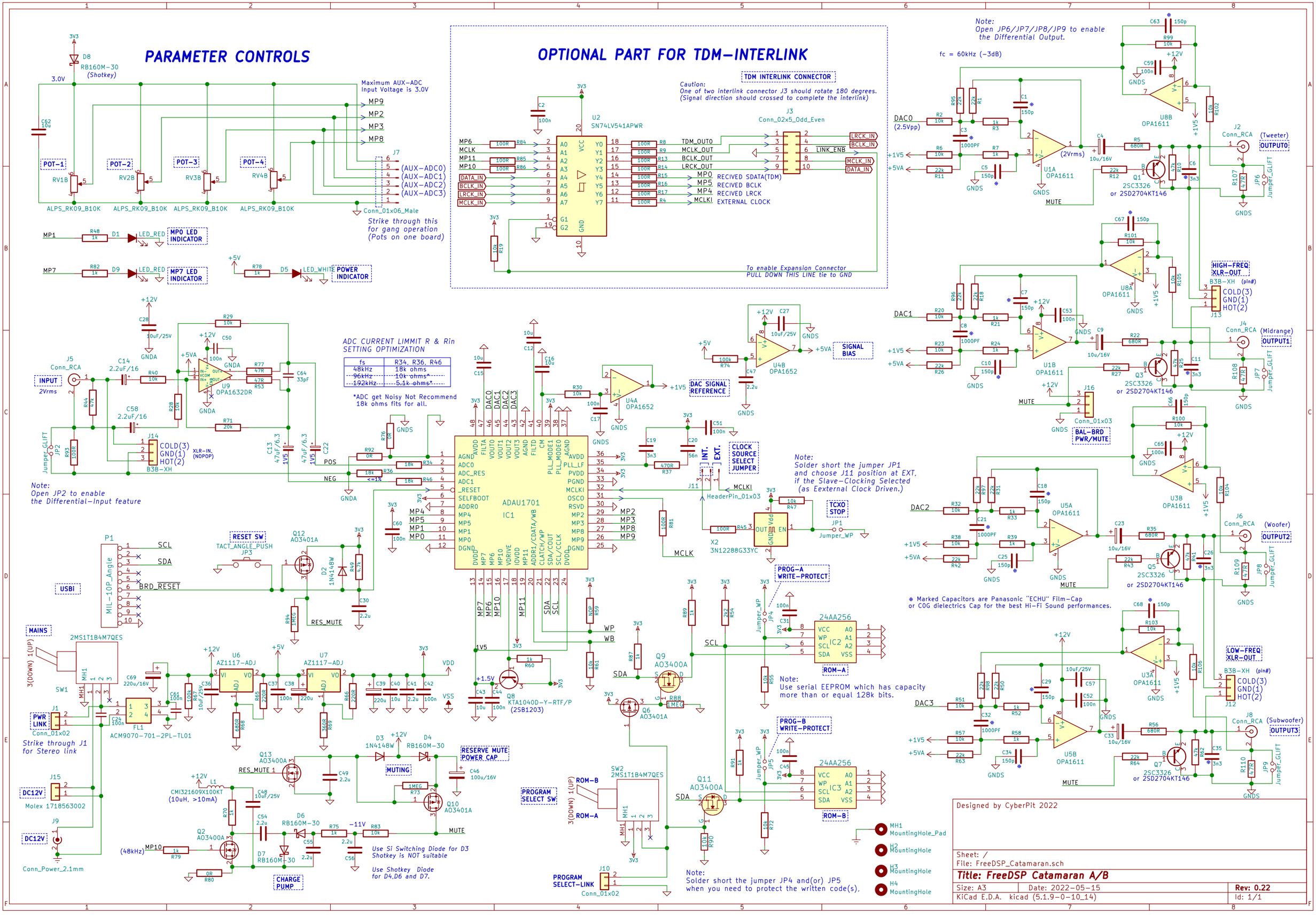
PARAMETER CONTROLS
OPTIONAL PART FOR TDM-INTERLINK
TDM INTERLINK CONNECTOR
Caution:
One of two interlink connector J3 should rotate 180 degrees.
(Signal direction should crossed to complete the interlink)
To enable Expansion Connector
PULL DOWN THIS LINE tie to GND
Maximum AUX-ADC
Input Voltage is 3.0V
Strike through this
for gang operation
(Pots on one board)
Note:
Open JP6/JP7/JP8/JP9 to enable
the Differential Output.
fc = 60kHz (-3dB)
ADC CURRENT LIMMIT R & Rin
SETTING OPTIMIZATION
*ADC get Noisy Not Recommend
18k ohms fits for all.
Note:
Open JP2 to enable
the Differential-Input feature
Note:
Solder short the jumper JP1
and choose J11 position at EXT,
if the Slave-Clocking Selected
(as Exeternal Clock Driven.)
Note:
Use serial EEPROM which has capacity
more than or equal 128k bits.
Note:
Solder short the jumper JP4 and(or) JP5
when you need to protect the written code(s).
Use Si Switching Diode for D3
Shotkey is NOT suitable
Use Shotkey Diode
for D4,D6 and D7.
Strike through J1
for Stereo link
* Marked Capacitors are Panasonic "ECHU" Film-Cap
or C0G dielectrics Cap for the best Hi-Fi Sound performances.
Designed by CyberPit 2022
Sheet: /
File: FreeDSP_Catamaran.sch
Title: FreeDSP Catamaran A/B
Size: A3
Date: 2022-05-15
Rev: 0.22
KiCad E.D.A. kicad (5.1.9-0-10_14)
Id: 1/1