R65C02_TC: Timing in cycles for all Op Codes - Aug. 18th 2008 Jens.Gutschmidt V0.92

	Accumulator	Immediate	Zero Page	Zero Page Relative	Zero Page, X	Zero Page, Y	Absolute	Absolute, X	Absolute, Y	Implied	Relative	(Indirect)	(Indirect, X)	(Indirect), Y	(Absolute Indirect)	(Absolute Indirect, X)
ADC		2	3		4		4	4/p5	4/p5			5/6d	6	5/p6		
AND		2	3		4		4	4/p5	4/p5			5	6	5/p6		
ASL	2		5		6		6	7								
BBR				5/j6/jp7												
BBS				5/j6/jp7												
BCC											2/j3/jp4					
BCS											2/j3/jp4					
BEQ											2/j3/jp4					
BIT		2	3		4		4	4/p5								
BMI											2/j3/jp4					
BNE											2/j3/jp4					
BPL											2/j3/jp4					
BRA											j3/jp4					
BRK										7						
BVC											2/j3/jp4					
BVS											2/j3/jp4					
CLC										2						
CLD										2						
CLI										2						
CLV										2						
CMP		2	3		4		4	4/p5	4/p5			5	6	5/p6		
CPX		2	3				4									
CPY		2	3				4									
DEC	2		5		6		6	7								
DEX										2						
DEY										2						
EOR		2	3		4		4	4/p5	4/p5			5	6	5/p6		
INC	2		5		6		6	7								
INX										2						
INY										2						
JMP							3								6	6
JSR							6									
LDA		2	3		4		4	4/p5	4/p5			5	6	5/p6		
LDX		2	3			4	4		4/p5							
LDY		2	3		4		4	4/p5								
LSR	2		5		6		6	7								
NOP										2						
ORA		2	3		4		4	4/p5	4/p5			5	6	5/p6		
PHA										3						
PHX										3						
PHY										3						
PHP										3						
PLA										4						
PLX										4						
PLY										4						
PLP										4						
RMB			5													
ROL	2		5		6		6	7								
ROR	2		5		6		6	7								
RTI										6						
RTS										6						
SBC		2	3		4		4	4/p5	4/p5			5/6d	6	5/p6		
SEC										2						
SED										2						
SEI										2						
SMB			5													
STA			3		4		4	5	5			5	6	6		
STX			3			4	4									
STY			3		4		4									
STZ			3		4		4	5								
TAX										2						
TAY										2						
TRB			5				6									
TSB			5				6									
TSX										2						
TXA										2						
TXS										2						
TYA										2						

BACKGROUND COLOR: Implemented but not tested yet (outstanding)
BACKGROUND COLOR: Not implemented yet (outstanding)
BACKGROUND COLOR: Tested for function and timing
BACKGROUND COLOR: Same State Machine – not fully tested
BACKGROUND COLOR: Test failed

TEXT COLOR: New for 65C02 – not implemented in 6502
TEXT COLOR: changed number of cycles