

## DM54LS259/DM74LS259 8-Bit Addressable Latches

#### **General Description**

These 8-bit addressable latches are designed for general purpose storage applications in digital systems. Specific uses include working registers, serial-holding registers, and active-high decoders or demultiplexers. They are multifunctional devices capable of storing single-line data in eight addressable latches, and being a 1-of-8 decoder or demultiplexer with active-high outputs.

Four distinct modes of operation are selectable by controlling the clear and enable inputs as enumerated in the function table. In the addressable-latch mode, data at the datain terminal is written into the addressed latch. The addressed latch will follow the data input with all unaddressed latches remaining in their previous states. In the memory mode, all latches remain in their previous states and are unaffected by the data or address inputs. To eliminate the possibility of entering erroneous data in the latches, the enable should be held high (inactive) while the address lines are changing. In the 1-of-8 decoding or demultiplexing mode, the addressed output will follow the level of the D input with all other outputs low. In the clear mode, all outputs are low and unaffected by the address and data inputs.

#### **Features**

- 8-Bit parallel-out storage register performs serial-to-parallel conversion with storage
- Asynchronous parallel clear
- Active high decoder
- Enable/disable input simplifies expansion
- Direct replacement for Fairchild 9334
- Expandable for N-bit applications
- Four distinct functional modes
- Typical propagation delay times: Enable-to-output 18 ns Data-to-output 16 ns Address-to-output 21 ns Clear-to-output 17 ns
- Fan-out

I<sub>OL</sub> (sink current) 54LS259 4 mA 74LS259 8 mA I<sub>OH</sub> (source current) -0.4 mA

■ Typical I<sub>CC</sub> 22 mA

#### **Connection Diagram**

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Order Number DM54LS259E, DM54LS259J, DM54LS259W, DM74LS259M, DM74LS259WM or DM74LS259N See NS Package Number E20A, J16A, M16A, M16B, N16E or W16A

#### **Function Table**

Inputs  Clear E		Output of	Each	Function		
		Addressed Latch	Other Output			
Н	L	D	Q <sub>i0</sub>	Addressable Latch		
H	Н	Q <sub>i0</sub>	Q <sub>i0</sub>	Memory		
L	L	D	L	8-Line Demultiplexer		
L	Н	L	L	Clear		

#### **Latch Selection Table**

Select Inputs			Latch		
С	В	Addressed			
L	L	L	0		
L	L	Н	1		
L	Н	L	2		
L	Н	Н	3		
Н	L	L	4		
Н	L	Н	5		
Н	Н	L	6		
Н	Н	Н	7		

H = High Level, L = Low Level

D =the Level of the Data Input

 $Q_{i0}=$  the Level of  $Q_i$  (i  $=0,\,1,\ldots 7,$  as Appropriate) before the Indicated Steady-State Input Conditions Were Established.

#### **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 7V
Operating Free Air Temperature Range

Storage Temperature Range  $-65^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$ 

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

Symbol	Parameter		DM54LS259			DM74LS259			Units
Symbol			Min	Nom	Max	Min	Nom	Max	Onits
V <sub>CC</sub>	Supply Voltage		4.5	5	5.5	4.75	5	5.25	٧
V <sub>IH</sub>	High Level Input Volt	age	2			2			٧
V <sub>IL</sub>	Low Level Input Voltage				0.7			0.8	٧
Іон	High Level Output Current				-0.4			-0.4	mA
l <sub>OL</sub>	Low Level Output Current				4			8	mA
t <sub>W</sub>	Pulse Width	Enable	17			15			- ns
	(Note 7)	Clear	17			15			
t <sub>SU</sub>	Setup Time (Notes 1, 2, 3 & 7)	Data	20 ↑			15↑			ns
		Select	15↓			15↓			
t <sub>H</sub>	t <sub>H</sub> Hold Time	Data	5↑			2.5 ↑			ns
	(Notes 1, 2 & 7)	Select	0 ↑			2.5 ↑			
T <sub>A</sub>	Free Air Operating Temperature		-55		125	0		70	°C

### **Electrical Characteristics** over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter Conditions		Min	Typ (Note 4)	Max	Units	
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 mA$				-1.5	V
V <sub>OH</sub>	High Level Output	$V_{CC} = Min, I_{OH} = Max$	DM54	2.5			V
	Voltage	$V_{IL} = Max, V_{IH} = Min$	DM74	2.7	3.4		
V <sub>OL</sub>	Low Level Output	$V_{CC} = Min, I_{OL} = Max$	DM54			0.4	V
	Voltage	$V_{IL} = Max, V_{IH} = Min$	DM74		0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$	DM74		0.25	0.4	
I	Input Current @ Max	$V_{CC} = Max, V_I = 7V$ $V_I = 10V$	DM74			0.1	mA
	Input Voltage		DM54			0.1	
liH	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μΑ
I <sub>IL</sub>	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.4	mA
	Enable	$V_{CC} = Max, V_I = 0.4V$				-0.8	
los	Short Circuit	V <sub>CC</sub> = Max	DM54	-20		-100	mA
	Output Current	(Note 5)	DM74	-20		-100	
Icc	Supply Current	V <sub>CC</sub> = Max (Note 6)			22	36	mA

Note 1: The symbols ( $\downarrow$ ,  $\uparrow$ ) indicate the edge of the clock pulse used for reference:  $\uparrow$  for rising edge,  $\downarrow$  for falling edge.

Note 2: Setup and hold times are with reference to the enable input.

Note 3: The select-to-enable setup time is the time before the High-to-Low enable transition that the select must be stable so that the correct latch is selected and the others not affected.

Note 4: All typicals are at  $V_{CC}=5V$ ,  $T_A=25^{\circ}C$ .

Note 5: Not more than one output should be shorted at a time, and the duration should not exceed one second.

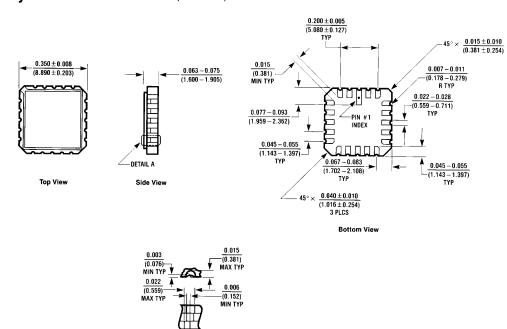
Note 6:  $\ensuremath{\text{I}_{\text{CC}}}$  is measured with all inputs at 4.5V, and all outputs open.

Note 7:  $T_A = 25^{\circ}C$  and  $V_{CC} = 5V$ .

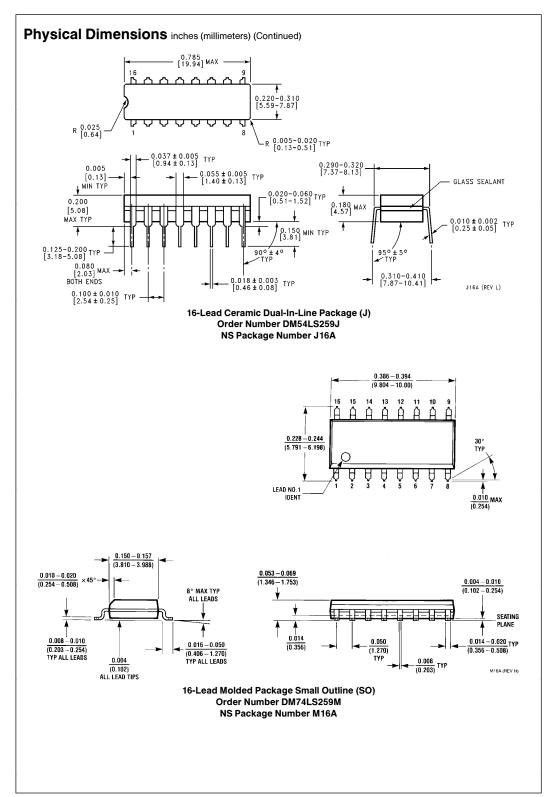
Switching	<b>Characteristics</b>	at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$
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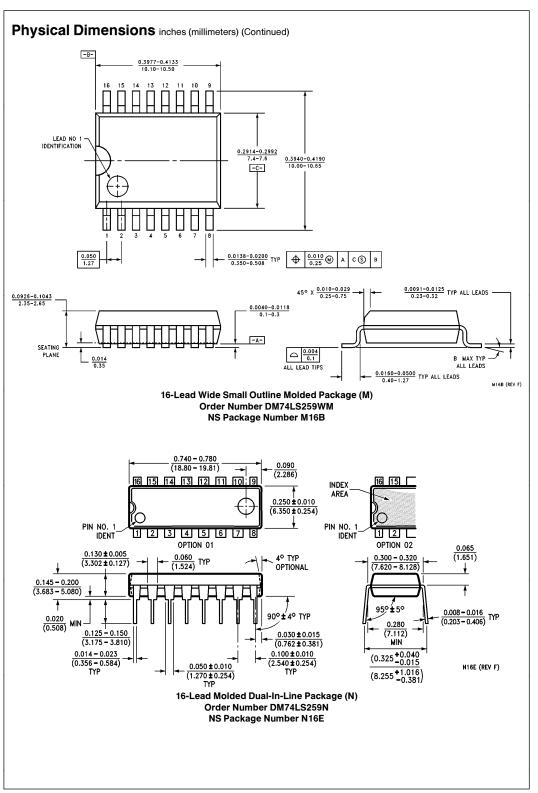
		From (Input) To (Output)	DM54LS $C_L = 15 \text{ pF}$		$\begin{aligned} & \text{DM74LS} \\ & \text{C}_{\text{L}} = \text{50 pF} \\ & \text{R}_{\text{L}} = \text{2 k}\Omega \end{aligned}$		Units
Symbol	Parameter						
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Enable to Output		27		38	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Enable to Output		24		32	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Data to Output		30		35	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Data to Output		20		30	ns
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	Select to Output		30		41	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Select to Output		29		38	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	Clear to Output		18		36	ns

# Physical Dimensions inches (millimeters)

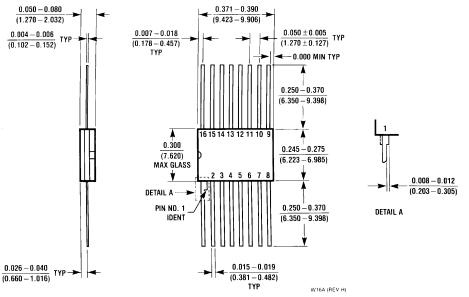


Ceramic Leadless Chip Carrier (E) Order Number DM54LS259E NS Package Number E20A E20A (REV D)





#### Physical Dimensions inches (millimeters) (Continued)



16-Lead Ceramic Flat Package (W) Order Number DM54LS259W NS Package Number W16A

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