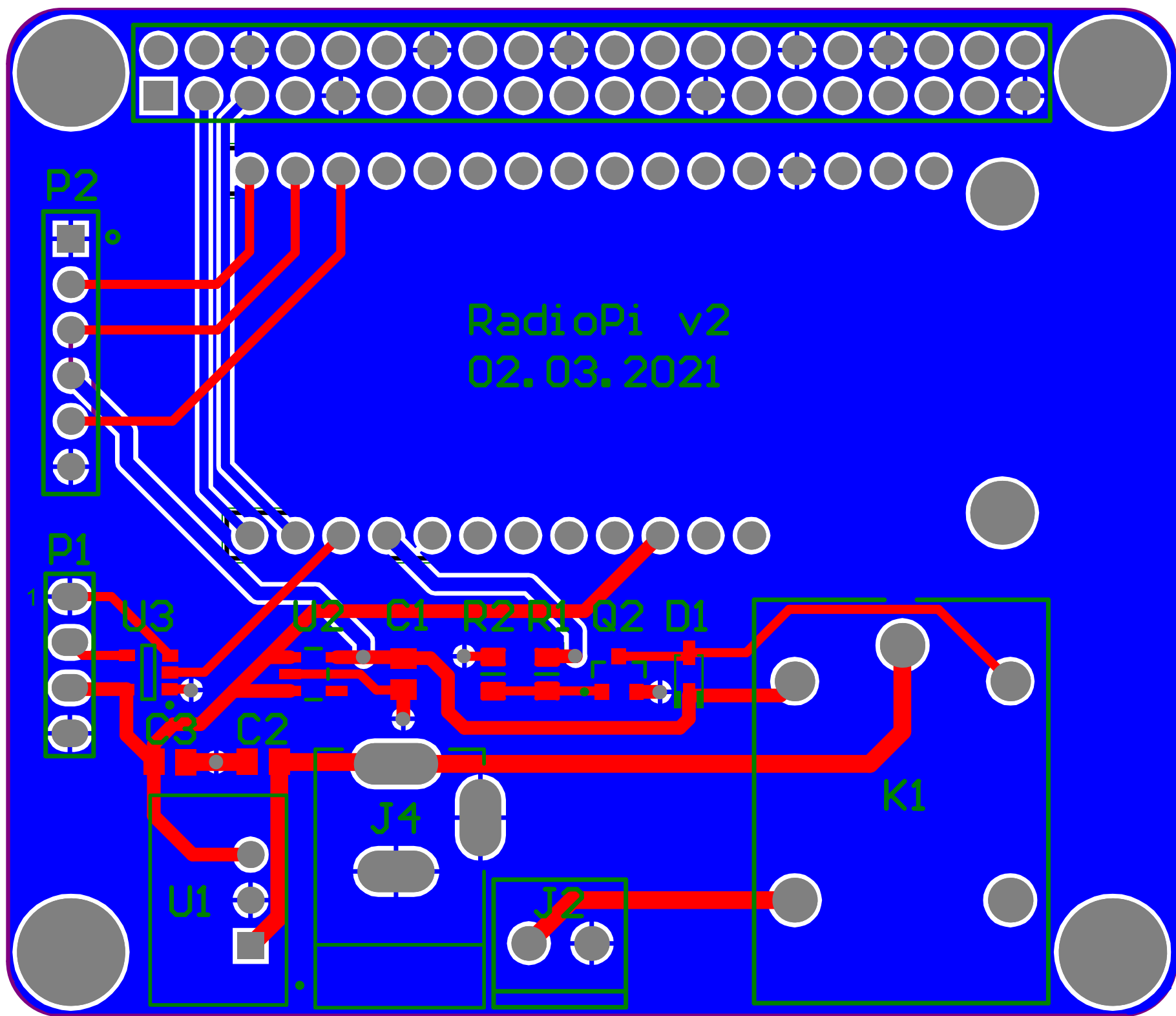
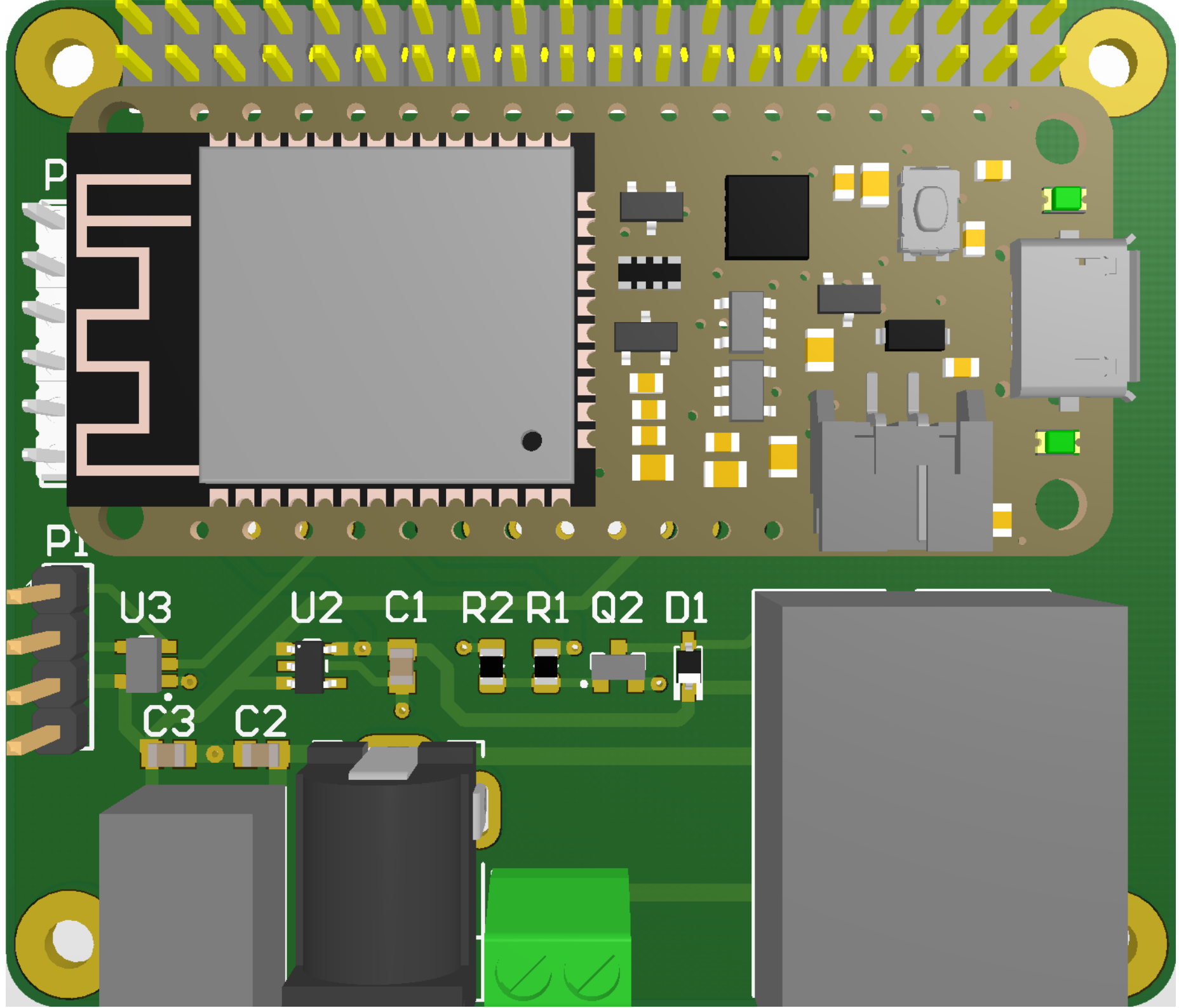
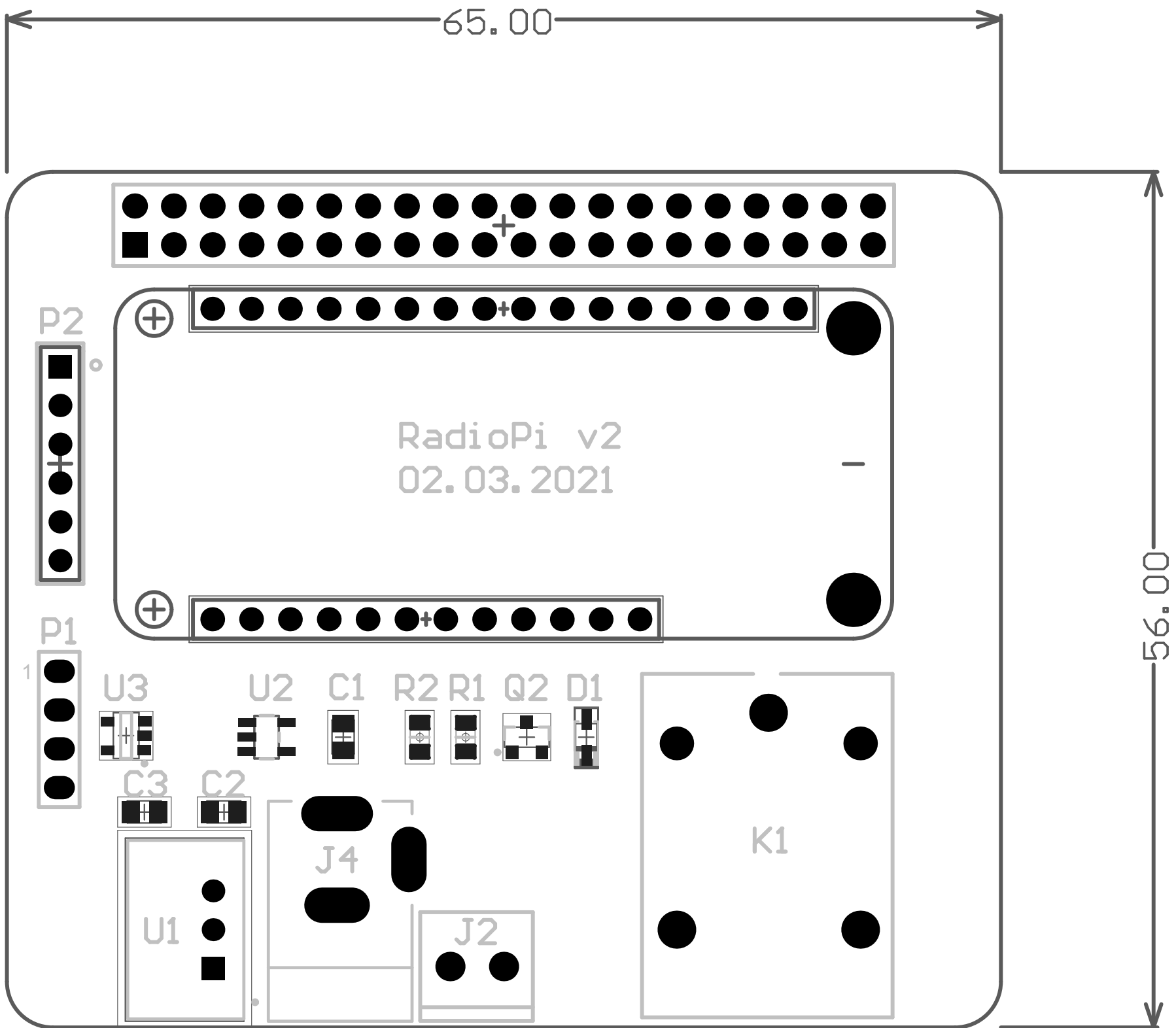
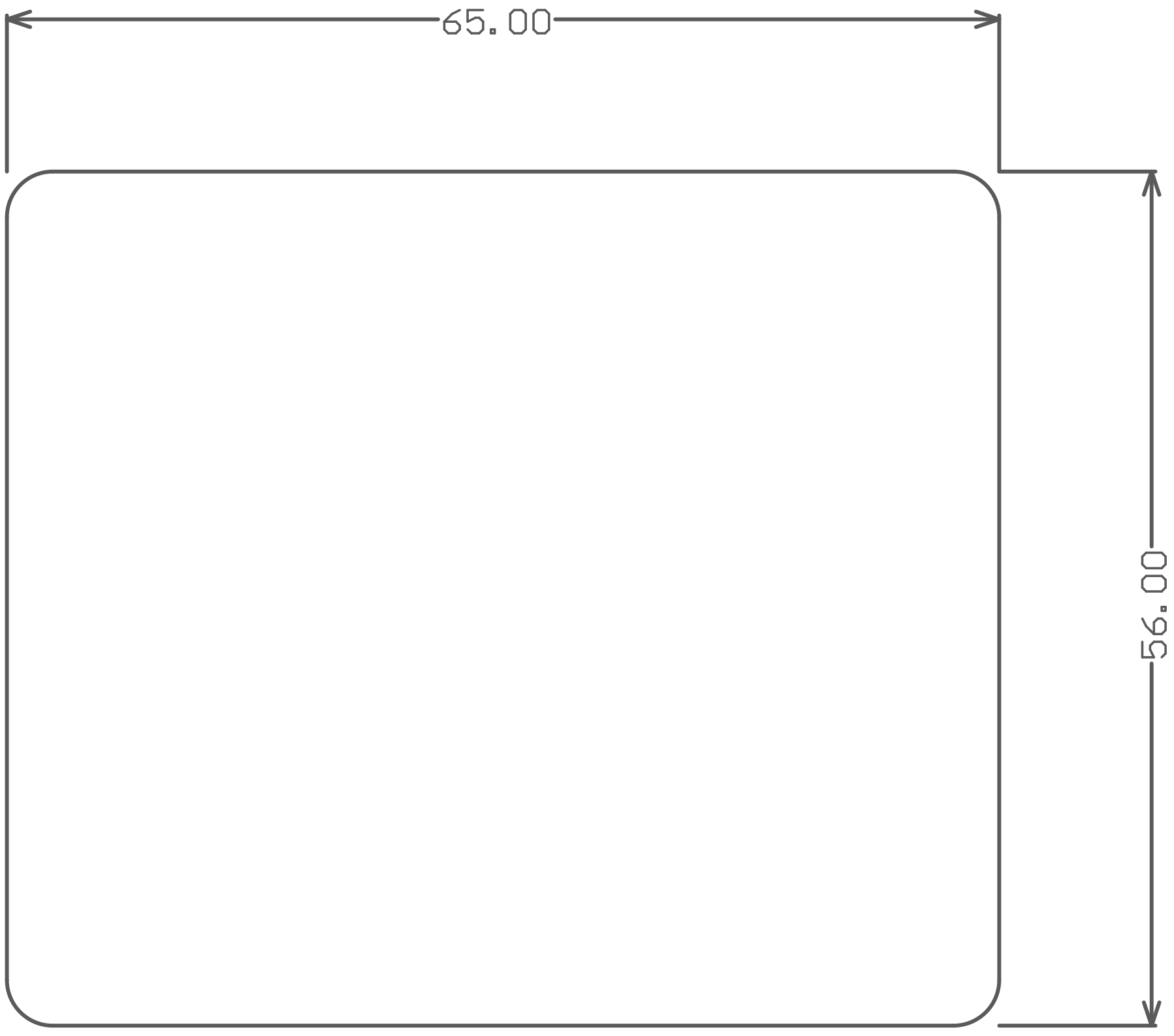


Title		
Size	Number	Revision
A4		
Date:	02.03.2021	Sheet of
File:	RadioPi.SchDoc	Drawn By:









## Design Rules Verification Report

Filename : C:\ProgramData\Altium\CircuitMaker {F2B950E4-FE8E-4059-9DBF-C5E7DC775378}\Projects\282F76D7-A7FD-433E-AC35-3374226C E5EE\fae0c382-be68-46b6-9bed-4960c9f8584f\RadioPi.CMPcbDoc Warnings 0  
Rule Violations 0

### Warnings

Total	0
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### Rule Violations

Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm)	0
Width Constraint (Min=0.5mm) (Max=1mm) (Preferred=0.5mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=4mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.095mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.1mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.254mm) (All),(All)	0
Silk primitive without silk layer	0
Net Antennae (Tolerance=0mm) (All)	0
Unpoured Polygon (Allow unpoured: False)	0
Width Constraint (Min=0.5mm) (Max=1mm) (Preferred=1mm) (InNetClass('Power'))	0
Total	0

## Electrical Rules Check Report

Class	Document	Message
Warning	RadioPi.SchDoc	Net NEO has no driving source (Pin *1-19,Pin U3-2)
Warning	RadioPi.SchDoc	Part *1 is not linked to Octopart
Warning	RadioPi.SchDoc	Part C1 is not linked to Octopart
Warning	RadioPi.SchDoc	Part C2 is not linked to Octopart
Warning	RadioPi.SchDoc	Part C3 is not linked to Octopart
Warning	RadioPi.SchDoc	Part D1 is not linked to Octopart
Warning	RadioPi.SchDoc	Part P2 is not linked to Octopart
Warning	RadioPi.SchDoc	Part R1 is not linked to Octopart
Warning	RadioPi.SchDoc	Part R2 is not linked to Octopart
Warning	RadioPi.SchDoc	Part U1 is not linked to Octopart
Warning	RadioPi.SchDoc	Part U3 is not linked to Octopart