

## 1 List of Blocks added to the core folder

1. CLSS_f	32. AUTOMAT	63. Modulo_count	94. ANDLOG_f
2. GAINBLOCK_f	33. TCLSS	64. Counter	95. INTRPBLK
3. Inductor	34. DLSS	65. TkScale	96. INTRP2BLK
4. SineVoltage	35. DLR	66. FROMWSB	97. CLKOUTV_f
5. CLR_f	36. DOLLAR	67. READAU_f	98. Edge_trigger
6. Gen_SQR	37. DOLLAR_m	68. READC_f	99. CLKFROM
7. BOUNCE	38. DOLLAR_f	69. RFILE_f	100. MCLOCK_f
8. Gen_Si,	39. Dlradapt_f	70. INIMPL_f	101. AFFICH_m
9. CScope	40. Samphold_m	71. Backlash	102. BARXY
10. Clock	41. Register	72. Deadband	103. CMSCOPE
11. SampleClock	42. Switch	73. DELAYV_f	104. CSCOPXY3D
12. Rand_m	43. Resistor	74. SELECT_f	105. WRITEAU_f
13. Matcatv	44. Step_function	75. MFCCLICK_f	106. CANIMXY
14. Matcath	45. Ground	76. HALT_f	107. CMAT3D
15. Const_m	46. BIGSOM_f	77. IN_f	108. CANIMXY3D
16. Gainblk	47. generic_block	78. DIFF_f	109. CSCOPXY
17. BOUNCEXY	48. C_block	79. GENERAL_f	110. CMATVIEW
18. IFTHEL_f	49. Fortran_block	80. ZCROSS_f	111. TRAS_f
19. VanneReglante	50. Debug	81. End_c	112. TOWS_c
20. Summation	51. Expression	82. Endblk	113. OUTIMPL_f
21. SourceP	52. CBLOCK4	83. HYSTERESIS	114. OUT_f
22. Bache	53. CBLOCK	84. RATELIMITER	115. Evtldly_c
23. PerteDP	54. MBLOCK	85. SATURATION	116. DEMUX
24. MatEig	55. BPLATFORM	86. M_freq	117. DEMUX_f
25. Rootcoef	56. CLKINV_f	87. CLK_GOTO	118. Extractor
26. Extract	57. TIME_f	88. CLKOUTV_f	119. ISELECT_m
27. Integral_m	58. RAMP	89. Extract_Activation	120. MUX_f
28. Sci_func	59. Const_f	90. ANDBLK	121. M_SWITCH
29. Matinv	60. CONST	91. Virtualclk0	122. FROM
30. SATURATION	61. PULSE_SC	92. EVTGEN_f	123. FROMMO
31. Mux	62. Sigbuilder	93. EVTWARDLY	124. NRMSOM_f
			125. RELAY_f

126. SELECT_m	142. CVS	158. MATZCONJ	174. CLOCK_f
127. Switch_f	143. Capacitor	159. MATBKSL	175. NEGTOPOS_f
128. MATMAGPHI	144. OpAmp	160. EXTTRI	176. POSTONEG_f
129. TrigFun	145. VsourceAC	161. BITSET	177. CONSTRAINT2_c
130. Expblk_m	146. VVsourceAC	162. BITCLEAR	178. GotoTagVisibility
131. Powblk_m,	147. PMOS	163. MATPINV	179. GotoTagVisibilityMO
132. Relationalop,	148. NMOS	164. MATSING	180. VariableResistor
133. SQRT,	149. SHIFT	165. MATRESH	181. SCALAR2VECTOR
134. MAXMIN,	150. SUBMAT	166. DLATCH	182. CLKGotoTagVisiblity
135. SIGNUM	151. CONVERT	167. NPN	183. CEVENTSCOPE
136. ABS_VALUE	152. RICC	168. PNP	184. IdealTransformer
137. INVBLK	153. INTMUL	169. DERIV	185. PotentialSensor
138. TANBLK_f	154. MATEXPM	170. Voltagesensor	186. ConstantVoltage
139. LOGBLK_f	155. MATLU	171. Currentsensor	187. EXTRACTBITS
140. MATZREIM	156. MATDET	172. CURV_F	
141. CCS	157. MATDIAG	173. SUM_f	

## 2 Special Cases

Some of the Blocks have inconsistent schema and data is stored in different sub-blocks within the main Basic Block.

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| 1. Delay_f    | 4. LOGICAL_op  |
| 2. SRFLIPFLOP | 5. TextBlock   |
| 3. JKFLIPFLOP | 6. SELF_SWITCH |