



Dev Cube

User Manual

Version: *1.0*

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Dev Cube Introduction

Dev Cube is a chip integration development tool provided by Bouffalo Lab, which includes IoT Program download, MCU Program download and RF Performance testing. This document mainly introduces...IoT and MCU Download the relevant configurations for the program. For RF performance testing, please refer to the "RF Performance Testing User Manual".

Dev Cube provides users with the ability to download applications and supports configuring the clock. The program can be encrypted and signed according to user needs, and it also has the function of burning user resource files and partition tables.

The main functions are as follows:

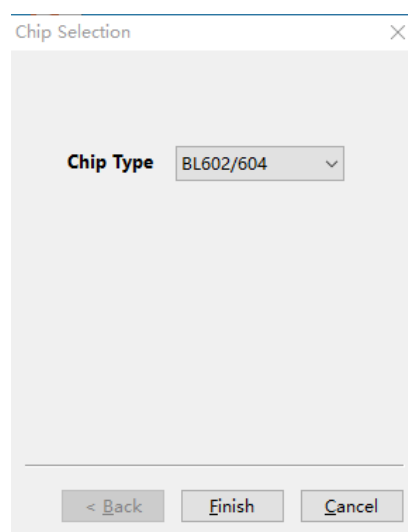
1. Support IoT Applications and MCU Application download

2. Supports multiple models Flash Erasing, writing, reading

3. Supports downloading various types of files. Flash And verify

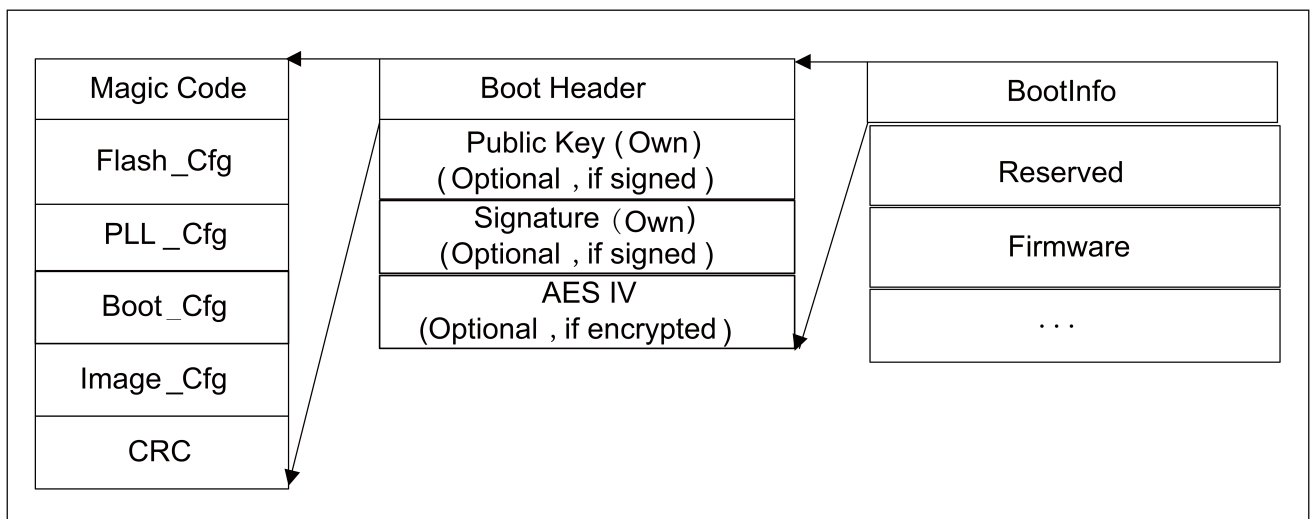
4. Download communication interface support UART and JLink, Download speed is configurable

Users can [Bouffalo Lab Dev Cube](#) Get the latest version Dev Cube. Double-click the extracted folder. BLDevCube.exe, exist Chip Selection. Select the corresponding chip model in the dialog box and click Finish. Enter Dev Cube Main interface.



picture1.1: Dev Cube Chip selection interface

Whether IoT Program or MCU The programs, their mirror images have the same structural composition, as shown in the following figure:



picture2.1:Boot image composition structure

The boot image mainly consists of two parts:

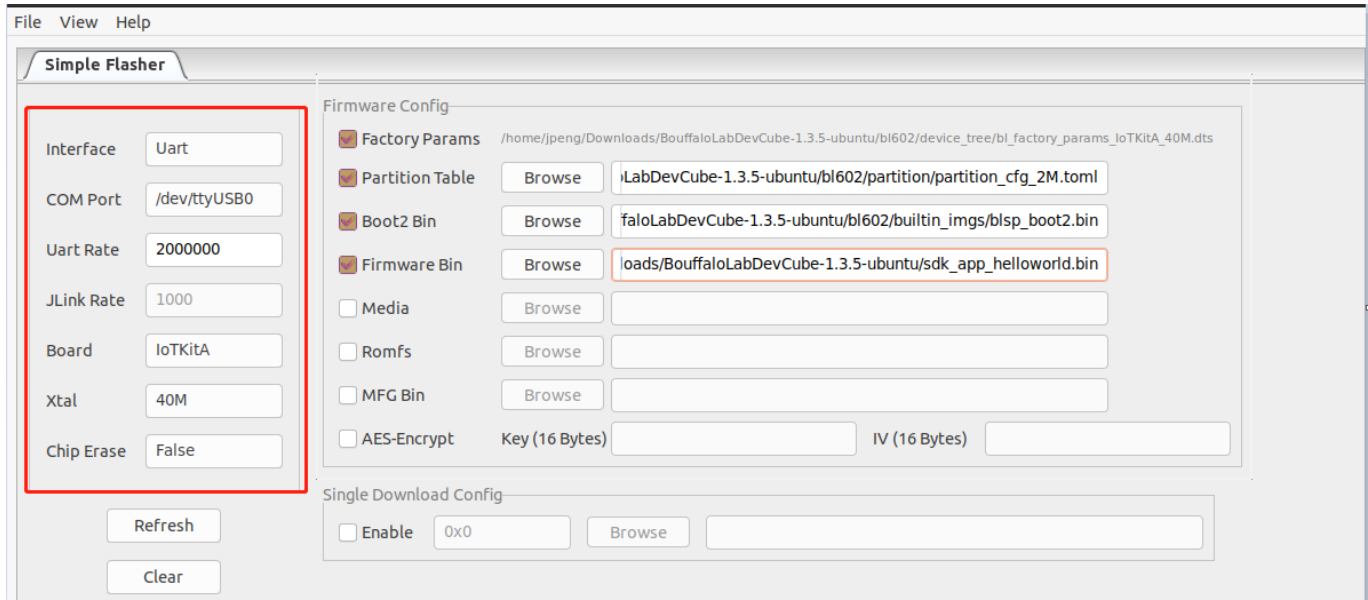
- **BootInfo** Mainly includes **BootInfo** of Magic Code, Flash Configuration information, PLL Configuration information, boot parameters, and image configuration information, etc.
- **Firmware** Firmware, i.e., application code

Simply downloading the application will not make the chip work properly; the startup information must also be downloaded. **BootInfo** Download to the specified location. Taking single-core download as an example, the actual parameters of the hardware circuit need to be considered. XTAL, PLL, Flash Once the configuration information is burned to **Bootinfo Addr** The corresponding address contains the compiled application. bin File burned to **Image Addr** In the corresponding address.

existViewSelect from menuIoTThe options will lead to...IoTThe program download interface is mainly divided into two parts: program download method configuration and burning file configuration.

3.1 Configuration program download method

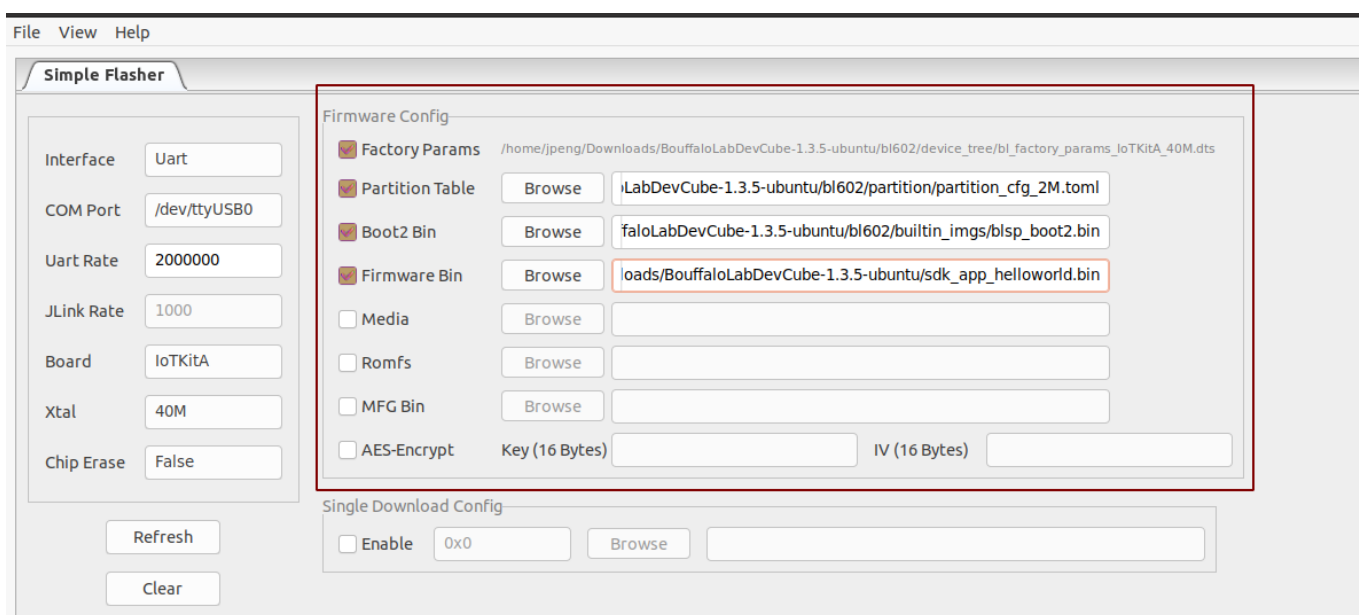
- Configuration parameters include:
 - Interface: Used to select the communication interface for downloading and burning.JlinkorUART,Users select based on actual physical connection.
 - COM PortWhen choosingUARTWhen downloading, select the option connected to the chip.COMSlogan, you can clickRefreshButton to proceed COMNumber refresh
 - Uart SpeedWhen choosingUARTWhen downloading, enter the baud rate and recommended download frequency.2M
 - BoardThe choice of board model, board model, and crystal oscillator type together determine the...DTSThe file, in other words, determines the board-level hardware configuration parameters.
 - Chip Erase: The default setting isFalseDuring download, erase according to the burning address and content size, select...TrueAt that time, before the program is burned, it will beFlashErase All
 - XtalUsed to select the type of crystal oscillator used on the board.



picture3.1: IoTProgram download method selection interface

3.2 Configure download file

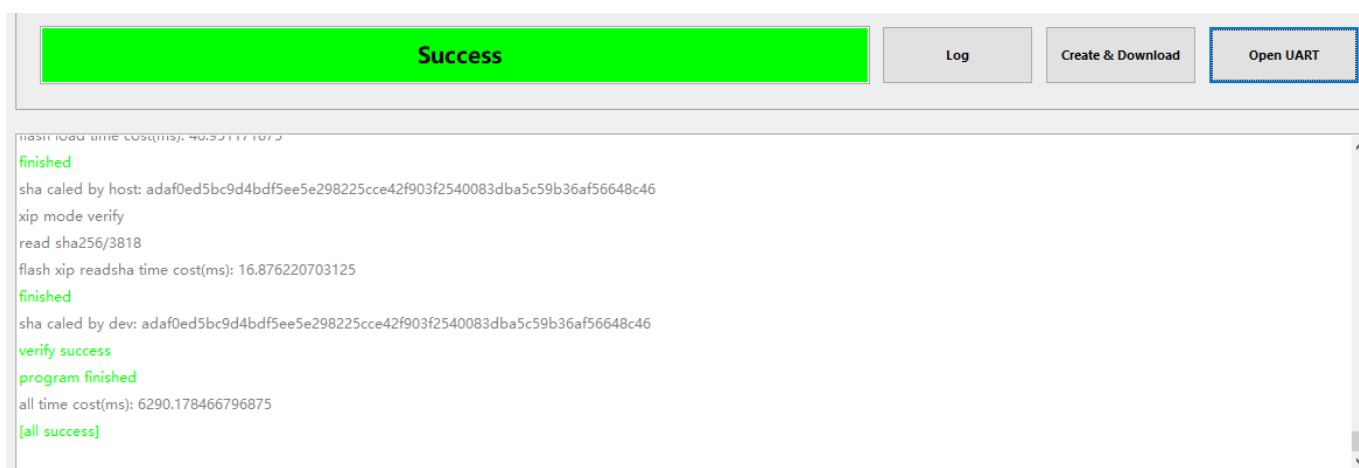
- Configuration parameters include:
 - Partition Table: use Dev Cube Corresponding chip model in the directory partition. The partition table and partition files in the folder are mainly based on... Flash Size determined, default selection 2M. Partition table configuration file
 - Boot2 Bin: It is the first program to run after the system starts. Flash. The program is responsible for establishing BLSPA secure environment is provided, and the main program is guided to run. Dev Cube Corresponding chip model in the directory builtin_imgs. In the folder Boot2 Bin document
 - Firmware Bin: User-compiled bin file, here select the generated one helloworld.bin
 - Media/Romfs: Media and Romfs. Choose one of the two options; if you select one... Media. The selected item is a file; if you check the box... Romfs. Then the folder is selected.
 - MFG Bin: choose MFG document, MFG. The file is RF. The application used during production testing should be selected based on the crystal oscillator type. Dev Cube Corresponding chip model in the directory builtin_imgs/mfg. In the folder mfg bin document
 - AES-Encrypt: If encryption is used, it is necessary to... AES-Encrypt. Select the option and enter the encryption method used in the text box next to it. Key and IV. The input is the hexadecimal representation of ""0~"F", one byte. It consists of two characters, so Key and IV. Each requires input 32 individual Characters. It is important to note that... IV. The end 8 characters (i.e.) 4 Bytes must be all 0
 - Single Download Config: Check the box Enable. You can then download a single file. Raw file to specified Flash. For the download address, enter the starting address in the text box on the left. 0xHead



picture3.2:File selection interface for burning

3.3Download program

- When choosingUARTTo program the circuit, you need to burn the board with the necessary components.BOOTSet to high level to reset the chip, bringing it to a high state.UARTThe download status is being guided (if the user board has...).Bootpins andResetPins, all are withUSBSerial converterDTRandRTSFor connections, no manual setup is required; the download program will automatically configure them.Bootpins andReset(Chip). When selectingJlinkDuring the burning process, you can keep...BootSet the pin low to put it in a slave state.Flash Startup status
- ClickCreate & DownloadThe tool can then automatically generate the application image and startup parameter configuration file, and begin flashing the various configuration files. The following image will appear.logInformation: Program downloaded successfully.

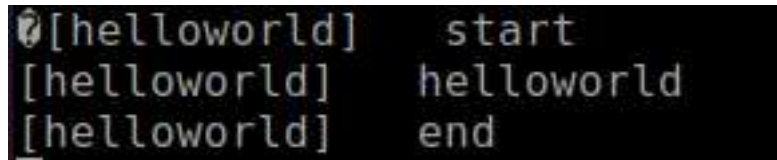


picture3.3: IoTDownload program

annotation:If no board is connected, simply generate the application image and startup parameter configuration file by clicking [\[click here\]](#).Create & ProgramButton

- After successful download, put the board...BOOTSetting the pin to low level resets the chip, causing it to...FlashOnce started, the application will begin running.

The image below ishello worldThe effect of the program running.



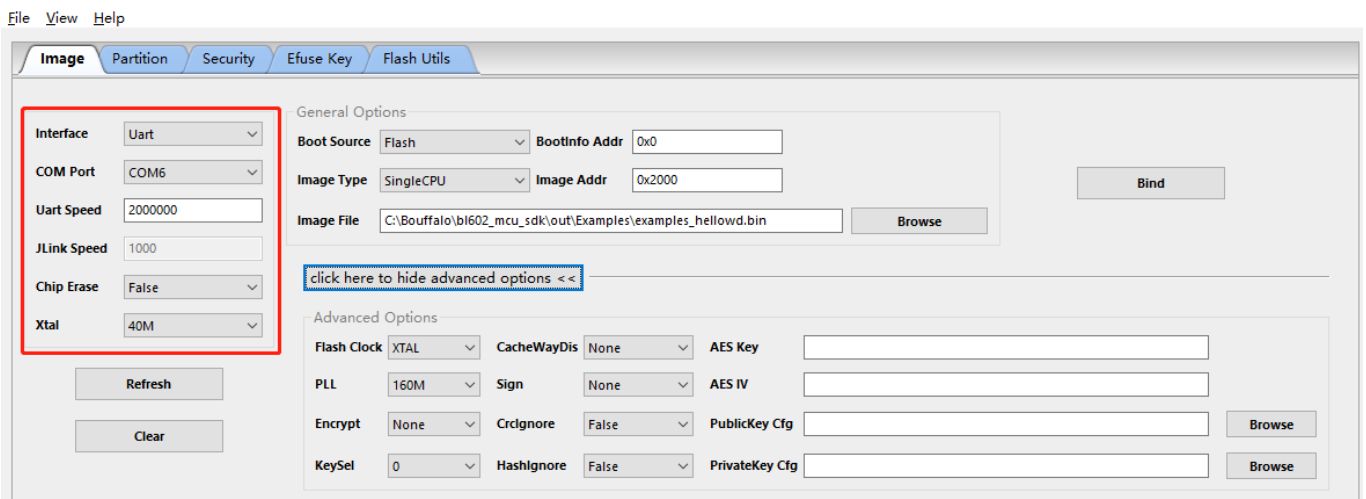
picture3.4: Hello worldProgram running effect

MCUProgram Download

existViewSelect from menuMCUThe options will lead to...MCUThe program download interface is mainly divided into three parts: firmware download method configuration, image parameter configuration, and advanced image parameter configuration.

4.1Configure firmware download method

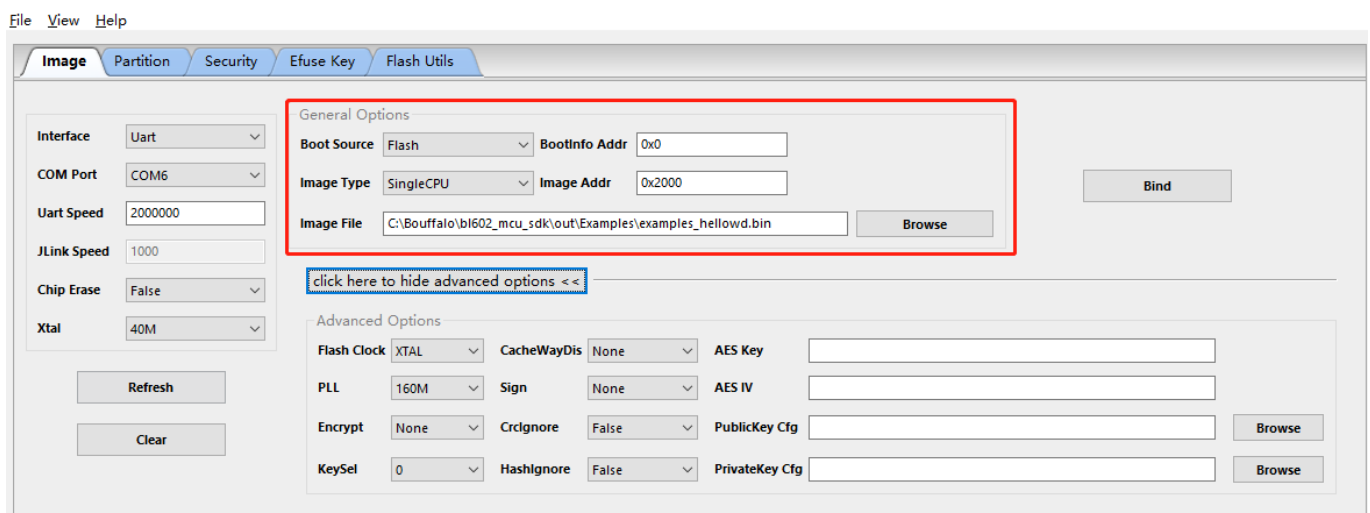
- Configuration parameters include:
 - Interface: Used to select the communication interface for downloading and burning.JlinkorUART,Users select based on actual physical connection.
 - COM PortWhen choosingUARTWhen downloading, select the option connected to the chip.COMSlogan, you can clickRefreshButton to proceed COMNumber refresh
 - Uart SpeedWhen choosingUARTWhen downloading, enter the baud rate and recommended download frequency.2M
 - Chip Erase: The default setting isFalseDuring download, erase according to the burning address and content size, select...TrueAt that time, before the program is burned, it will beFlashErase All
 - XtalUsed to select the type of crystal oscillator used on the board.



picture4.1: MCUFirmware download method selection interface

4.2 Configure image parameters

- Configuration parameters include:
 - Boot Source: Default isFlashOnly when it is necessary to generate fromUARTorSDIOYou only need to select this option when booting the image.UART/SDIO
 - BootInfo Addr:BootinfoThe location where startup parameters are stored. For single-core programs, fill in...0x0,For dual-coreCPU0Mirror, fill in 0x0,For dual-coreCPU1Mirror, fill in0x1000
 - Image Type:SingleCPUUsed to generate a single-core image.CPU0Used to generate dual-coreCPU0The mirror image,CPU1Used to generate dual-coreCPU1 The mirror image,Boot2Used to generateBoot2Mirror imageRAWUsed to download user-defined raw resource files.
 - Image Addr: The location where the application is stored. It is recommended to fill in this.0x2000or0x2000Future address
 - Image File: Select applicationBinFiles or user resource files



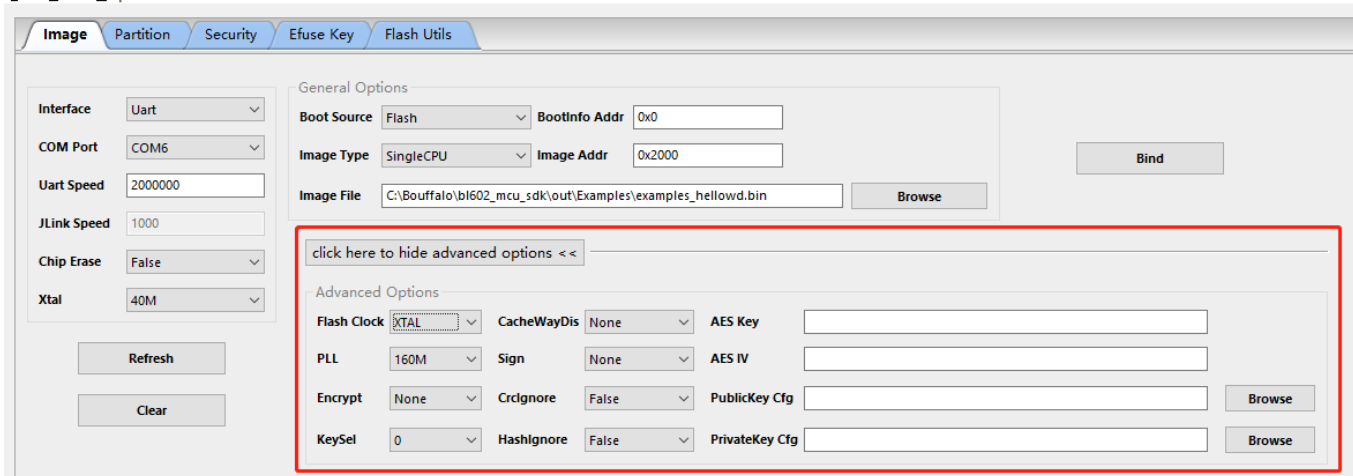
picture4.2:Mirror parameter selection interface

4.3 Configure advanced image parameters

- When clickclick here to show advanced optionsWhen prompted, advanced image configuration will be displayed, and the configurable parameters include:
 - Flash ClockUsed for settingFlashclock
 - PLL : PLLClock configuration, default is160M
 - CacheWayDis: L1C Cacheof4stripwayThe setting is set to default:none,Even if all of them could4stripway
 - Sign:Choose whether to perform an image update.ECCsign
 - CrcIgnore :Is it necessary?CRCVerification. When parameters are selected.FalseAt that time, turn onBoot InfoofCRCVerify; otherwise, do not set.Boot Info of CRCcheck
 - HashIgnore:Is it necessary to ensure the integrity of the image?HashVerification. When parameters are selected.FalseIt is necessary to do it at the timeHashVerification is performed; conversely, no integrity verification is performed on the image.

- Encrypt: Select an encryption method and enable encryption for the program image. After enabling encryption, you need to... AES Encryption method in AES Key and AES IV Enter the corresponding value in the input field.

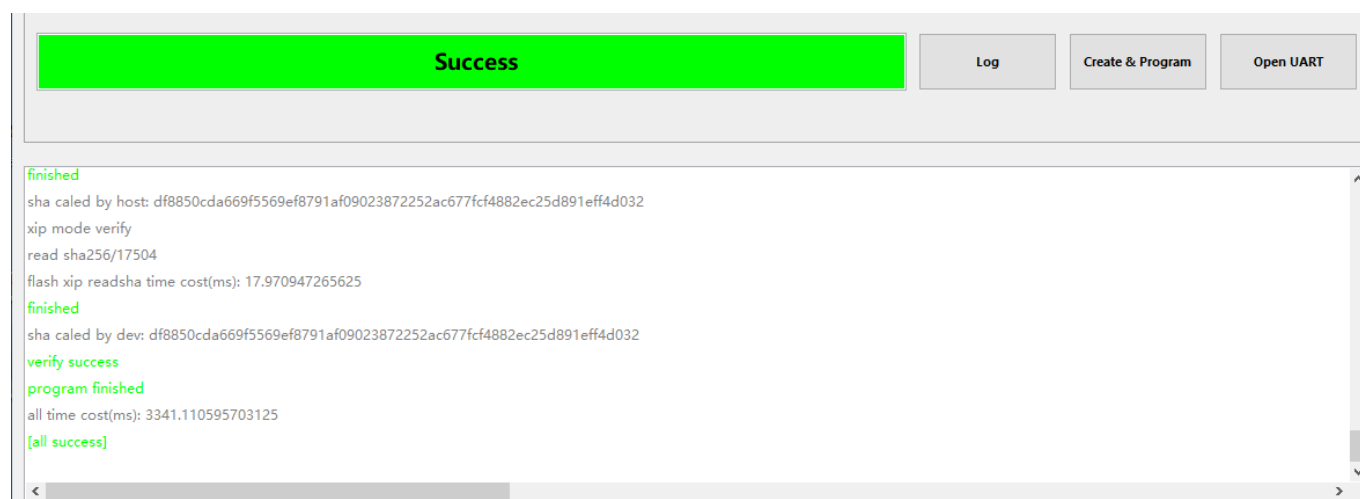
File View Help



picture4.3: Advanced image parameter selection interface

4.4 Download program

- When choosing UART to program the circuit, you need to burn the board with the necessary components. BOOT Set to high level to reset the chip, bringing it to a high state. UART The download status is being guided (if the user board has...). Bootpins and Reset Pins, all are with USB Serial converter DTR and RTS For connections, no manual setup is required; the download program will automatically configure them. Bootpins and Reset (Chip). When selecting JLink During the burning process, you can keep... BOOT Set the pin low to put it in a slave state. Flash Startup status
- Click Create & Program The tool can then automatically generate the application image and boot parameter configuration file and begin the flashing process. (See the image below.) log Information: Program downloaded successfully.



picture4.4: Download program

annotation:If no board is connected, simply generate the application image and startup parameter configuration file by clicking [\[click here\]](#).Create & ProgramButton

- After successful download, put the board...BOOTSetting the pin to low level resets the chip, causing it to...FlashOnce started, the application will begin running.

The image below isHello worldThe effect of the program running.

```
system clock=160M
hello world
Loop 1000,1000
Loop 2000,1000
Loop 3000,1000
Loop 4000,1000
clic_timer_handlerLoop 5000,1000
```

picture4.5: Hello WorldProgram running effect

Configure hardware security parameters

existViewSelect from menuMCUOptions, clickSecuritySelect the option to enter the hardware security parameter configuration interface. Configuration information includes firmware download method settings,AES Mode configuration and key configuration.

File View Help

The screenshot shows a software interface for configuring hardware security parameters. At the top, there are five tabs: "Image", "Partition", "Security" (which is selected), "Efuse Key", and "Flash Utils". Below the tabs, the "Security" section contains several configuration options:

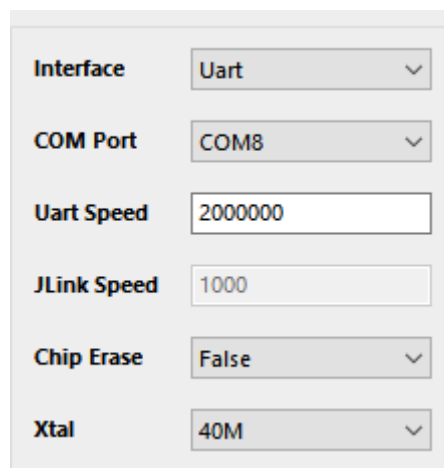
- Interface:** A dropdown menu set to "Uart".
- COM Port:** A dropdown menu set to "COM8".
- Uart Speed:** A text input field containing "2000000".
- JLink Speed:** A text input field containing "1000".
- Chip Erase:** A dropdown menu set to "False".
- Xtal:** A dropdown menu set to "40M".
- AESMode:** A dropdown menu set to "AES128".
- public key cfg:** A text input field with a "Browse" button to its right and a checked "Write Lock" checkbox.
- AES Key:** A text input field with checked "Write Lock" and "Read Lock" checkboxes to its right.

At the bottom of the configuration area, there are four buttons: "Refresh", "Clear", "Create", and "Program". Below the configuration area is a "LOG" section with a large empty text area.

picture5.1:Hardware parameter configuration interface

5.1 Configuration program download method

- Configuration parameters include:
 - Interface: Used to select the communication interface for downloading and burning.JlinkorUART,Users select based on actual physical connection.
 - COM PortWhen choosingUARTWhen downloading, select the option connected to the chip.COMSlogan, you can clickRefreshButton to proceed COMNumber refresh
 - Uart SpeedWhen choosingUARTWhen downloading, enter the baud rate and recommended download frequency.2M
 - Chip Erase: The default setting isFalseDuring download, erase according to the burning address and content size, select...TrueAt that time, before the program is burned, it will beFlashErase All
 - XtalUsed to select the type of crystal oscillator used on the board.



Interface	Uart
COM Port	COM8
Uart Speed	2000000
JLink Speed	1000
Chip Erase	False
Xtal	40M

picture5.2:Download method interface

5.2 Configure key parameters

To encrypt the chip, besides using [method/mechanism] during program download...ImageIn the functionAESSoftware encryption of the chip is followed by hardware encryption.

- existAESModeSelect the corresponding encryption mode; in this example, select [encryption mode].AES128
- existpublic key cfgSelect public keyPEMFile, in this example, we choose /common/pem/publickey_uecc.pem
- AES KeyEnter the same value as the software encryption.

ClickCreate,generateEfuseFile, clickProgramBurningEfusedocument.

AESMode AES128

public key cfg

Browse ☒ Write Lock

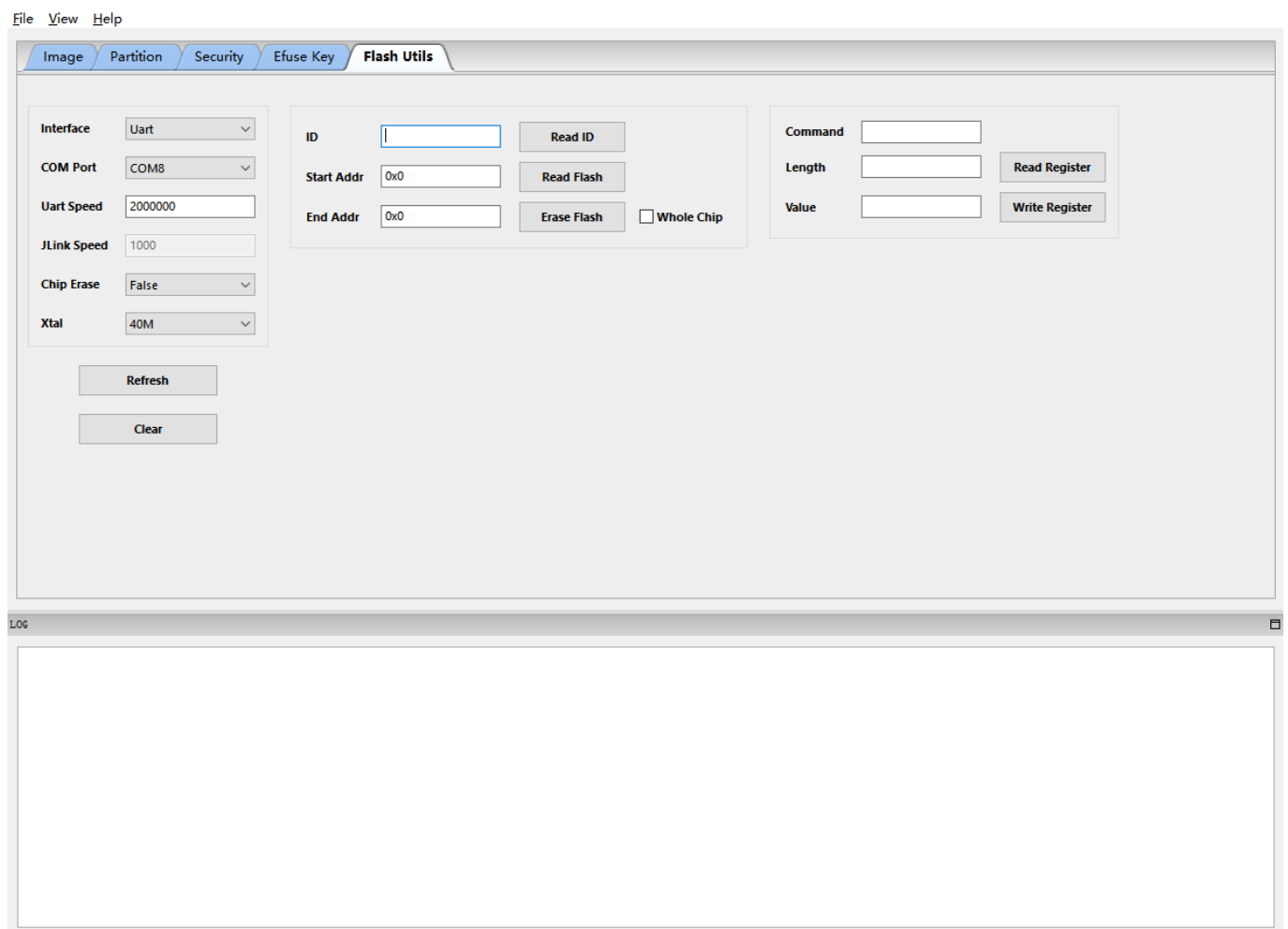
AES Key

☒ Write Lock ☒ Read Lock

Create Program

picture5.3:Key parameter configuration interface

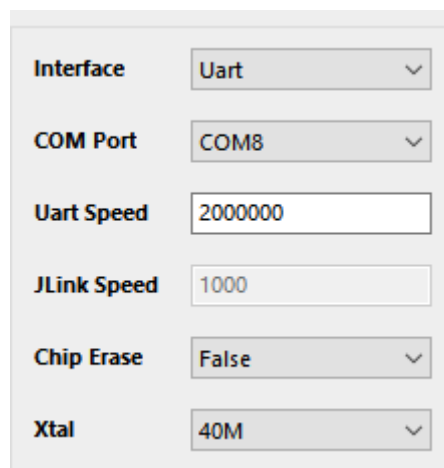
existViewSelect from menuMCUOptions, clickFlash UtilsOptions, enterFlashDebugging assistant interface.FlashDebugging assistant is used to obtainFlash IDRead and eraseFlashThe content at the specified address, and the values of the corresponding registers can be read and written.



picture6.1: FlashDebugging Assistant Interface

6.1 Configuration program download method

- Configuration parameters include:
 - Interface: Used to select the communication interface for downloading and burning.JlinkorUART,Users select based on actual physical connection.
 - COM PortWhen choosingUARTWhen downloading, select the option connected to the chip.COMSlogan, you can clickRefreshButton to proceed COMNumber refresh
 - Uart SpeedWhen choosingUARTWhen downloading, enter the baud rate and recommended download frequency.2M
 - Chip Erase: The default setting isFalseDuring download, erase according to the burning address and content size, select...TrueAt that time, before the program is burned, it will beFlashErase All
 - XtalUsed to select the type of crystal oscillator used on the board.



Interface	Uart
COM Port	COM8
Uart Speed	2000000
JLink Speed	1000
Chip Erase	False
Xtal	40M

picture6.2:Download method interface

6.2 Read and eraseFlashcontent

- ReadFlashofIDClickRead ID
- ReadFlashFixed length value: inStart AddrSet the starting address of the data to be read in the middle;End AddrSet the end address of the data to be read in the middle, and click.Read FlashThe read content will be stored inflash.binThe file path is:BouffaloLabDevCube-1.3.4-win32/flash.bin
- eraseFlashFixed length value: inStart AddrSet the starting address of the data to be erased in the middle;End AddrSet the end address of the data to be erased in the settings, and click [here].Erase Flash(To erase the values of the entire chip, simply check the box.Whole Chip

ID	<input type="text"/>	Read ID
Start Addr	<input type="text" value="0x0"/>	Read Flash
End Addr	<input type="text" value="0x0"/>	Erase Flash <input type="checkbox"/> Whole Chip

picture6.3:Read and eraseFlashinterface

6.3Reading and writing register contents

- Reading the contents of the register: CommandEnter the read command0x05/0x35,LengthEnter the number of bits to be read in the field and click [click].Read RegisterThe read data is displayedValuemiddle
- Contents written to the register: CommandInput write command0x01,LengthEnter the number of digits to be written in the field, and then enter the data to be written in the field. Value In the middle, clickWrite Register

Command	<input type="text"/>	
Length	<input type="text"/>	Read Register
Value	<input type="text"/>	Write Register

picture6.4:Read/Write Register Interface