

Tensor Expression



Select Schedule
Primitives



Final Schedule



Code Lowering



Low level code

Schedule primitives
used in various hardware backends

[Halide] Loop Transformations

[Halide] Thread Binding

[Halide] Compute Locality

[TVM] Special Memory Scope

[TVM] Tensorization

[TVM] Latency Hiding

CPU

Schedule

GPU

Schedule

Accel.

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