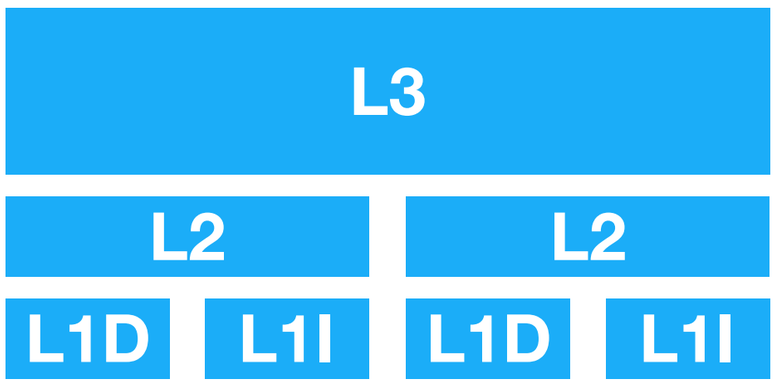
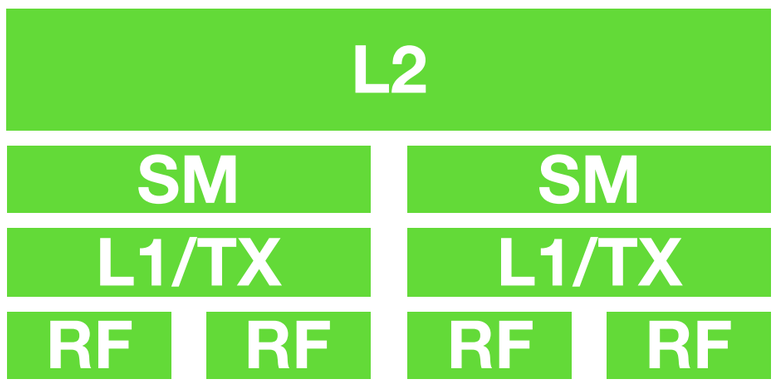
Memory Subsystem Architecture

CPU



implicitly managed

GPU

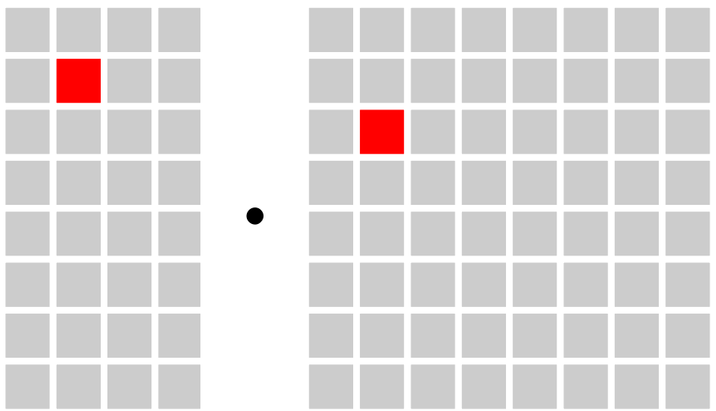


mixed

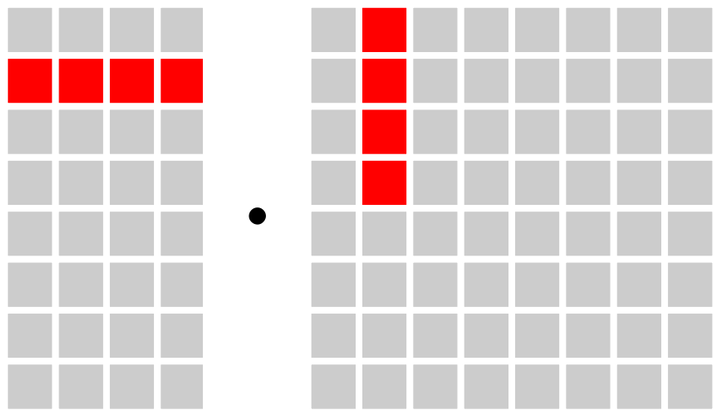
‘TPU’

explicitly managed

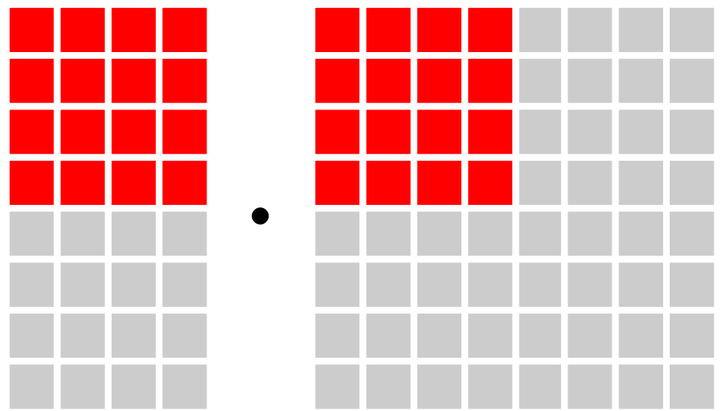
Compute Primitive



scalar



vector



tensor