

Solution to Question 1

Total Resource Matrix $\rightarrow [15, 6, 9, 10]$

Based on the given matrices we can construct the need matrix:

	A	B	C	D
P ₀	7	5	3	4
P ₁	2	1	2	2
P ₂	3	4	4	2
P ₃	2	3	3	1
P ₄	4	1	2	1
P ₅	3	4	3	3

Moreover, the available resource matrix becomes $\rightarrow [6, 3, 5, 4]$

Now check the case for each process

- \rightarrow P₁ can be satisfied, the available matrix $\rightarrow [6, 4, 6, 5]$
- \rightarrow P₂ can be satisfied, " " " $\rightarrow [10, 5, 6, 7]$
- \rightarrow P₃ can be satisfied, " " " $\rightarrow [11, 5, 6, 8]$
- \rightarrow P₄ can be satisfied, " " " $\rightarrow [12, 6, 6, 8]$
- \rightarrow P₅ can be satisfied, " " " $\rightarrow [13, 6, 7, 9]$
- \rightarrow P₀ can be satisfied, " " " $\rightarrow [15, 6, 8, 10]$

Hence, the current state is safe.

Suppose that we granted $P5(3,2,3,3)$, the new need matrix is \Rightarrow

	A	B	C	D
P0	7	5	3	4
P1	2	1	2	2
P2	3	4	4	2
P3	2	3	3	1
P4	4	1	2	1
P5	0	2	0	0

Available resource matrix $\rightarrow [3, 1, 2, 1]$

As can be seen granting $P5(3,2,3,3)$ causes all processes to become deadlocked. So $P5(3,2,3,3)$ should not be granted.

Solutions to Question 2

a) 2 physical memory accesses required. Hence:

$$EAT = 150 \text{ ns} * 2 = 300 \text{ ns}$$

b) In the case of a TLB hit, access time = $150 + 10 = 160 \text{ ns}$

In the case of a TLB miss, access time = $150 + 150 + 10 = 310 \text{ ns}$

$$\text{Hence, } EAT = (0.85) 160 + (0.15) 310 = 136 + 46.5 =$$

$$\boxed{182.5 \text{ ns}}$$

c) In two level paging, if there is a TLB miss access time becomes $\rightarrow 150 + 150 + 150 + 10 = 460 \text{ ns}$. The other case's access time remains the same

$$\text{Hence, } EAT = \boxed{(0.85) 160 + (0.15) 460 = 205 \text{ ns}}$$

Solutions to Question 3

a) $48 - 12 = 36$, so 36 bits are required to store
a frame number
offset

b) Since the offset is 12 bits, page size is 2^{12} bytes, which makes 4 KB

A fourth level table can map $2^9 * 2^{12} = 2^{21}$ bytes = 2 MB

A third level table can map $2^9 * 2^{21} = 2^{30}$ bytes = 1 GB

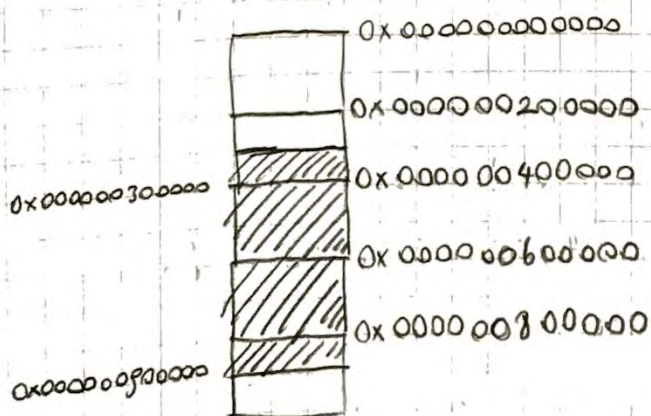
A second level table can map $2^9 * 2^{30} = 2^{39}$ bytes = 512 GB

Since the virtual memory addresses are 48 bits and a fourth level table is 2 MB (uses the last 21 bits of the virtual address), we can say that the following address ranges are mapped to the same fourth level table:

table: $0x000000000000 - 0x000000200000$,

$0x000000200000 - 0x000000400000 \dots$

Hence, if we draw our virtual memory



6 MB is mapped as shown.

Hence, we need 4 fourth level, 1 third level, 1 second level and 1 first level table.

In total 7 tables are required

A table requires $2^9 * 8$ bytes = 4 KB of memory

Hence we need 16 KB of memory for fourth level, 4 KB for third, 4 KB for second, and 4 KB for first level table. In total 28 KB of memory is required

c)

First level table = 1 table is required

Second level table = A second level table can map 512 GB via $0x8000000000$. Hence, the code segment and the data segment are in the first block. However, the stack part is in 30th block. As a result, [2] 2nd level tables are required.

Third level table = A third level table can map 1 GB via $0x40000000$. The code segment is in the first block. However, the data segment starts from block 32 and takes up 2 GB. Hence 1 third-level table is needed for code segment and 2 third-level table is needed for data segment. Lastly we need another third level table for the stack. Hence, we need 4 third-level tables in total.

Fourth level table = 2 MB is mapped by $0x200000$. Code segment is 128 KB and it is enough to have 1 table. For the data segment, $2048 / 2 = 1024$ tables are required. For the stack, $4 / 2 = 2$ tables are required. In total $1 + 1024 + 2 = 1027$ fourth level tables are required.

How much memory is consumed?

First-level page tables: $1 * 4 = 4 \text{ KB}$

Second-level page tables: $2 * 4 = 8 \text{ KB}$

Third-level page tables: $4 * 4 = 16 \text{ KB}$

Fourth-level page tables: $1027 * 4 = 4108 \text{ KB}$

In total: 4136 KB

(4)

Solutions to Question 4

2: 2(1) f

4: 2(1) 4(1) f

1: 2(1) 4(1) 1(1) f

3: 3(1) 4(0) 1(0) f

4: 3(1) 4(1) 1(0)

—— clear ——

6: 3(0) 6(1) 1(0) f

3: 3(1) 6(1) 1(0)

6: 3(1) 6(1) 1(0)

1: 3(1) 6(1) 1(1)

2: 3(0) 6(0) 2(1) f

—— clear ——

1: 1(1) 6(0) 2(0) f

5: 1(1) 5(1) 2(0) f

2: 1(1) 5(1) 2(1)

5: 1(1) 5(1) 2(1)

4: 1(0) 5(0) 4(1) f

—— clear ——

1: 1(1) 5(0) 4(0)

2: 1(0) 2(1) 4(0) f

4: 1(0) 2(1) 4(1)

3: 3(1) 2(1) 4(0) f

b)

2: 2 f

4: 2 4 f

1: 2 4 1 f

3: 3 4 1 f

4: 3 4 1

6: 3 6 1 f

3: 3 6 1

6: 3 6 1

1: 3 6 1

2: 3 2 1 f

1: 3 2 1

5: 5 2 1 f

2: 5 2 1

5: 5 2 1

4: 4 2 1 f

1: 4 2 1

2: 4 2 1

4: 4 2 1

3: 4 2 3 f (smallest page number is replaced)

Note: To save up space, the frames are represented horizontally

i.e.,

1
2
3

 → 1 2 3

Solutions to Question 5

\Rightarrow FCFS

$$5200 - 4500 = 700$$

$$5200 - 2000 = 3200$$

$$9500 - 2000 = 7500$$

$$9500 - 4300 = 5200$$

$$4300 - 1500 = 2800$$

$$5100 - 1500 = 3600$$

$$9600 - 5100 = 4500$$

$$9600 - 4000 = 5600$$

$$4600 - 4000 = 600$$

$$\text{Total} = \boxed{133700}$$

\Rightarrow SCAN

1500 2000 4000 4300 4500 [\gg] 4600 5100 5200 9500 9600

$$(9999 - 4500) + (9999 - 1500) = \boxed{13998}$$

\Rightarrow SSTF

4500 \rightarrow 4600 \rightarrow 4300 \rightarrow 4000 \rightarrow 5100 \rightarrow 5200 \rightarrow 2000

100

300

300

1100

100

3200

\rightarrow 1500 \rightarrow 9500 \rightarrow 9600

500

8000

100

$$100 + 300 + 300 + 1100 + 100 + 3200 + 500 + 8000 + 100 = \boxed{13700}$$

Solutions to Question 6

a) Transfer time for 4KB: $4 \text{ KB} / 30 \text{ MB/s} \approx 0.13 \text{ ms}$

3600 RPM means that 1 rotation takes $\frac{1}{60} \text{ s}$.

Average rotational latency is $\frac{1}{120} \text{ s} \approx 8.3 \text{ ms}$

Then, total time to transfer 4 KB = $8.3 + 4 + 0.13 = 12.43$

Number of I/O per second = 80 ($1000 / 12.43$)

Throughput = $(80 \times 4) / 1024 = 0.3125 \text{ MB/s}$

b) $512 * 4 \text{ KB} / 30 \text{ MB/s} \approx 66.56 \text{ ms}$

Time for I/O = $8.3 + 4 + 66.56 = 78.86 \text{ ms}$

Number of I/Os per second = 12 ($1000 / 78.86$)

Throughput = 24 MB/s

Solutions to Question 7

a) An inode can store $2^{12} / 2^3 = 512$ pointers.

The total file size:

$$10 + 512 + 512^2 + 512^3 = 134480394 \approx 2^{27}$$

Hence,

$$2^{27} * 2^{12} = 2^{39} \text{ bytes} = 512 \text{ GB}$$

b) A leaf index block is able to map $2^9 * 2^{12} = 2^{21} = 2 \text{ MB}$
 $8 \text{ GB} \rightarrow 2^{33} \text{ bytes}$ $2^{33} / 2^{21} = 2^{12} = 4096 \text{ leaf nodes}$

Hence, we need to use the single level index block.

All of the 512 second level index blocks will be used.

Remaining blocks will come from the third level index blocks. To direct pointers do not change the number of used second level index tables

$$4096 - 512 = 3584$$

$$3584 / 512 = 7 \rightarrow \text{number of second level index tables from three level}$$

$$\text{In total} = 7 + 512 = 519$$

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c) i. $2^{15} / 2^{12} = 2^3 = 8$. The offset 2^{15} is in block 8. 1 disk access is needed to retrieve the block.

ii. $2^{23} = 8 \text{ MB}$. A single index node can map $2^{12} \times 2^5 = 2^{17} = 2 \text{ MB}$ which is not sufficient. Hence, two level index structure is required since it can map 1 GB. So, we need to access the first level index block of the two level index, then a second level index block at this structure and then the data block. In total, we need 3 disk accesses.

iii. $2^{32} = 4 \text{ GB}$. A third level index block is required since it can map 512 GB. Using similar logic given in ii, 4 disk accesses are required.