т	1										10 ns										20 lps						
Time reset		1		ı		İ		ı	İ		10 115			İ	İ	1	ı		İ		DJ LIS	T	İ	ı	ı		İ
clock		$\Box$			ΠL																						
mem[0][31:0]									(0000000				<b>X</b> 0000000				<b>X</b> 0000000				X0000000A						
mem[1][31:0]				(000000000		0000000				<b>X</b> 00000002				<b>X</b> 0000000				X0000000				(0000000					
pc_addr[31:0]	00000000		(00000004	X00000008	X00000000	C <b>X</b> 0000001	0 <b>X</b> 000000		8 <b>X</b> 00000000	C <b>X</b> 00000010	(0000001	4 <b>X</b> 000000	08 <b>X</b> 0000000	OC <b>X</b> 0000001	.0 <b>X</b> 0000001	14 <b>X</b> 0000000	0000000 <b>X</b>	C <b>X</b> 0000001		14 <b>X</b> 00000008	<b>X</b> 00000000	X0000001	0 <b>X</b> 0000001		8 <b>X</b> 0000001C	(00000020 <b>)</b> (00	000024 <b>X</b> 0000
	dp						<b>X</b> brn	<b>(</b> dp			brn	<b>X</b> dp			<b>(</b> brn	<b>X</b> dp			<b>X</b> brn	<b>/</b> dp			brn	<b>d</b> p			
	mov			add		<b>X</b> cmp	rsc	<b>X</b> add			rsc	add		cmp	rsc	<b>X</b> add		Cmp	rsc	add		cmp	rsc	andop			
	store					<b>X</b> load		store		load		store		load		<b>X</b> store		load		store		load		store			
offset[23:0]	A00000		<b>X</b> A01000	<b>X</b> 800001	811001	<b>X</b> 510005	FFFFFB	<b>X</b> 800001	811001	510005	FFFFFB	<b>X</b> 800001	811001	510005	FFFFFB	<b>X</b> 800001	<b>X</b> 811001	<b>X</b> 510005	FFFFFB	<b>X</b> 800001	<b>X</b> 811001	510005	FFFFFB	000000			