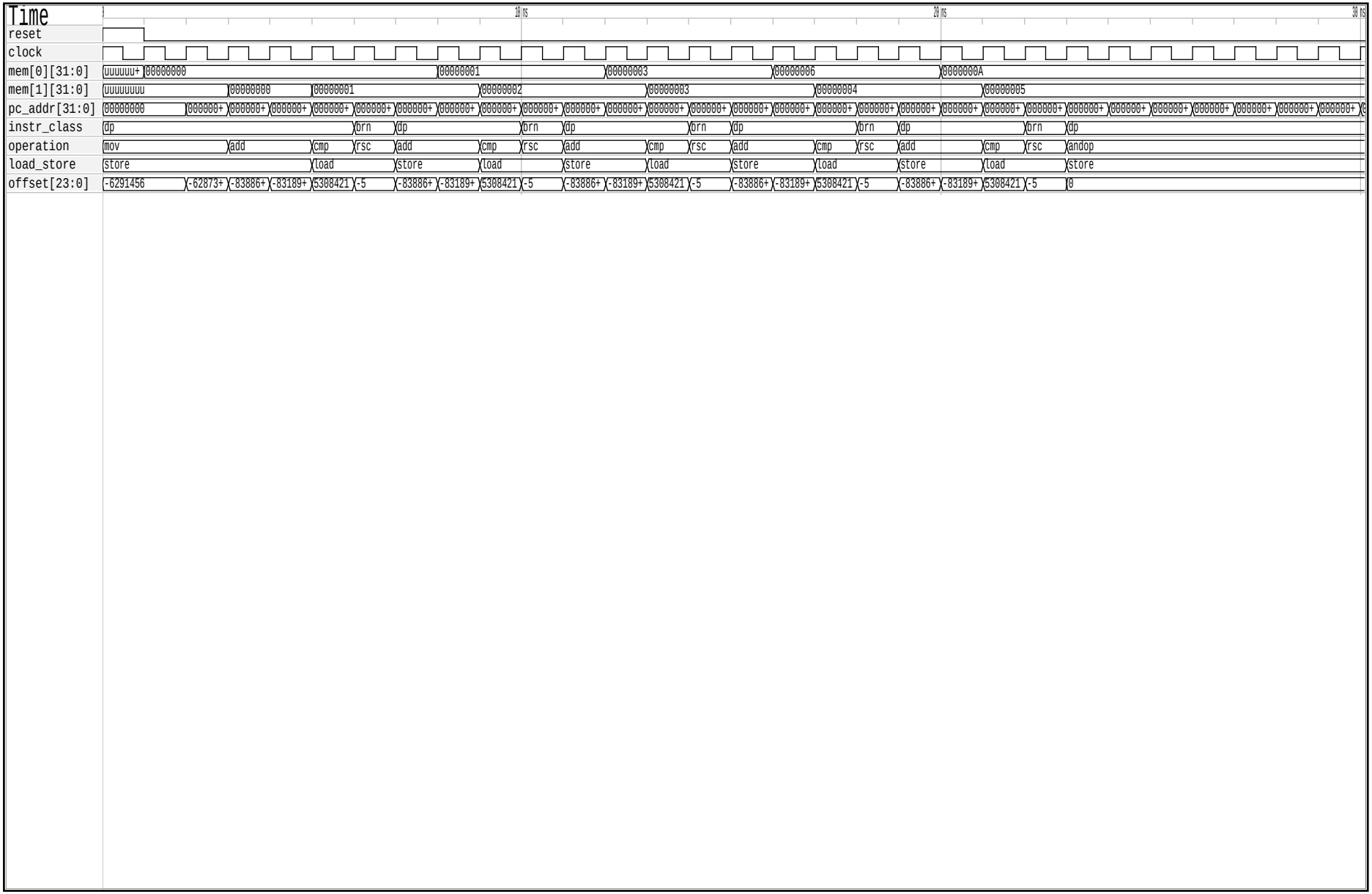
Time
10 ns
20 ns
30 ns
reset
clock
mem[0][31:0]
uuuuuu+ 00000000 00000001 00000003 00000006 0000000A
mem[1][31:0]
uuuuuuuu 00000000 00000001 00000002 00000003 00000004 00000005
pc_addr[31:0]
00000000 000000+ 000000+ 000000+