	,	* 1		41.	11.	11.	ri.	<b>/</b> ±	1.1.	A.J.	A.L	M.1.	11 1	16 J.	11	11	17.1
Time		1111	: 	2 ts	3 tis	4 ts	5 ts	b tis	7 tls	8 tis	9 tis	0 ts	11 ts	12 rts	13 tis	14 ns	15 ns
clock																	
enable																	-
	cmp					Xadd				cmp							=
	7FFFFFF													00000000			
	7FFFFFFF	Y	00000000	00000001	FFFFFFF	7FFFFFF	(00000000	00000001	FFFFFFF	7FFFFFFF	(00000000	00000001	FFFFFFFF	7FFFFFFF	(00000000	00000001	FF
result[31:0]	00000000		7FFFFFFF	X7FFFFFFE	X80000000	XFFFFFFFE	X7FFFFFFF	X800000000	7FFFFFFE	00000000	7FFFFFFF	X7FFFFFFE	X80000000	X80000001	00000000	FFFFFFF	
	00000000		755555	VELLELE	V00000000	FFFFFF	Aleterer	V00000000	VELLERE	100000000	TEFFFFF	VIELELE	V0000000	V00000001	100000000	FFFFFFF	
n																	
Z																L	
С																	
v																	
																	1
																	1
																	1
																	1
	1																