				18					M					**					M			
Time				10 ns					70 15					20 15					40 ns			
clock	$\perp$			$\sqcup \sqcup$		<u> Ш L</u>	<u> </u>	шш		шшш	_ Ц Ц	Ш Ш		$\perp$	<u> </u>	ШЪ			$\perp$	шШ		ШЦ
write_enable																						
write_addr[3:0]	2	X3	X4	X5	X6	X7	X8	X9	XA.	XB	_Xc	XD	XE	F								
data_in[31:0]		X00000003	00000004	00000005	X00000006	X00000007	00000008	<b>X</b> 00000009	X0000000A	X0000000B	X0000000C	<b>X</b> 0000000D	X0000000E	X0000000F								
read_addr1[3:0]	2	X3	<u> </u>	<u> </u>	X6		X8	X9	<b>X</b> A	/B	Xc	<b>X</b> D	XE	F	0							
read_addr2[3:0]			XB	XA.	X9	X8		X6	<b>X</b> 5	X4	X3	X2	_X1	0			X2	X3	X4	X5	X6	X7
data_out1[31:0]		02 <b>u+ (</b> 000000	03 <b>J</b> u+ <b>J</b> 00000004	u+ (0000000	05 u+ 000000	06 <b>(</b> u+ <b>(</b> 000000									OF 00000000							
data_out2[31:0]	uuuuuuuu						00000007	00000006	X00000005	00000004	00000003	(00000002	00000001	00000000		(00000001	(00000002	(00000003	X00000004	<b>X</b> 00000005	00000006	(00000007