

CmpE 443 Term Project Interim Report #001

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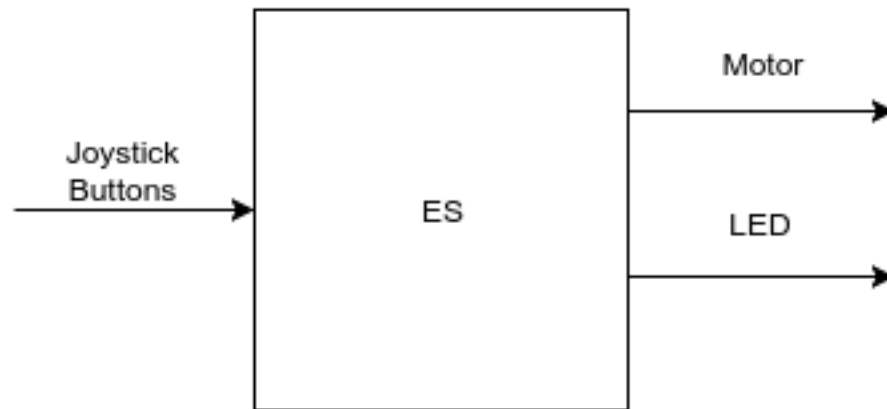
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1 Block Diagram

This is where block diagram will be put. Depict inputs and outputs to the embedded system.



2 Sequence Diagram

Sequence Diagram would be here. You should show the relationships between modules (by sending variables, commands etc.) clearly and thoroughly. Explain each module as in previous lab reports.

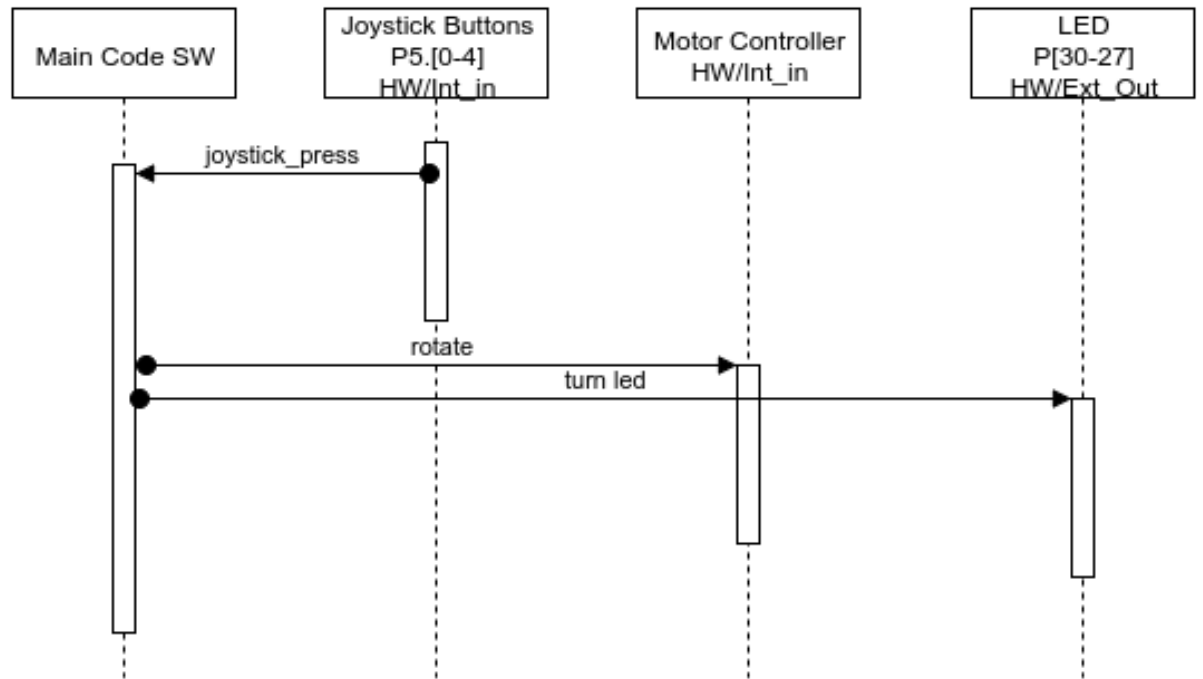


Figure 1: Sequence Diagram

3 LED Connection

LED connection material described in the report (Section 4) definition goes here.

3.1 Table

Front-Left	P1[2] - P30	PWM0[1]	Pin has PWM functionality
Front-Right	P1[3] - P29	PWM0[2]	Pin has PWM functionality
Back-Left	P1[5] - P28	PWM0[3]	Pin has PWM functionality
Back-Right	P1[6] - P27	PWM0[4]	Pin has PWM functionality

Table 1: LEDs pins

3.2 Circuit Schematic

4 Motor - Driver Connection

Motor - Driver Connection material described in the report (Section 5) definition goes here.

4.1 Motor-Driver Connection Description

4.2 Basic Schematic for Connections

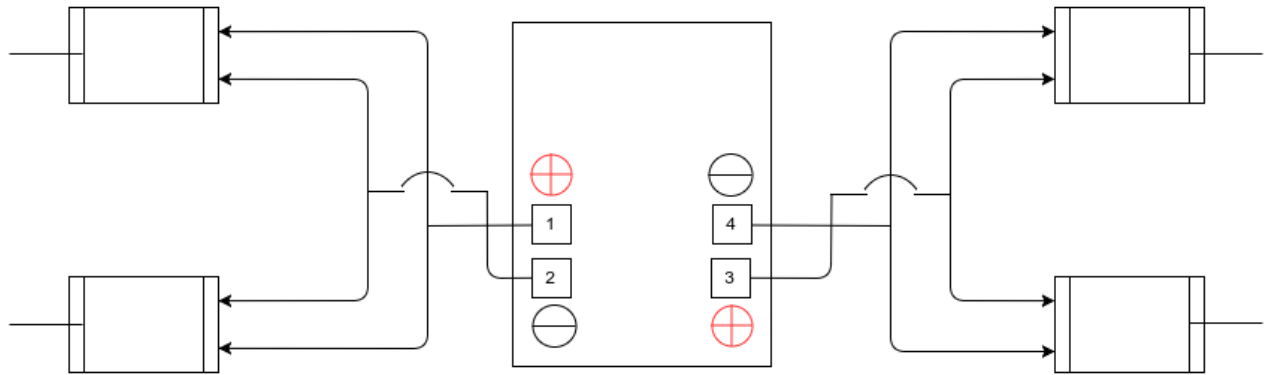


Figure 2: Motor - Driver Connection

5 Driver - Board Connection

Driver - Board Connection material described in the report (Section 6) definition goes here.

ENA	P1[18]	PWM1[1]	Pin has PWM functionality
ENB	P1[20]	PWM1[2]	Pin has PWM functionality
IN1	P1[21]	PWM1[3]	Pin has PWM functionality
IN2	P1[23]	PWM1[4]	Pin has PWM functionality
IN3	P1[24]	PWM1[5]	Pin has PWM functionality
IN4	P1[26]	PWM1[6]	Pin has PWM functionality

Table 2: MotorDriver - Board Pins

Here is an example figure for you to use the LaTeX format.

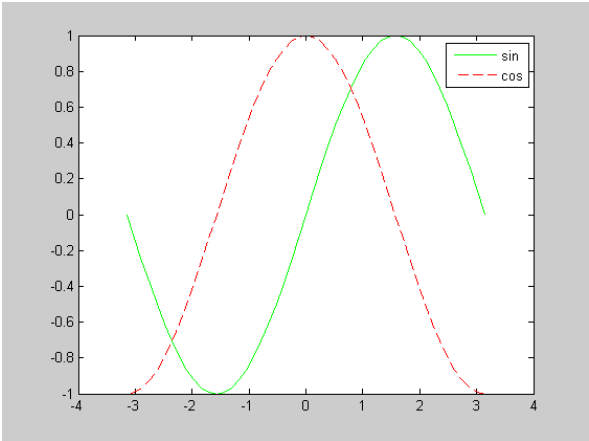


Figure 3: Sin and Cosine.

Here is an example table for you to use the LaTeX format.

Algorithm	Feature 1	Feature 2	Feature 3	Feature n
Alg 1	1	2	23	68.7%
Alg 2	9	2	37	75.68%
Alg 3	10	3	51	96.34%

Table 3: Should be a caption