# Purpose: Simple Makefile

# Creation: 5-14-2011

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CC=gcc

CFLAGS=-O

EXEC=hello

DEST=~/bin

$(EXEC): $(EXEC).o

@echo "Linking ..."

$(CC) $(CFLAGS) -o $(EXEC) $(EXEC).o

$(EXEC).o: $(EXEC).c

@echo "Compiling ..."

$(CC) $(CFLAGS) -c -o $(EXEC).o $(EXEC).c

install: $(EXEC)

@echo "Installing ..."

install $(EXEC) $(DEST)

clean:

@echo "Cleaning ..."

rm -f \*.o

clobber: clean

@echo "Clobbering ..."

rm -f $(EXEC)

run: $(EXEC)

@echo "Executing ..."

$(DEST)/$(EXEC)

all: install run