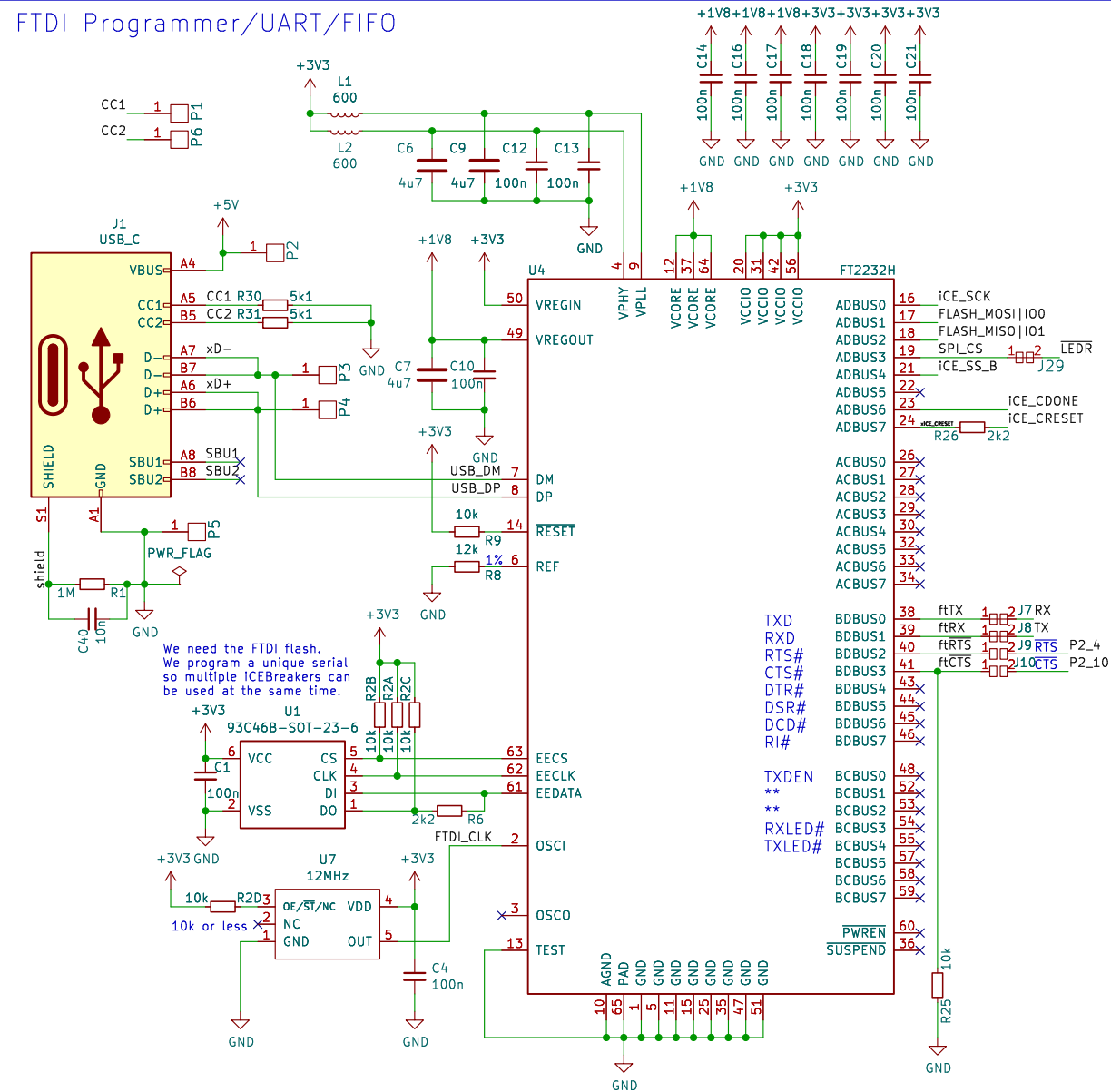
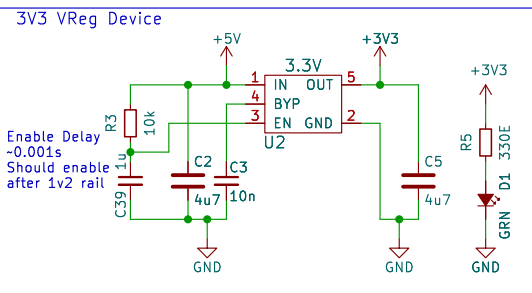


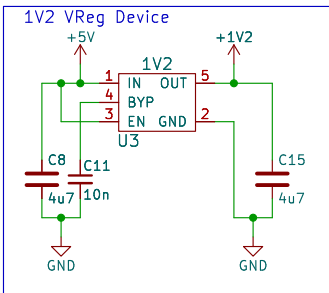
FTDI Programmer/UART/FIFO



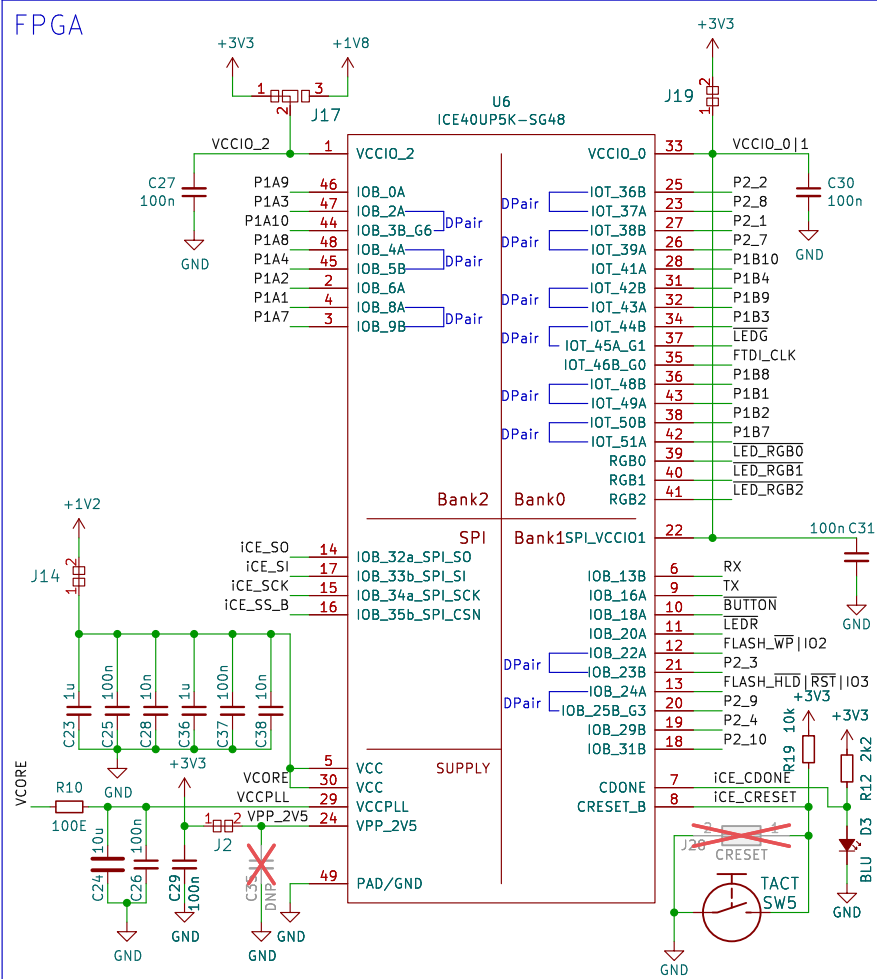
3V3 VReg Device



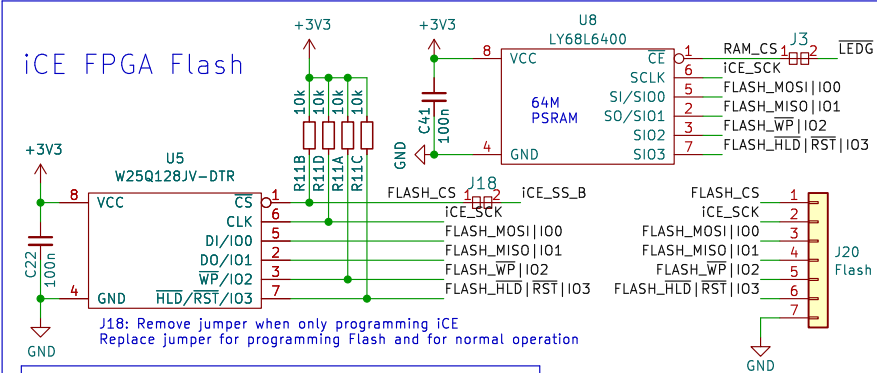
1V2 VReg Device



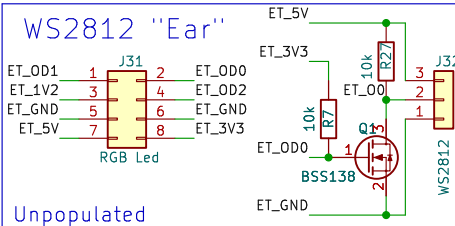
FPGA



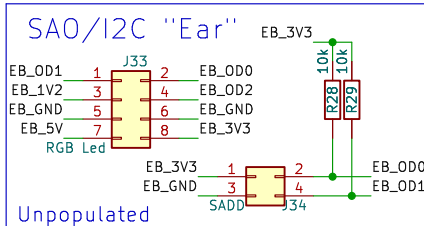
iCE FPGA Flash



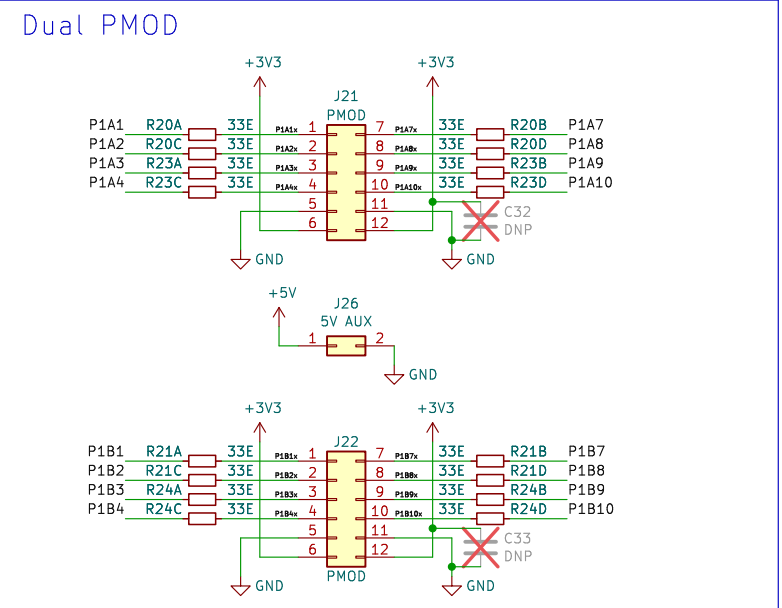
WS2812 "Ear"



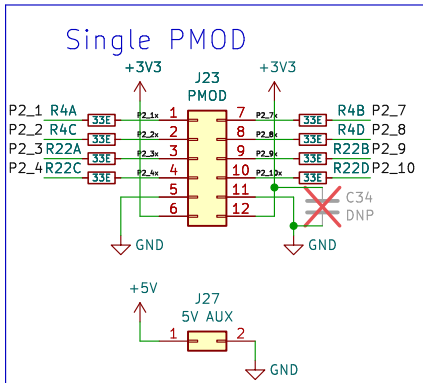
SA0/I2C "Ear"



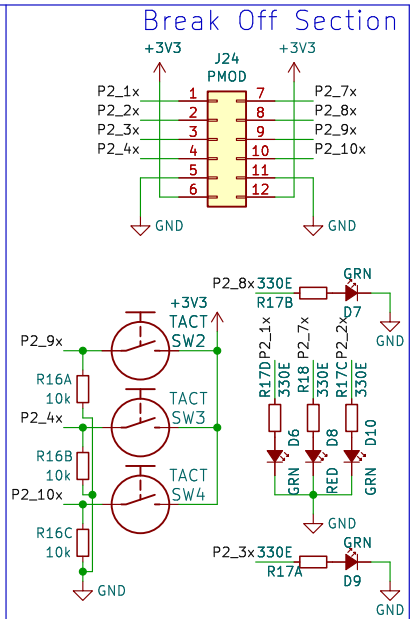
Dual PMOD



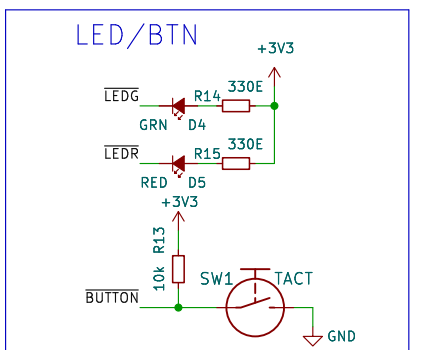
Single PMOD



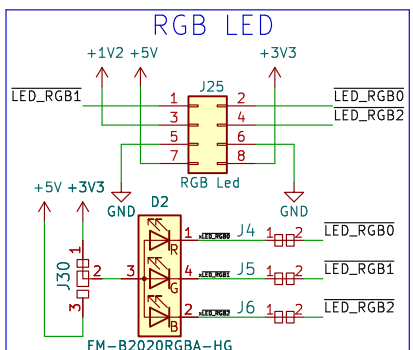
Break Off Section



LED / BTN



RGB LED



- * Changes from V1.0
- * ☒ Switch to USB-C
- * ☒ Switch to iCEBreaker-Bitsy RGB led
- * ☒ Add optional PSRAM
- * ☒ Add reset button
- * ☒ Remove FIFO Jumpers (Not useful. SPI on the first port is faster.)
- * ☒ Remove Zener for VPP (it does not work anyways ;)
- * ☒ Length match Pmod IO
- * ☒ Add CC1&2 test pads
- * ☒ Update USB connector stencil footprint
- * ☒ Make updates to increase stencil reliability

License: CC-BY-SA V4.0
 2019-2024 (C) Piotr Esden-Tempski <piotr@esden.net>
 2019-2024 (C) 1BitSquared <info@1bitsquared.com>
1BitSquared

Sheet: /
 File: icebreaker.kicad_sch

Title: iCEBreaker

Size: A3	Date:
KiCad E.D.A. 8.0.8	