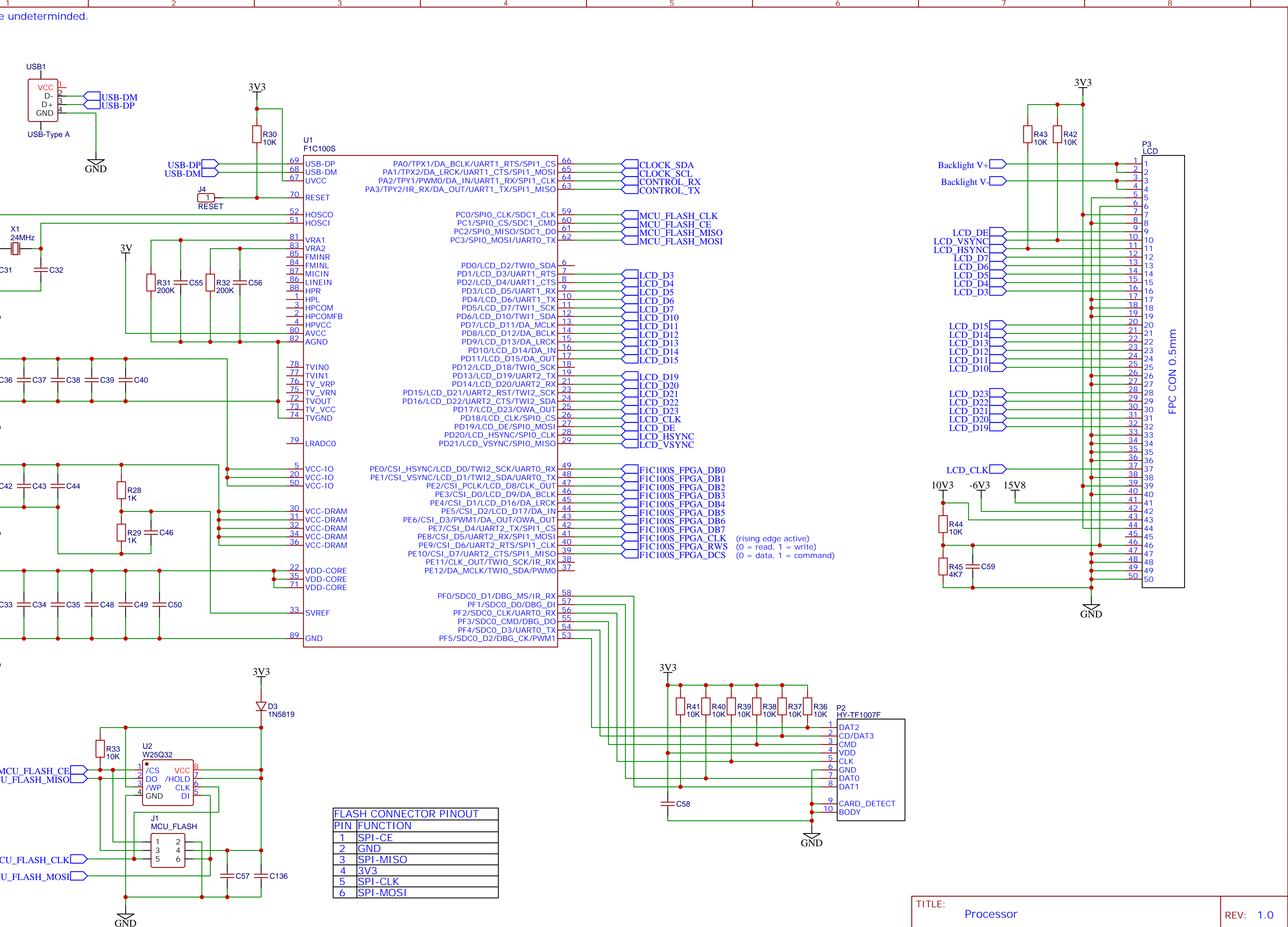
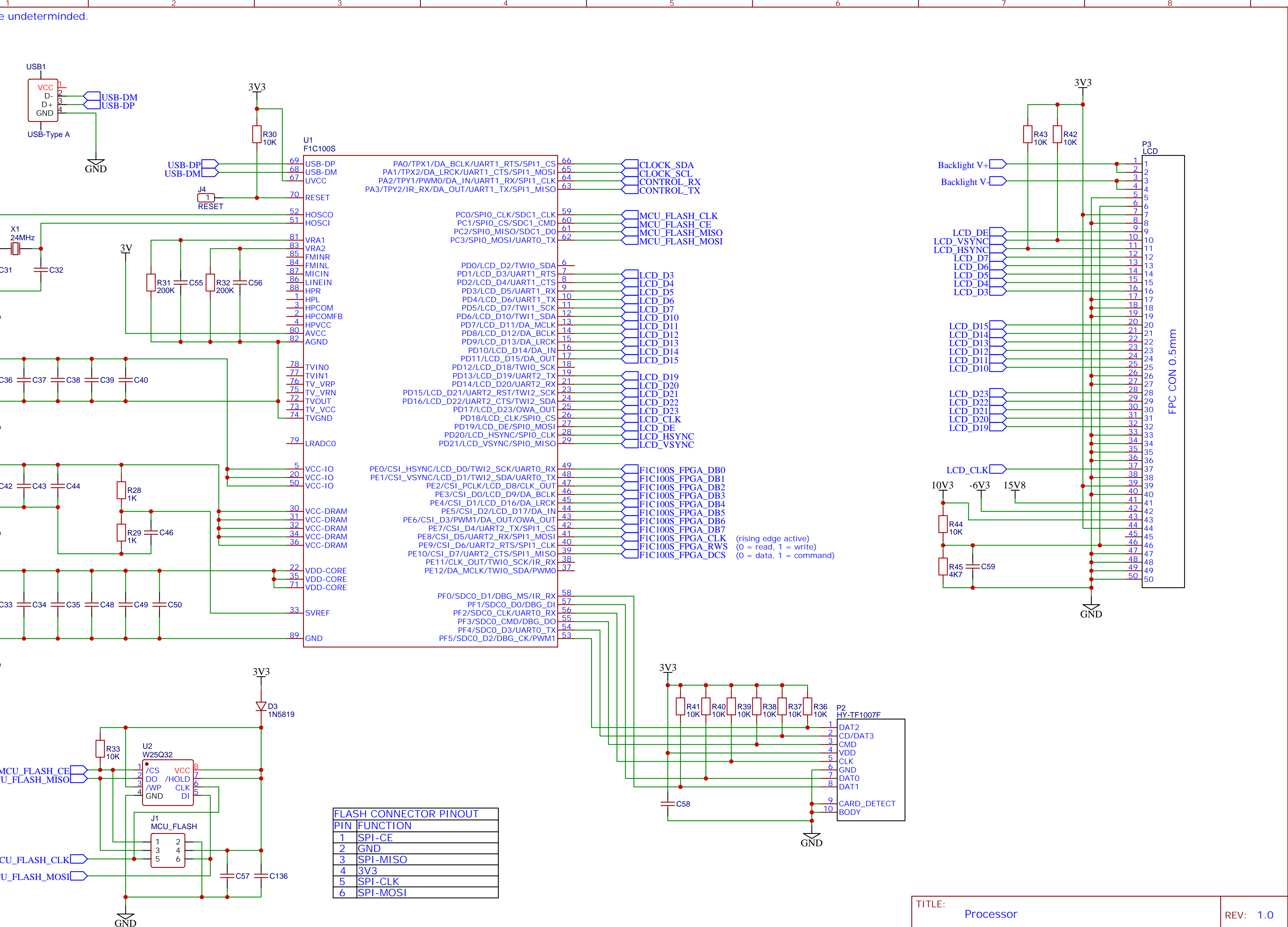



| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
|---|---|---|---|---|---|---|---|

[illegible]

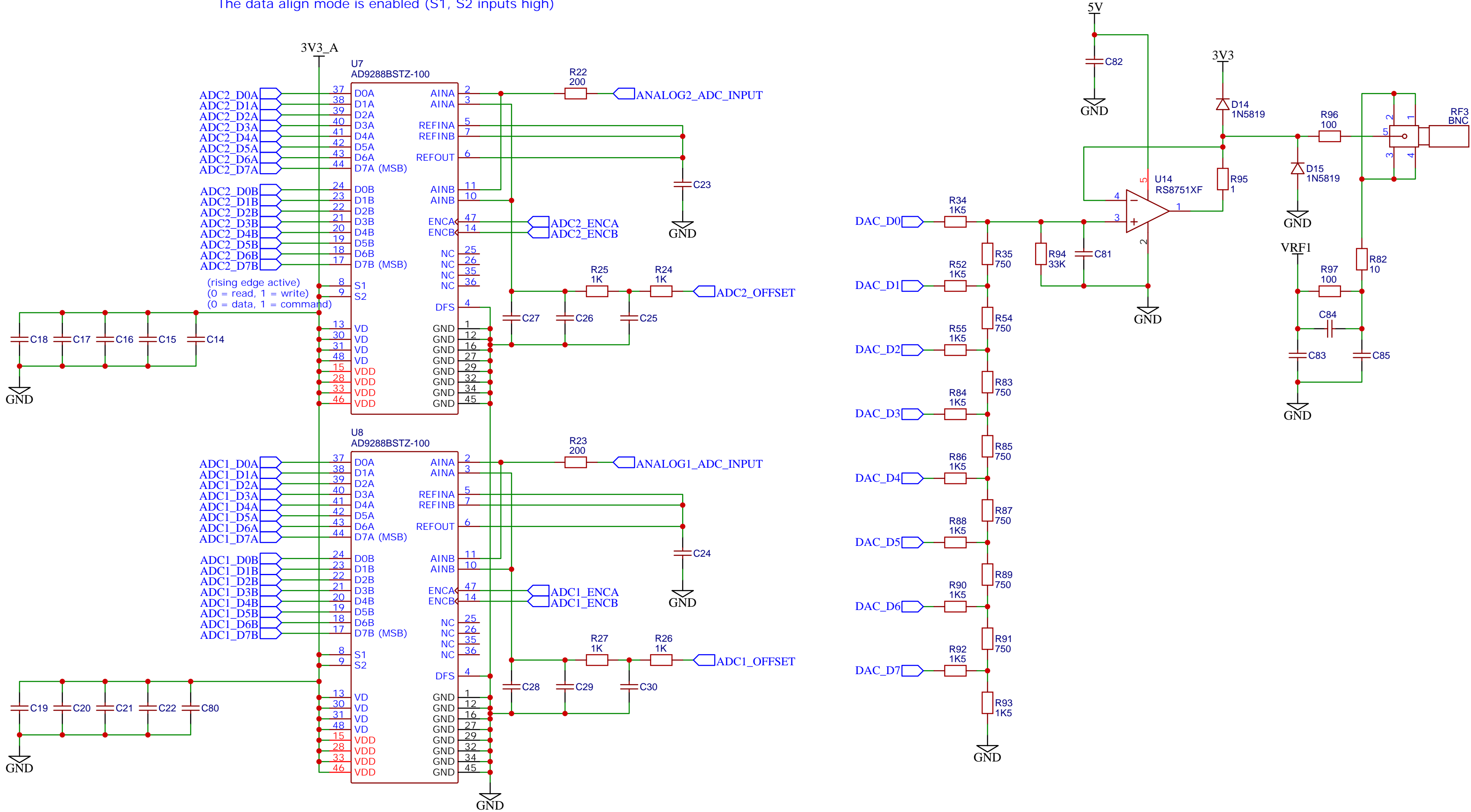
E



| | | |
|---|---------------------------------|------------|
| TITLE: Analog Input | | REV: 1.0 |
|  | Company: | Sheet: 4/6 |
| | Date: 2022-05-08 Drawn By: | |

Capacitors are undetermined.

For the ADC's offset binary output is selected (DFS input low)
The data align mode is enabled (S1, S2 inputs high)



Capacitors are undetermined.

