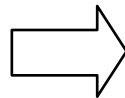


Generated codes by NVCC compiler

```
1:                                /*0x08dc109c14801410*/
2:
3:  FFMA R28, R41, R15, R28;      /*0xcc007000079ca472*/
4:  LDS.128 R12, [0x40];          /*0x7a700000201ffc32*/
5:
6:  FFMA R39, R41, R9, R39;       /*0xcc009c00049ca49e*/
7:  FFMA R29, R41, R10, R29;      /*0xcc007400051ca476*/
8:  FFMA R32, R41, R11, R32;      /*0xcc008000059ca482*/
9:
10: LDS.128 R8, [0x50];           /*0x7a700000281ffc22*/
11: IADD R4.CC, R4, R21;          /*0xe08400000a9c1012*/
```

(a)



The optimized codes

```
1: /*control codes */
2: -::D:-:05      FFMA R31, R150, R147, R31;
3: -::D:-:04      FFMA R27, R150, R146, R27;
4: -::D:-:05      FFMA R6, R149, R146, R6;
5:
6: -::D:-:04      FFMA R7, R148, R147, R7;
7: -::D:-:05      FFMA R2, R149, R147, R2;
8:
9: -::D:-:04      FFMA R3, R148, R146, R3;
10: T:-:D:S:00     LDS.64 R168, [R222+0x400];
```

(b)