A Memory-Bounded, Deterministic and Terminating Semantics for the Synchronous Programming Language Céu

Rodrigo C. M. Santos
PUC-Rio, Brazil
rsantos@inf.puc-rio.br

Guilherme F. Lima
PUC-Rio, Brazil
glima@inf.puc-rio.br

Francisco Sant'Anna
UERJ, Brazil
francisco@ime.uerj.br

Roberto Ierusalimschy
PUC-Rio, Brazil
roberto@inf.puc-rio.br

Edward H. Haeusler
PUC-Rio, Brazil
hermann@inf.puc-rio.br

Abstract

Céu is a synchronous programming language for embedded soft real-time systems. It focuses on control-flow safety features in the presence of shared-memory concurrency and abortion of lines of execution, while enforcing memory-bounded, deterministic, and terminating reactions to the environment. In this work, we present a small-step structural operational semantics for Céu and a proof that reactions have the properties enumerated above: that for a given arbitrary timeline of input events, multiple executions of the same program always react in bounded time and arrive at the same final finite memory state.

Keywords Determinism, Termination, Operational semantics, Synchronous languages

1 Introduction

Céu [18, 19] is a Esterel-based [9] programming language for embedded soft real-time systems that aims to offer a concurrent, safe, and expressive alternative to C with the characteristics that follow:

Reactive: code only executes in reactions to events.

Structured: programs use structured control mechanisms, such as `await` (to suspend a line of execution), and `par` (to combine multiple lines of execution).

Synchronous: reactions run atomically and to completion on each line of execution, i.e., there's no implicit preemption or real parallelism.

Structured reactive programming lets developers write code in direct style, recovering from the inversion of control imposed by event-driven execution [1, 14, 17]. Synchronous languages offer a simple run-to-completion execution model that enables deterministic execution and make formal reasoning tractable. For this reason, it has been successfully adopted in safety-critical real-time embedded systems [3].

Previous work in the context of embedded sensor networks evaluates the expressiveness of Céu in comparison to event-driven code in C and attests a reduction in source code size (around 25%) with a small increase in memory usage (around 5–10%) [19]. Céu has also been used in the context of multimedia systems [20] and games [**?**].

Céu inherits the synchronous and imperative mindset of Esterel but adopts a simpler semantics with fine-grained execution control. The list that follows summarizes the semantic peculiarities of Céu [18]:

- Fine-grained, intra-reaction deterministic execution, which makes Céu fully deterministic.
- Stack-based execution for internal events, which provides a limited but memory-bounded form of subroutines.
- Finalization mechanism for lines of execution, which makes abortion safe with regard to external resources.
- First-class timers with dedicated syntax, which provides automatic synchronization for multiple timers running simultaneously.

In this work, we present a formal small-step structural operational semantics for Céu and prove that it enforces memory-bounded, deterministic, and terminating reactions to the environment, i.e., that for a given arbitrary timeline of input events, multiple executions of the same program always react in bounded time and arrive at the same final finite memory state. Conceiving a formal semantics for Céu leads to a number of capabilities and outcomes as follows:

1. Understanding, finding corner cases, and stabilizing the language. After the semantics was complete and discussed in extent in our group, we found a critical bug in the order of execution of statements.
2. Explaining core aspects of the language in a reduced, precise, and unambiguous way. This is particularly important when comparing languages that are similar in the surface (e.g., Céu and Esterel).
3. Implementing the language. A small-step operational semantics describes an abstract machine that is close to a concrete implementation, which we concretized in Haskell for testing. Also, the current real-world implementation of the Céu scheduler is based on the formal semantics presented in this paper.

4. Proving properties for particular or all programs in the language. For instance, in this work, we prove that all programs in Céu are memory bounded, deterministic, and react in finite time.

The last item is particularly important in the context of constrained embedded systems:

Memory Boundedness: At compile time, we can ensure that the program fits in the device's restricted memory and that it will not grow unexpectedly during runtime.

Deterministic Execution: We can simulate an entire program execution providing an input history with the guarantee that it will always have the same behavior. This can be done in negligible time in a controlled development environment before deploying the application to the actual device (e.g., by multiple developers in standard desktops).

Terminating Reactions: Real-time applications must guarantee responses within specified deadlines. A terminating semantics enforces upper bounds for all reactions and guarantees that programs always progress with the time.

The rest of the paper is organized as follows: Section 2 is a review of the main characteristics of Céu based on previous work [18, 19], namely, deterministic event-driven execution, safe shared-memory concurrency, abortion and finalization for concurrent lines of execution, and first-class synchronized timers. Section 3 proposes a formal small-step operational semantics for Céu that encompasses all peculiarities of the language. Section 4 presents the proofs for the properties of memory boundedness, deterministic execution, and terminating reactions, which apply to all programs in Céu. Section 5 compares the semantics of Céu with related synchronous languages. Section 6 concludes the paper.

2 Céu

Céu [18, 19] is a synchronous reactive language in which programs evolve in a sequence of discrete reactions to external events. It is designed for control-intensive applications, supporting concurrent lines of execution, known as *trails*, and instantaneous broadcast communication through events. Computations within a reaction (such as expressions, assignments, and system calls) are also instantaneous considering the synchronous hypothesis [10]. Céu provides an `await` statement that blocks the current running trail allowing the program to execute its other trails; when all trails are blocked, the reaction terminates and control returns to the environment.

In Céu, every execution path within loops must contain at least a `break` or `await` statement to an external input event [6, 19]. This restriction, which is statically checked by the compiler, ensures that every reaction runs in bounded time, eventually terminating with all trails blocked in `await` statements. Céu has an additional restriction, which it shares with Esterel and synchronous languages in general [4]: computations that take a non-negligible time to run (e.g., cryptography or image processing algorithms) violate the zero-delay hypothesis, and thus cannot be directly implemented.

```
// Declarations
input ⟨type⟩ ⟨id⟩; // declares an external input event
event ⟨type⟩ ⟨id⟩; // declares an internal event
var   ⟨type⟩ ⟨id⟩; // declares a variable

// Event handling
⟨id⟩ = await ⟨id⟩; // awaits event and assigns the received value
emit ⟨id⟩(⟨expr⟩); // emits event passing a value

// Control flow
⟨stmt⟩ ; ⟨stmt⟩                                  // sequence
if ⟨expr⟩ then ⟨stmt⟩ else ⟨stmt⟩ end            // conditional
loop do ⟨stmt⟩ end                               // repetition
every ⟨id⟩ in ⟨id⟩ do ⟨stmt⟩ end                 // event iteration
finalize [⟨stmt⟩] with ⟨stmt⟩ end                // finalization

// Logical parallelism
par/or  do ⟨stmt⟩ with ⟨stmt⟩ end   // aborts if any side ends
par/and do ⟨stmt⟩ with ⟨stmt⟩ end   // terminates if all sides end

// Assignment & Integration with C
⟨id⟩ = ⟨expr⟩;    // assigns a value to a variable
_⟨id⟩(⟨exprs⟩)    // calls a C function (id starts with '_')
```

Listing 1. The concrete syntax of Céu.

Listing 1 shows a compact reference of Céu.

Listing 2 shows a complete example in Céu that toggles a LED whenever a radio packet is received, terminating on a button press always with the LED off. The program first declares the `BUTTON` and `RADIO_RECV` as input events (ln 1–2). Then, it uses a `par/or` composition to run two activities in parallel: a single-statement trail that waits for a button press before terminating (ln 4), and an endless loop that toggles the LED on and off on radio receives (ln 9–14). The `finalize` clause (ln 6–8) ensures that, no matter how its enclosing trail terminates, the LED will be unconditionally turned off (ln 7).

The `par/or` composition, which stands for a *parallel-or*, provides an orthogonal abortion mechanism [4] in which its composed trails do not know when and how they are aborted (i.e., abortion is external to them). The finalization mechanism extends orthogonal abortion to activities that use stateful resources from the environment (such as files and network handlers), as we discuss in Section 2.3.

In Céu, any identifier prefixed with an underscore (e.g., `_led`) is passed unchanged to the underlying C compiler. Therefore, access to C is straightforward and syntactically traceable. To ensure that programs operate under the synchronous hypothesis, the compiler environment should only provide access to C operations that can be assumed to be

instantaneous, such as non-blocking I/O and simple data structure accessors.[1]

```
1  input void BUTTON;
2  input void RADIO_RECV;
3  par/or do
4      await BUTTON;
5  with
6      finalize with
7          _led(0);
8      end
9      loop do
10         _led(1);
11         await RADIO_RECV;
12         _led(0);
13         await RADIO_RECV;
14     end
15 end
```

Listing 2. A Céu program that toggles a LED on receive, terminating on a button always with the LED off.

2.1 External and Internal Events

Céu defines time as a discrete sequence of reactions to unique external input events received from the environment. Each input event delimits a new logical unit of time that triggers an associated reaction. The life-cycle of a program in Céu can be summarized as follows [19]:

(i) The program initiates a "boot reaction" in a single trail (parallel constructs may create new trails).
(ii) Active trails execute until they await or terminate, one after another. This step is called a *reaction chain*, and always runs in bounded time.
(iii) When all trails are blocked, the program goes idle and the environment takes control.
(iv) On the occurrence of a new external input event, the environment awakes *all* trails awaiting that event, and the program goes back to step (ii).

A program must react to an event completely before handling the next one. By the synchronous hypothesis, the time the program spends in step (ii) is conceptually zero (in practice, negligible). Hence, from the point of view of the environment, the program is always idle on step (iii). In practice, if a new external input event occurs while a reaction executes, the event is saved on a queue, which effectively schedules it to be processed in a subsequent reaction.

2.1.1 External Events and Discrete Time

The sequential processing of external input events induces a discrete notion of time in Céu, as illustrated in Figure 1. The

[1] The actual implementation of Céu supports a command-line option that accepts a whitelist of library calls. If a program tries to use a function not in the list, the compiler raises an error.

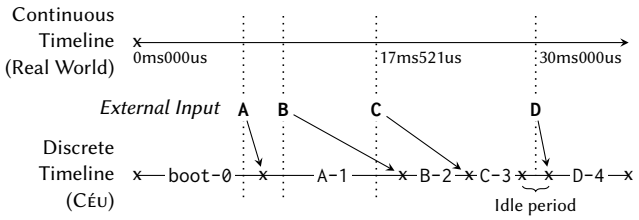

Figure 1. The discrete notion of time in Céu.

continuous timeline shows an absolute reference clock with "physical timestamps" for the event occurrences (e.g., event `C` occurs at 17ms521us). The discrete timeline shows how the same occurring events fit in the logical notion of time of Céu. The boot reaction `boot-0` happens before any input, at program startup. Event `A` "physically" occurs during `boot-0` but, because time is discrete, its corresponding reaction only executes afterwards, at logical instant `A-1`. Similarly, event `B` occurs during `A-1` and its reaction is postponed to execute at `B-2`. Event `C` also occurs during `A-1` but its reaction must also wait for `B-2` to execute and so it is postponed to execute at `C-3`. Finally, event `D` occurs during an idle period and can start immediately at `D-4`.

Unique input events imply mutually exclusive reactions, which execute atomically and never overlap. Automatic mutual exclusion is a prerequisite for deterministic reactions as we discuss in Section 3.

In practice, the synchronous hypothesis for Céu holds if reactions execute faster than the rate of incoming input events. Otherwise, the program would continuously accumulate delays between physical occurrences and actual reactions for the input events. Considering the context of soft real-time systems, postponed reactions might be tolerated as long as they are infrequent and the application does not take too long to catch up with real time. Note that the synchronous semantics is also the norm in typical event-driven systems, such as event dispatching in UI toolkits, game loops in game engines, and clock ticks in embedded systems.

2.1.2 Internal Events as Subroutines

In Céu, queue-based processing of events applies only to external input events, i.e., events submitted to the program by the environment. Internal events, which are events generated internally by the program via `emit` statements, are processed in a stack-based manner. Internal events provide a fine-grained execution control, and, because of their stack-based processing, can be used to implement a limited form of subroutines, as illustrated in Listing 3.

In the example, the "subroutine" `inc` is defined as an event iterator (ln 4–6) that continuously awaits its identifying event (ln 4), and increments the value passed by reference (ln 5). A trail in parallel (ln 8–11) invokes the subroutine through two consecutive `emit` statements (ln 9–10). Given the stack-based execution for internal events, as the first

emit executes, the calling trail pauses (ln 9), the subroutine awakes (ln 4), runs its body (yielding v=2), iterates, and awaits the next "call" (ln 4, again). Only after this sequence does the calling trail resumes (ln 9), makes a new invocation (ln 10), and passes the assertion test (ln 11).

```
1  event int* inc;          // declares subroutine "inc"
2  par/or do
3      var int* p;
4      every p in inc do // implements "inc" as event iterator
5          *p = *p + 1;
6      end
7  with
8      var int v = 1;
9      emit inc(&v);        // calls "inc"
10     emit inc(&v);        // calls "inc"
11     _assert(v==3);       // asserts result after the two returns
12 end
```

Listing 3. A "subroutine" that increments its argument.

Céu also supports nested `emit` invocations, e.g., the body of the subroutine `inc` (ln 5) could `emit` an event targeting another subroutine, creating a new level in the stack. We can think of the stack as a record of the nested, fine-grained internal reactions that happen inside the same outer reaction to a single external event.

This form of subroutines has a significant limitation that it cannot express recursion, since an `emit` to itself is always ignored as a running trail cannot be waiting on itself. That being said, it is this very limitation that brings important safety properties to subroutines. First, they are guaranteed to react in bounded time. Second, memory for locals is also bounded, not requiring data stacks.

At first sight, the constructions "`every e do ⟨···⟩ end`" and "`loop do await e; ⟨···⟩ end`" seem to be equivalent. However, the `loop` variation would not compile since it does not contain an external input `await` (`e` is an internal event). The `every` variation compiles because event iterators have an additional syntactic restriction that they cannot contain `break` or `await` statements. This restriction guarantees that an iterator never terminates from itself and, thus, always awaits its identifying event, essentially behaving as a safe blocking point in the program. For this reason, the restriction that execution paths within loops must contain at least one external `await` is extended to alternatively contain an `every` statement.

2.2 Shared-Memory Concurrency

Embedded applications make extensive use of global memory and shared resources, such as through memory-mapped registers and system calls to device drivers. Hence, an important goal of Céu is to ensure a reliable behavior for programs with concurrent lines of execution sharing memory and interacting with the environment.

In Céu, when multiple trails are active in the same reaction, they are scheduled in lexical order, i.e., in the order they appear in the program source code. For instance, consider the examples in Figure 2, both defining shared variables (ln 2), and assigning to them in parallel trails (ln 5,8).

In Listing 4, the two assignments to `x` can only execute in reactions to different events `A` and `B` (ln 4,7), which cannot occur simultaneously by definition. Hence, for the sequence of events `A->B`, `x` becomes `4` (`(1+1)*2`), while for `B->A`, `x` becomes `3` (`(1*2)+1`).

In Listing 5, the two assignments to `y` are simultaneous because they execute in reaction to the same event `A` (ln 4,7). Since Céu employs lexical order for intra-reaction statements, the execution is still deterministic, and `y` always becomes `4` (`(1+1)*2`). However, note that an apparently innocuous change in the order of trails modifies the behavior of the program. To mitigate this threat, Céu performs concurrency checks at compile time to detect conflicting accesses to shared variables: if a variable is written in a trail segment, then a concurrent trail segment cannot access that variable [19]. Nonetheless, the static checks are optional and are not a prerequisite for the deterministic semantics of the language.

```
input void A, B;
var int x = 1;
par/and do
    await A;
    x = x + 1;
with
    await B;
    x = x * 2;
end
```

Listing 4.

```
1  input void A;
2  var int y = 1;
3  par/and do
4      await A;
5      y = y + 1;
6  with
7      await A;
8      y = y * 2;
9  end
```

Listing 5.

Figure 2. Shared-memory concurrency in Céu: Listing 4 is never concurrent because the trails access `x` atomically in different reactions; Listing 5 is concurrent, but still deterministic, because both trails access `y` atomically in the same reaction.

2.3 Abortion and Finalization

The `par/or` of Céu is an orthogonal abortion mechanism because the two sides in the composition need not be tweaked with synchronization primitives nor state variables to affect each other. In addition, abortion is *immediate* in the sense that it executes atomically inside the current micro reaction. Immediate orthogonal abortion is a distinctive feature of synchronous languages and cannot be expressed effectively in traditional (asynchronous) multi-threaded languages [4, 15].

However, aborting lines of execution that deal with resources may lead to inconsistencies. Therefore, Céu provides

a `finalize` construct to unconditionally execute a series of statements even if the enclosing block is externally aborted.

Céu also enforces, at compile time, the use of `finalize` for system calls that deal with pointers representing resources, as illustrated in the two examples of Figure 3. If Céu *passes* a pointer to a system call (Listing 6, ln 5), the pointer represents a *local* resource (ln 2) that requires finalization (ln 7). If Céu *receives* a pointer from a system call return (Listing 7, ln 4), the pointer represents an *external* resource (ln 2) that requires finalization (ln 6).

Céu tracks the interaction of system calls with pointers and requires finalization clauses to accompany them. In Listing 6, the local variable `msg` (ln 2) is an internal resource passed as a pointer to `_send` (ln 5), which is an asynchronous call that transmits the buffer in the background. If the block aborts (ln 11) before receiving an acknowledge from the environment (ln 9), the local `msg` goes out of scope and the external transmission now holds a *dangling pointer*. The finalization ensures that the transmission also aborts (ln 7). In Listing 7, the call to `_fopen` (ln 4) returns an external file resource as a pointer. If the block aborts (ln 12) during the `await A` (ln 9), the file remains open as a *memory leak*. The finalization ensures that the file closes properly (ln 6). In both cases, the code does not compile without the `finalize`.[2]

```
par/or do
   var _msg_t msg;
   ⟨···⟩    // prepare msg
   finalize
      _send(&msg);
   with
      _cancel(&msg);
   end
   await SEND_ACK;
with
   ⟨···⟩
end
```

Listing 6. Local resource.

```
1  par/or do
2     var _FILE* f;
3     finalize
4        f = _fopen(...);
5     with
6        _fclose(f);
7     end
8     _fwrite(..., f);
9     await A;
10    _fwrite(..., f);
11 with
12    ⟨···⟩
13 end
```

Listing 7. External resource.

Figure 3. Céu enforces the use of finalization to prevent dangling pointers and memory leaks.

The finalization mechanism of Céu is fundamental to preserve the orthogonality of the `par/or` construct since the clean up code is encapsulated in the aborted trail itself.

2.4 First-Class Timers

Embedded systems typically rely on activities that react to *wall-clock time*[3], such as timeout watchdogs and periodic sensor readings [7]. Céu supports wall-clock timers through the `await` statement [18], as illustrated in Listing 8. The `await` blocks the current running trail for the specified amount of time (ln 2,4).

However, underlying system timers do not activate programs immediately with zero delay due to intrinsic overhead, such as OS scheduling, interrupts with higher priorities, or even losses due to clock resolutions. We define the difference between a requested timeout and the actual expiring time as the *residual delta time (delta)*. Without explicit manipulation, the recurrent use of timed activities in a row (or in a loop) may accumulate a considerable amount of deltas that can lead to incorrect behavior in programs.

Céu handles deltas automatically, as illustrated in Listing 8. Suppose that after the first `await 10ms` request (ln 2), the underlying system gets busy and takes 15ms to notify Céu. The scheduler will notice that the `await` has not only already expired, but is delayed with `delta=5ms`. The awaiting trail awakes, sets `v=1` (ln 3), and then invokes `await 1ms` (ln 4). Notice that non-awaiting statements are considered to take no time in the synchronous model (e.g, ln 3). Since the current delta is still higher than the requested timeout (i.e. $5ms > 1ms$), the trail is rescheduled for execution, now with `delta=4ms`.

Delta compensation also makes timers in parallel to be perfectly synchronized. In the example in Listing 9, although the scheduler cannot guarantee that the first trail terminates exactly in 11ms (ln 2,4), it can at least ensure that it terminates before the second trail (yielding `v=1`), because $10 + 1 < 12$. A similar program in a language without first-class support for timers would depend on the execution timings for the code marked as <...> (ln 3), making the reasoning about the execution behavior more difficult.

```
var int v;
await 10ms;
v = 1;
await 1ms;
v = 2;
```

Listing 8. Wall-clock await.

```
1  par/or do
2    await 10ms;
3    <...> // non awaiting stmts
4    await  1ms;
5    v = 1;
6  with
7    await 12ms;
8    v = 2;
9  end
```

Listing 9. Synchronized timers.

Figure 4. First-class timers in Céu.

3 Formal Semantics of Céu

We now introduce and formalize the semantics of a reduced version of Céu, called *basic Céu*. Although simpler than the full language presented in Section 2, basic Céu is expressive

[2] The compiler only forces the programmer to write the finalization clause, but cannot check if it actually handles the resource properly.

[3] By wall-clock time we mean the passage of time from the real world, measured in hours, minutes, etc.

enough to capture all the essential characteristics of full Céu, in particular, the stack-based execution of internal events. Once basic Céu is defined, the more elaborate constructs of full Céu can be defined on top of it, as we will discuss shortly.

The statements of basic Céu are presented in Figure 5. In the figure, the metavariables v (ln 2), e (ln 3–5, 10), and n (ln 15–16) range over variable identifiers, event identifiers, and integers. The metavariable *vars* (ln 1) denotes zero or more variable identifiers. The metavariable *stmt* (ln 1, 7–13, 17–19) denotes a statement, i.e., any of the statements of Figure 5—complex statements are defined recursively in terms of simpler statements. Finally, the metavariable *expr* (ln 2, 7) denotes an expression.

Guilherme: Outra opção para o basic seria usar o mesmo estilo do full mas colocar um β no final. E.g., **`block`**$_\beta$ ..., **`set`**$_\beta$ `x:=y`, **`seq`**$_\beta$ `x; y`, **`if`**$_\beta$ ... **`then`** ... **`else`** ..., ... **`par/and`**$_\beta$ .... Assim daria para usar Céu$_\beta$ (basic) e Céu (full).

Guilherme: Que tal "local" ao invés de "block"? Já que "block" também significa bloquear.

```
1  block vars stmt                          declaration block
2  v := expr                                assignment statement
3  await_ext(e)                             await external event
4  await_int(e)                             await internal event
5  emit_int(e)                              emit internal event
6  break                                    loop escape
7  if expr then stmt1 else stmt2            conditional
8  stmt1 ; stmt2                            sequence
9  loop stmt                                infinite loop
10 every e stmt                             event iteration
11 stmt1 par/and stmt2                      par/and statement
12 stmt2 par/or stmt2                       par/or statement
13 fin stmt                                 finalization statement
14 @nop                                     dummy statement
15 @runat(n)                                run at stack level n
16 @restore(n)                              restore environment
17 stmt1 @loop stmt2                        unwinded loop
18 stmt1 @par/and stmt2                     unwinded par/and
19 stmt1 @par/or stmt2                      unwinded par/or
```

Figure 5. The statements of basic Céu.

For simplicity, we only consider integer expressions. These are build up from integer constants and variables by the usual mathematical operators (+, −, ≤, ...). We assume that expression evaluation takes zero time (in accordance with the synchronous hypothesis) and that it always produces an integer value. In places where a boolean value is expected, any nonzero value means true while zero means false.

We distinguish between three kinds of basic Céu statements. First, there are those statements which are common in imperative languages and behave as usual, namely, blocks, assignments, conditionals, sequences, loops, and breaks. The `block` statement of basic Céu introduces its local variables at once in a list of identifiers.

The statements of the second kind are those which are specific to Céu. These are the statements `await`$_{ext}$, `await`$_{int}$, `emit`$_{int}$, and `every`, which deal with events, the statements `par/and` and `par/or`, which define parallel compositions, and the statement `fin`, which defines a finalization block. These basic Céu statements are more or less equivalent to their counterparts in full Céu. We defer a precise description of their behavior and entailed properties to Section 3.3.

Finally, the statements of the third kind are the remaining ones, namely, `@nop`, `@runat`, `@restore`, `@loop`, `@par/and`, and `@par/or`. These are hidden statements used by the interpreter to encode in the program's text information about its execution. We will have more to say about these `@`-statements in Section 3.3. Before that, however, we need to present the syntactical restrictions of basic Céu and discuss the mapping of full Céu programs into basic Céu programs.

3.1 Syntactic Restrictions of Basic Céu

The syntax of basic Céu shown in Figure 5 can be seen as a schema for generating programs. Not all programs generated by this schema are well-formed though. To be considered a *well-formed* basic Céu program, the generated program must satisfy the following restrictions:

1. If variable v occurs in an expression or assignment statement of the program, then this occurrence happens in the body of a `block` that declares v.
2. If a `break` occurs in the program, then this occurrence happens in the body of a `loop`.
3. If a statement of the form `loop` *stmt* occurs in the program, then all execution paths within *stmt* contain a matching `break` or an `await`$_{ext}$ or an `every`.
4. If a statement of the form `every` *e stmt* or `fin` *stmt* occurs in the program, then *stmt* does not contain occurrences of `loop`, `break`, `await`$_{ext}$, `await`$_{int}$, `every`, or `fin`.

Francisco: Também não faz sentido eles terem parand e paror (mas acho que nao afeta as provas).

Guilherme: Não adicionei parand/or na restrição porque de fato não precisa, mas fique à vontade para adicionar.

Restrictions 1 and 2 prevent the use of undeclared variables or orphan `break`'s. Restriction 3 ensures that the program does not have an infinite loop with a body that runs in zero time (which violates the synchronous hypothesis). And restriction 4 ensures that the body of `every` and `fin` statements always execute to completion within the same reaction. Similar restrictions exist in full Céu, as discussed in Section 2.

From now on, whenever we speak of a basic Céu program we mean a well-formed basic Céu program.

Francisco: A restrição do fin não é por ser blocking point, pelo contrário. Uma vez que ele começa a executar, ele tem que terminar na mesma reação pois a trilha tem que morrer pro programa continuar.

Francisco: A restrição do every também não tem a ver com ser blocking point. O bloco do every também precisa terminar na mesma reação pro every não perder eventos.

Francisco: A única restrição que tem a ver com safe blocking point é não poder ter break dentro de every.

Guilherme: Veja se agora está OK.

3.2 From Full Céu to Basic Céu

Most statements of full Céu are also present in basic Céu. These shared statements, however, are not exactly equivalent. It is sometimes the case that a statement full Céu (say `await`) has more features than its basic Céu counterpart ($\texttt{await}_{int}$). In this section, we discuss how the extra features of full Céu are implemented in basic Céu.

Francisco: Não gostei desse parágrafo. (1) foco duplo no @-stmt que é óbvio que está fora da comparação. (2) "It is often the case" dá a impressão que temos 100 casos, quando na verdade são casos claros e enumeráveis. (3) o `finalize` está sim presente como `fin` mas com diferenças notáveis.

Guilherme: Veja se está melhor.

Await and emit

The `await` and `emit` primitives of full Céu are slightly more complex than those of basic Céu, as they support communication of values between them.

Figure 6 shows a translation that adds a variable to hold the value being communicated. The original full Céu code in Listing 10 declares an internal event `e` (ln 2) and has an `await` (ln 4) and an `emit` (ln 10) that communicate the value 1 between the trails in parallel. The translation to basic Céu in Listing 11 declares an additional shared variable `e_` (ln 1) to hold the emitted value (ln 9) and which can be accessed by the awaking trail (ln 5).

External events require a similar translation, i.e., each event needs a corresponding global variable shared between all awaiting trails.

First-class timers

To add support for first-class timers to basic Céu, we introduce a `TIMER` input event, which notifies the passage of time, and two global variables: `timer_` which holds the elapsed time, and `delta_` which holds the residual time, initially set to 0.

Listing 13 shows the basic Céu program resulting from the translation of the two timers shown in Listing 12 (ln 1–11). A timer first adds a (possibly) negative delta from the time it should await (ln 1). Then, it enters in a loop that awakes on every occurrence of `TIMER` (ln 7) and decrements the time it should await (ln 8). Each iteration of the loop checks if the timer has expired (ln 3), and sets the new delta, which may affect a timer in sequence. The check happens before the await because the timer may already start expired due to the residual time.

```
event int e;
par/or do
  var int v = await e;

  <...>
with
  <...>

  emit e(1);
end
```

Listing 10.

```
1  var int e_;
2  event int e;
3  par/or do
4    await e;
5    var int v = e_;
6    <...>
7  with
8    <...>
9    e_ = 1;
10   emit e;
11 end
```

Listing 11.

Figure 6. Full-to-Basic translation for `await` and `emit`.

```
await 10ms;

await 1ms;
```

Listing 12.

```
1  var int tot_ = 10000 + delta_;
2  loop do
3      if tot_ <= 0 then
4          delta_ = tot_;
5          break;
6      end
7      await TIMER;
8      tot_ = tot_ - timer_;
9  end
10
11 var int tot_ = 1000 + delta_;
12 <...> // same loop as above
```

Listing 13.

Figure 7. Full-to-Basic translation for timers.

Finalization

The biggest mismatch between full Céu and basic Céu is in their support for finalization, i.e., between the statements `finalize` of full Céu and `fin` of basic Céu. Listing 14 shows a full Céu program containing an explicit block (ln 1–8) that executes the statements in `<A>` (ln 3) immediately followed by `<C>` (ln 7), and unconditionally executes `<B>` (ln 5) when the block terminates or aborts. To simulate this behavior in basic Céu we need to perform the translation shown in Listing 15. The basic Céu code also executes `<A>` (ln 1) immediately followed by `<C>` (ln 5). The difference is that the basic `fin <B>` statement (ln 3) blocks the pending statement forever, which only awakes and executes when it is aborted. The `par/or` (ln 2–6) serves this purpose since it allows `<C>` to execute immediately and aborts the `fin` when terminating.

```
do
    finalize
        <A>
    with
        <B>
    end
    <C>
end
```

Listing 14.

```
<A>
par/or do
    fin <B>
with
    <C>
end
```

Listing 15.

Figure 8. Full-to-Basic translation for finalization.

3.3 Operational Semantics

We proceed to formalize the operation of the basic Céu interpreter. Our goal here is twofold. First, we want to define a function *reaction* that describes precisely the operation steps taken by the interpreter to compute a single reaction to an external event. Second, we want to establish (prove) some properties of this function. In particular, we want to establish that:

1. *reaction* is indeed a function: the same program will always react in the same way to a same external event (i.e., reactions are deterministic);
2. *reaction* is a total function: its computation always yields a result (i.e., reactions terminate); and
3. *reaction* can be computed by a linear bounded automaton: the amount of memory it uses never exceeds a fixed threshold which depends solely on the input program (i.e., reactions are memory-bounded).

We will define *reaction* using a set of rules for a small-step operational semantics [16]. These rules dictate how the internal state of the basic Céu interpreter progresses while it is computing a reaction. A snapshot of this internal state is called a *description*, denoted δ, and consists of a quadruple $\langle stmt, n, e, \theta \rangle$ where

- *stmt* is a well-formed basic Céu program;
- n is a nonnegative integer, called the stack level;
- e is an event identifier or the empty identifier ε; and
- θ is a memory.

We will detail the precise meaning and purpose of the components of the description in due course. For now, the important thing is that the steps taken by the interpreter to compute a reaction can be viewed as transitions between descriptions. The transitions are dictated by rules hardcoded in the interpreter. Each rule establishes that when the interpreter is in a description δ and certain criteria are met, then it will *transition* to a modified description δ', in symbols,

$$\delta \longrightarrow \delta'.$$

We call the description on the left-hand side of the symbol $\longrightarrow$ the *input description*, and the one on its right-hand side the *output description*.

After transitioning to a modified description, the interpreter repeats the rule-evaluation/transition process, and continues to do so until a final description is reached. This final description, called an *irreducible description*, embodies the result of the reaction.

A full *reaction* is thus defined as a sequence of the transitions of the form

$$\delta_0 \longrightarrow \delta_1 \longrightarrow \cdots \longrightarrow \delta_f.$$

The initial description $\delta_0 = \langle stmt_0, 0, e, \theta_0 \rangle$ contains the three inputs of the reaction: the text of the program at the beginning of the reaction ($stmt_0$), the event e to which the program must react, and the memory θ_0 which holds the values of the variables used in $stmt_0$ at the beginning of the reaction. The final description $\delta_f = \langle stmt_f, 0, \varepsilon, \theta_f \rangle$ contains the two outputs of the reaction: the text of the program at the end of the reaction ($stmt_f$) and the values of the variables used in $stmt_f$ at the end of the reaction. The output program $stmt_f$ and memory θ_f will be used by the interpreter as inputs in the next reaction.

We write $\delta_0 \xrightarrow{i} \delta_f$ to indicate that δ_0 leads to δ_f after exactly i transitions, and we write $\delta_0 \xrightarrow{*} \delta_f$ to indicate that it does so after an unspecified but finite number of transitions. Using this notation, we can define function *reaction* (the first part of our goal) as follows:

$$reaction(stmt_0, \theta_0, e) = \langle stmt_f, \theta_f \rangle$$

if, and only if,

$$\langle stmt_0, 0, e, \theta_0 \rangle \xrightarrow{*} \langle stmt_f, 0, \varepsilon, \theta_f \rangle,$$

where $\langle stmt_f, 0, \varepsilon, \theta_f \rangle$ is an irreducible description. Under this definition, *reaction* will be deterministic, terminating, and memory-bounded (the second part of goal) if relation $\xrightarrow{*}$ happens to be so. That this is the case is a consequence of the way transitions are defined, as we will see in Section 4.

Francisco: Parei aqui também.

The next two sections, Sections 3.4 and 3.5, give the rules for transitions. There are two types of transitions: *outermost transitions* $\xrightarrow[out]{}$ and *nested transitions* $\xrightarrow[nst]{}$. Both are defined by rules of the form

$$\frac{\text{condition}_1 \quad \text{condition}_2 \quad \cdots \quad \text{condition}_n}{\delta \longrightarrow \delta'}$$

which establish that a transition $\delta \longrightarrow \delta'$ shall take place if condition_1, condition_2, …, and condition_n are all true. If the number of conditions is zero, then the line is omitted and the rule is called an axiom.

3.4 Outermost Transitions

The rules **push** and **pop** for $\xrightarrow[out]{}$ transitions are non-recursive definitions that apply to the program as a whole. These are the only rules that manipulate the stack level—the component of descriptions that determines the order of execution

of internal reactions.

$$\frac{e \neq \varepsilon}{\langle stmt, n, e, \theta\rangle \xrightarrow[out]{} \langle bcast(stmt, e), n+1, \varepsilon, \theta\rangle} \quad \textbf{(push)}$$

$$\frac{n > 0 \quad stmt = \texttt{@nop} \vee isblocked(stmt, n)}{\langle stmt, n, \varepsilon, \theta\rangle \xrightarrow[out]{} \langle stmt, n-1, \varepsilon, \theta\rangle} \quad \textbf{(pop)}$$

Rule **push** can be applied whenever there is a nonempty event in the input description; it instantly broadcasts the event to the program, which means it: (i) awakes any active $\texttt{await}_{ext}$ or $\texttt{await}_{int}$ statements in the program (see *bcast* in Figure 9); (ii) creates a nested reaction by increasing the stack level; and (iii) consumes the event (e becomes ε). Rule **push** is the only rule that matches (and consumes) a nonempty event in the input description.

Rule **pop** simply decreases the stack level by one; it can only be applied if the program is blocked (see *isblocked* in Figure 9) or terminated ($stmt$ = `@nop`). This condition ensures that an $\texttt{emit}_{int}$ only resumes after its internal reaction completes and blocks at the current stack level.

At the beginning of a reaction, an external event is emitted, which triggers rule **push**, immediately raising the stack level to 1. At the end of the reaction, the program will block or terminate and successive applications of rule **pop** will lead to a description with this same program at stack level 0.

3.5 Nested Transitions

The $\xrightarrow[nst]{}$ transitions have the general form

$$\langle stmt, n, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle stmt', n, e, \theta'\rangle.$$

They do not affect the stack level and never have an emitted event as a precondition. The distinction between $\xrightarrow[out]{}$ and $\xrightarrow[nst]{}$ prevents rules **push** and **pop** from matching and, consequently, from inadvertently modifying the current stack level before the nested reaction is complete.

A complete reaction is a sequence of transitions

$$\langle stmt_0, 0, e_{ext}, \theta_0\rangle \xrightarrow[out]{push} \left[\xrightarrow[nst]{}^{*} \xrightarrow[out]{}\right]^{*} \xrightarrow[nst]{}^{*} \xrightarrow[out]{pop} \langle stmt_f, 0, \varepsilon, \theta_f\rangle.$$

First, a $\xrightarrow[out]{push}$ starts a nested reaction at level 1. Then, a series of alternations between zero or more $\xrightarrow[nst]{}$ transitions (nested reactions) and a single $\xrightarrow[out]{}$ transition (stack operation) takes place. Finally, a last $\xrightarrow[out]{pop}$ transition decrements the stack level to 0 and terminates the reaction.

We now give the rules for nested transitions. Since these rules do not affect the stack level, whenever convenient, we will omit the stack level in the their definition. Thus, in this section, we will sometimes write descriptions as triples $\langle stmt, e, \theta\rangle$ with the tacit understanding that there is a hidden integer for the stack level between the components $stmt$ and e.

Declarations and assignments

There are three $\xrightarrow[nst]{}$ rules for dealing with variables: **block**, which introduces a new block of declarations; **restore**, which makes a previously introduced block go out of scope; and **assign**, which evaluates an expression and assigns the resulting value to a variable.

$$\langle \texttt{block}\ vars\ stmt, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle stmt\ ;\ \texttt{@restore}(|\theta|), \varepsilon, decl(\theta, vars)\rangle \quad \textbf{(block)}$$

$$\langle \texttt{@restore}(n), \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle \texttt{@nop}, \varepsilon, rest(\theta, n)\rangle \quad \textbf{(restore)}$$

$$\langle v := expr, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle \texttt{@nop}, \varepsilon, updt(\theta, v, eval(\theta, expr))\rangle \quad \textbf{(assign)}$$

The three rules, **block**, **restore**, and **assign**, act on the input memory θ. A *memory* is a stack of environments $[E_1, \ldots, E_k]$ where each environment E_i is a set of bindings, i.e., pairs of the form (v, n) which associate a variable v to an integer value n. For instance, the memory

$$\left[\{(v_1, 1), (v_2, \bot)\},\ \{(v_1, 0)\}\right]$$

has two environments: $E_1 = \{(v_1, 1), (v_2, \bot)\}$ (top-of-stack) and $E_2 = \{(v_1, 0)\}$. This memory tells us that, at some point, variable v_1 was declared and set to 0, and that later, in a nested scope, v_1 was re-declared (shadowing the previous declaration) and v_2 was declared for the first time. Still in the nested scope, which corresponds to the most recent environment E_1, variable v_1 was set to 1 but v_2 remained undefined, hence its value $\bot$ (*undefined*).

We use the following functions to manipulate memories (all of them return a modified memory):

$$decl(\theta, v_1, , \ldots, v_n) = \{(v_1, \bot), \ldots, (v_n, \bot)\} : \theta$$

$$rest(\theta, n) = \text{pops } |\theta| - n \text{ elements from } \theta$$

$$updt(E{:}\theta, v, n) = \begin{cases} ((E - \{(v, n')\}) \\ \quad \cup \{(v, n)\}) : \theta & \text{if } (v, n') \in E \\ updt(\theta, v, n) & \text{otherwise} \end{cases}$$

The colon (:) stands for the list constructor operator—thus $E{:}\theta$ denotes the stack obtained by pushing E onto θ—and $|\theta|$ stands for the length of stack θ.

Function *decl* pushes onto stack θ a new environment in which variables $v_1, \ldots, v_n$ are declared but not initialized. Function *rest* restores stack θ to index n, dropping any environments above this index. And function *updt* binds variable v to the integer n in the most recent environment where v occurs.

Back to the rules, **block** transforms a statement of the form `block` *vars stmt* into a sequence $stmt$;`@restore`$(|\theta|)$ and pushes onto θ a new environment declaring all variables in *vars*. The idea is that after *stmt* executes and the block goes out of scope, the memory is restored to its original length by the subsequent `@restore` statement (rule **restore**), which effectively removes any environments introduced by the block and its body.

Rule **assign** transforms a $v := expr$ into a `@nop` (the dummy statement) and performs the assignment. That is, it evaluates *expr* to an integer n and assigns it to variable v in the most recent environment where v was declared.

Expression evaluation is carried out by function *eval*. For simplicity, we assume that *eval* always returns an integer. In practice, any error in the evaluation, such as access to an uninitialized variable or division by zero, will cause the interpreter to abort.

Emissions

$$\langle \texttt{emit}_{int}(e), n, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle \texttt{@runat}(n), n, e, \theta \rangle \quad \textbf{(emit-int)}$$

$$\langle \texttt{@runat}(n), n, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle \texttt{@nop}, n, \varepsilon, \theta \rangle \quad \textbf{(run-at)}$$

By rule **emit-int**, an $\texttt{emit}_{int}(e)$ generates an internal event e and becomes a $\texttt{@runat}(n)$, which can only resume at stack level n. This is so because rule **run-at** applies only if the integer n in the $\texttt{@runat}(n)$ statement is equal to the stack level. Since every $\xrightarrow[nst]{}$ rule expects the input event to be empty, an application of **emit-int** leads immediately to an application of **push** at the outer level, creating a new level $n + 1$ on the stack. With this new stack level, the resulting $\texttt{@runat}(n)$ itself cannot transition yet, providing the desired stack-based semantics for internal events.

Conditionals

$$\frac{eval(\theta, expr) \neq 0}{\langle \texttt{if } expr \texttt{ then } stmt_1 \texttt{ else } stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1, \varepsilon, \theta \rangle} \quad \textbf{(if-true)}$$

$$\frac{eval(\theta, expr) = 0}{\langle \texttt{if } expr \texttt{ then } stmt_1 \texttt{ else } stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_2, \varepsilon, \theta \rangle} \quad \textbf{(if-false)}$$

Rules **if-true** and **if-false** transform a conditional statement into its if-part ($stmt_1$) or else-part ($stmt_2$) depending on the value to which the associated expression (*expr*) evaluates in the current memory. If it evaluates to a nonzero value, then **if-true** applies and the conditional becomes $stmt_1$; otherwise, **if-false** applies and the conditional becomes $stmt_2$. These are the only rules that use the contents of the input memory in a way that affects the control flow.

Sequences

$$\langle \texttt{@nop}; stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_2, \varepsilon, \theta \rangle \quad \textbf{(seq-nop)}$$

$$\langle \texttt{break}; stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle \texttt{break}, \varepsilon, \theta \rangle \quad \textbf{(seq-brk)}$$

$$\frac{\langle stmt_1, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1', e, \theta' \rangle}{\langle stmt_1; stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1'; stmt_2, e, \theta' \rangle} \quad \textbf{(seq-adv)}$$

A sequence whose first part is terminated (`@nop`) becomes its second part (rule **seq-nop**). A sequence whose first part is a `break`, propagates the `break` by dropping its second part (rule **seq-brk**). Finally, a sequence whose first part ($stmt_1$) is neither `@nop` nor `break` advances to the sequence ($stmt_1'; stmt_2$), event (e), and memory (θ') resulting from advancing just its first part (rule **seq-adv**).

Loops

$$\langle \texttt{loop } stmt, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle (stmt \texttt{ @loop } stmt); \texttt{@restore}(|\theta|), \varepsilon, \theta \rangle \quad \textbf{(loop-expd)}$$

$$\langle \texttt{@nop @loop } stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle \texttt{loop } stmt_2, \varepsilon, \theta \rangle \quad \textbf{(loop-nop)}$$

$$\langle \texttt{break @loop } stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle \texttt{@nop}, \varepsilon, \theta \rangle \quad \textbf{(loop-brk)}$$

$$\frac{\langle stmt_1, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1', e, \theta' \rangle}{\langle stmt_1 \texttt{ @loop } stmt_2, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1' \texttt{ @loop } stmt_2, e, \theta' \rangle} \quad \textbf{(loop-adv)}$$

When the interpreter encounters a `loop`, it expands its body in sequence with itself (the saved body) followed by a `@restore` statement (rule **loop-expd**). The `@restore` ensures that if the loop is terminated by a `break`, then any environments introduced by the loop are popped from the memory.

The remaining rules for loops are similar to those for sequences but use "`@`" as separators to bind the current code of the loop to its saved body. Rules **loop-nop** and **loop-adv** are analogous to rules **seq-nop** and **seq-adv**. They advance the current code of the loop until it becomes a `@nop`. When this happens the loop is restarted (**seq-nop**). (Note that if we had used "`;`" as a separator in loops, rules **loop-brk** and **seq-brk** would conflict.) Rule **loop-brk** escapes the enclosing loop, transforming everything into a `@nop`.

Par/and and par/or compositions

Guilherme: Parei aqui.

$$\langle stmt_1 \texttt{ and } stmt_2, n, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle stmt_1 \texttt{ @and } (\texttt{@runat}(n); stmt_2), n, \varepsilon, \theta \rangle \quad \textbf{(and-expd)}$$

$$\langle stmt_1 \texttt{ or } stmt_2, n, \varepsilon, \theta \rangle \xrightarrow[nst]{} \langle (stmt_1 \texttt{ @or } (\texttt{@runat}(n); stmt_2)); \texttt{@restore}(|\theta|), n, \varepsilon, \theta \rangle \quad \textbf{(or-expd)}$$

Guilherme: TODO: Expd & restore.

The rules for `and` and `or` compositions ensure that their left branch always transition before their right branch.

$$\frac{\langle stmt_1, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle stmt_1', e, \theta'\rangle}{\langle stmt_1 \texttt{ @and } stmt_2, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle stmt_1' \texttt{ @and } stmt_2, e, \theta'\rangle} \quad \textbf{(and-adv1)}$$

$$\frac{isblocked(stmt_1, n) \quad \langle stmt_2, n, \varepsilon, \theta'\rangle \xrightarrow[nst]{} \langle stmt_2', n, e, \theta'\rangle}{\langle stmt_1 \texttt{ @and } stmt_2, n, \varepsilon, \theta\rangle \xrightarrow[nst]{} \langle stmt_1 \texttt{ @and } stmt_2', n, e, \theta'\rangle} \quad \textbf{(and-adv2)}$$

$$\frac{\langle p, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p', n, e\rangle}{\langle p \texttt{ @or } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p' \texttt{ @or } q, n, e\rangle} \quad \textbf{(or-adv1)}$$

$$\frac{isblocked(p, n) \quad \langle q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle q', n, e\rangle}{\langle p \texttt{ @or } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p \texttt{ @or } q', n, e\rangle} \quad \textbf{(or-adv2)}$$

Rules **and-expd** and **or-expd** insert a `@canrun`(n) at the beginning of the right branch. This ensures that any `emit`$_{int}$ on the left branch, which eventually becomes a `@canrun`(n), resumes before the right branch starts. The deterministic behavior of the semantics relies on the *isblocked* predicate (see Figure 9) which is used in rules **and-adv2** and **or-adv2**. These rules require the left branch p to be blocked for the right branch to transition from q to q'.

In a parallel `@and`, if one branch terminates, the composition becomes the other branch (rules **and-nop1** and **and-nop2** below). In a parallel `@or`, however, if one branch terminates, the whole composition terminates and *clear* is called to finalize the aborted branch (rules **or-nop1** and **or-nop2**).

$$\langle \texttt{@nop @and } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle q, n, \varepsilon\rangle \quad \textbf{(and-nop1)}$$

$$\frac{isblocked(p, n)}{\langle p \texttt{ @and @nop}, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p, n, \varepsilon\rangle} \quad \textbf{(and-nop2)}$$

$$\langle \texttt{@nop @or } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(q), n, \varepsilon\rangle \quad \textbf{(or-nop1)}$$

$$\frac{isblocked(p, n)}{\langle p \texttt{ @or @nop}, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(p), n, \varepsilon\rangle} \quad \textbf{(or-nop2)}$$

The *clear* function (see Figure 9) concatenates all active `fin` bodies of the branch being aborted, so that they execute before the composition rejoins.

As there are no transition rules for `fin` statements, once reached, a `fin` halts and will only be consumed if its trail is aborted. At this point, its body will execute as a result of the *clear* call. The body of a `fin` statement always execute within a reaction. This is due to a syntactic restriction: `fin` bodies cannot contain awaiting statements (namely, `await`$_{ext}$, `await`$_{int}$, `every`, or `fin`).

Finally, a `break` in one branch of a parallel escapes the closest enclosing `loop`, properly aborting the other branch with the *clear* function:

$$\langle \texttt{break @and } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(q); \texttt{break}, n, \varepsilon\rangle \quad \textbf{(and-brk1)}$$

$$\frac{isblocked(p, n)}{\langle p \texttt{ @and break}, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(p); \texttt{break}, n, \varepsilon\rangle} \quad \textbf{(and-brk2)}$$

$$\langle \texttt{break @or } q, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(q); \texttt{break}, n, \varepsilon\rangle \quad \textbf{(or-brk1)}$$

$$\frac{isblocked(p, n)}{\langle p \texttt{ @or break}, n, \varepsilon\rangle \xrightarrow[nst]{} \langle clear(p); \texttt{break}, n, \varepsilon\rangle} \quad \textbf{(or-brk2)}$$

A reaction eventually blocks in `await`$_{ext}$, `await`$_{int}$, `every`, `fin`, and `@canrun` statements in parallel trails. Then, if none of the trails is blocked in `@canrun`, it means that the program cannot advance in the current reaction. However, `@canrun` statements can still resume at lower stack indexes and will eventually resume in the current reaction (see rule **pop**).

3.6 A Complete Example

Guilherme: TODO: Reação usando as regras.

4 Properties of Céu Programs

4.1 Deterministic Execution

Transitions $\xrightarrow[out]{}$ and $\xrightarrow[nst]{}$ are defined in such a way that given an input description either no rule is applicable or exactly one of them can be applied (no choice involved). This coupled with the fact that the output of every rule is a function of its input implies that transitions are deterministic: the same input description, if it can transition, will always result in the same output description. Thus the transition relation $\longrightarrow$ is in fact a partial function.

The next two lemmas establish the determinism of a single application of $\xrightarrow[out]{}$ and $\xrightarrow[nst]{}$. Lemma 4.1 follows from a simple inspection of rules **push** and **pop**. The proof of Lemma 4.2 follows by induction on the structure of the derivation trees produced by the rules for $\xrightarrow[nst]{}$. Both lemmas are used in the proof of the Theorem 4.3.

Lemma 4.1. *If* $\delta \xrightarrow[out]{} \delta_1$ *and* $\delta \xrightarrow[out]{} \delta_2$ *then* $\delta_1 = \delta_2$.

Lemma 4.2. *If* $\delta \xrightarrow[nst]{} \delta_1$ *and* $\delta \xrightarrow[nst]{} \delta_2$ *then* $\delta_1 = \delta_2$.

The main result of this section, Theorem 4.3, establishes that any given number $i \geq 0$ of applications of arbitrary transition rules, starting from the same input description, will always lead to the same output description. In other words, any finite sequence of transitions behave deterministically.

Theorem 4.3 (Determinism). $\delta \xrightarrow{i} \delta_1$ *and* $\delta \xrightarrow{i} \delta_2$ *implies* $\delta_1 = \delta_2$.

Proof. By induction on i. The theorem is trivially true if $i = 0$ and follows directly from the previous lemmas if $i = 1$. Suppose

$$\delta \xrightarrow{1} \delta_1' \xrightarrow{i-1} \delta_1 \quad \text{and} \quad \delta \xrightarrow{1} \delta_2' \xrightarrow{i-1} \delta_2\,,$$

(i) Function *bcast*:

$$
\begin{aligned}
bcast(\texttt{await}_{ext}(e), e) &= \texttt{@nop} \\
bcast(\texttt{await}_{int}(e), e) &= \texttt{@nop} \\
bcast(\texttt{every}\ e\ p, e) &= p;\ \texttt{every}\ e\ p \\
bcast(\texttt{@canrun}(n), e) &= \texttt{@canrun}(n) \\
bcast(\texttt{fin}\ p, e) &= \texttt{fin}\ p \\
bcast(p;\ q, e) &= bcast(p, e);\ q \\
bcast(p\ \texttt{@loop}\ q, e) &= bcast(p, e)\ \texttt{@loop}\ q \\
bcast(p\ \texttt{@and}\ q, e) &= bcast(p, e)\ \texttt{@and}\ bcast(q, e) \\
bcast(p\ \texttt{@or}\ q, e) &= bcast(p, e)\ \texttt{@or}\ bcast(q, e) \\
bcast(\_, e) &= \_\ \ (\texttt{mem}, \texttt{emit}_{int}, \texttt{break}, \\
&\qquad \texttt{if}\ \texttt{then}\ \texttt{else}, \texttt{loop}, \texttt{and}, \texttt{or}, \texttt{@nop})
\end{aligned}
$$

(ii) Predicate *isblocked*:

$$
\begin{aligned}
isblocked(\texttt{await}_{ext}(e), n) &= true \\
isblocked(\texttt{await}_{int}(e), n) &= true \\
isblocked(\texttt{every}\ e\ p, n) &= true \\
isblocked(\texttt{@canrun}(m), n) &= (n > m) \\
isblocked(\texttt{fin}\ p, n) &= true \\
isblocked(p;\ q, n) &= isblocked(p, n) \\
isblocked(p\ \texttt{@loop}\ q, n) &= isblocked(p, n) \\
isblocked(p\ \texttt{@and}\ q, n) &= isblocked(p, n) \wedge isblocked(q, n) \\
isblocked(p\ \texttt{@or}\ q, n) &= isblocked(p, n) \wedge isblocked(q, n) \\
isblocked(\_, n) &= false\ \ (\texttt{mem}, \texttt{emit}_{int}, \texttt{break}, \\
&\qquad \texttt{if}\ \texttt{then}\ \texttt{else}, \texttt{loop}, \texttt{and}, \texttt{or}, \texttt{@nop})
\end{aligned}
$$

(iii) Function *clear*:

$$
\begin{aligned}
clear(\texttt{await}_{ext}(e)) &= \texttt{@nop} \\
clear(\texttt{await}_{int}(e)) &= \texttt{@nop} \\
clear(\texttt{every}\ e\ p) &= \texttt{@nop} \\
clear(\texttt{@canrun}(n)) &= \texttt{@nop} \\
clear(\texttt{fin}\ p) &= p \\
clear(p;\ q) &= clear(p) \\
clear(p\ \texttt{@loop}\ q) &= clear(p) \\
clear(p\ \texttt{@and}\ q) &= clear(p);\ clear(q) \\
clear(p\ \texttt{@or}\ q) &= clear(p);\ clear(q) \\
clear(\_) &= \xi\ \ (\texttt{mem}, \texttt{emit}_{int}, \texttt{break}, \\
&\qquad \texttt{if}\ \texttt{then}\ \texttt{else}, \texttt{loop}, \texttt{and}, \texttt{or}, \texttt{@nop})
\end{aligned}
$$

Figure 9. (i) Function *bcast* awakes awaiting trails matching the event by converting $\texttt{await}_{ext}$ and $\texttt{await}_{int}$ to `@nop`, and by unwinding `every` statements. (ii) Predicate *isblocked* is true only if all branches in parallel are blocked waiting for events, finalization clauses, or certain stack levels. (iii) Function *clear* extracts `fin` statements in parallel and put their bodies in sequence. In (i), (ii), and (iii), "_" denotes the omitted cases and "ξ" denotes the empty string.

for some $i > 1$, δ_1' and δ_2'. Then, by Lemma 4.1 or 4.2, depending on whether the first transition is $\xrightarrow[out]{}$ or $\xrightarrow[nst]{}$ (it cannot be both), $\delta_1' = \delta_2'$, and by the induction hypothesis, $\delta_1 = \delta_2$. □

4.2 Terminating Reactions

We now turn to the problem of termination. We want to show that any sufficiently long sequence of applications of arbitrary transition rules will eventually lead to an irreducible description, i.e., one that cannot be modified by further transitions. Before doing that, however, we need to introduce some notation and establish some basic properties of the transition relations $\xrightarrow[nst]{}$ and $\xrightarrow[out]{}$.

Definition 4.4. A description $\delta = \langle p, n, e\rangle$ is *nested-irreducible* iff $e \neq \varepsilon$ or $p = \texttt{@nop}$ or $p = \texttt{break}$ or $isblocked(p, n)$.[4]

Nested-irreducible descriptions serve as normal forms for $\xrightarrow[nst]{}$ transitions: they embody the result of an exhaustive number of $\xrightarrow[nst]{}$ applications. We will write $\delta_{\#nst}$ to indicate that description δ is nested-irreducible.

The use of qualifier "irreducible" in Definition 4.4 is justified by Proposition 4.5, which states that if a finite number of applications of $\xrightarrow[nst]{}$ results in an irreducible description, then that occurs exactly once, at some specific number i. The proof of Proposition 4.5 follows directly from the definition of $\xrightarrow[nst]{}$ by contradiction on the hypothesis that there is such $k \neq i$.

Proposition 4.5. *If* $\delta \xrightarrow[nst]{i} \delta'_{\#nst}$ *then, for all* $k \neq i$*, there is no* $\delta''_{\#nst}$ *such that* $\delta \xrightarrow[nst]{k} \delta''_{\#nst}$.

The next lemma establishes that sequences of $\xrightarrow[nst]{}$ transitions behave as expected regarding the order of evaluation of composition branches. Its proof follows by induction on i.

Lemma 4.6.

If $\langle p_1, n, e\rangle \xrightarrow[nst]{i} \langle p_1', n, e'\rangle$*, for any* p_2*:*

(a) $\langle p_1;\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1';p_2, n, e'\rangle$.
(b) $\langle p_1\ \texttt{@loop}\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1'\ \texttt{@loop}\ p_2, n, e'\rangle$.
(c) $\langle p_1\ \texttt{@and}\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1'\ \texttt{@and}\ p_2, n, e'\rangle$.
(d) $\langle p_1\ \texttt{@or}\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1'\ \texttt{@or}\ p_2, n, e'\rangle$.

If $\langle p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_2', n, e'\rangle$*, for any* p_1 *such that* $isblocked(p_1, n)$*:*

(a) $\langle p_1\ \texttt{@and}\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1\ \texttt{@and}\ p_2', n, e'\rangle$.
(b) $\langle p_1\ \texttt{@or}\ p_2, n, e\rangle \xrightarrow[nst]{i} \langle p_1\ \texttt{@or}\ p_2', n, e'\rangle$.

The syntactic restriction discussed in Section 2.1 regarding the body of loops and the restriction mentioned in Section 3.3 about the body of `fin` statements are formalized in Assumption 4.7 below. These restrictions are essential to prove the next theorem.

Assumption 4.7 (Syntactic restrictions)**.**

(a) If $p = \texttt{fin}\ p_1$ then p_1 contains no occurrences of statements $\texttt{await}_{ext}$, $\texttt{await}_{int}$, `every`, or `fin`. And so, for any n, $\langle clear(p_1), n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{@nop}, n, \varepsilon\rangle$.
(b) If $p = \texttt{loop}\ p_1$ then all execution paths of p_1 contain a matching `break` or an $\texttt{await}_{ext}$. Consequently, for all n, there are p_1' and e such that $\langle \texttt{loop}\ p_1, n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p_1', n, e\rangle$, where $p_1' = \texttt{break}\ \texttt{@loop}\ p_1$ or $isblocked(p_1', n)$.

Theorem 4.8 establishes that a finite (possibly zero) number of $\xrightarrow[nst]{}$ transitions eventually leads to a nested-irreducible

[4] We sometimes abbreviate "$p = \texttt{@nop}$ or $p = \texttt{break}$" as "$p = \texttt{@nop, break}$".

description. Hence, for any input description δ, it is always possible to transform δ in a nested-irreducible description δ' by applying to it a sufficiently long sequence of $\xrightarrow[nst]{}$ transitions. The proof of the theorem follows by induction on the structure of programs (members of set P) and depends on Lemma 4.6 and Assumption 4.7.

Theorem 4.8. *For any δ there is a $\delta'_{\#nst}$ such that $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$.*

The main result of this section, Theorem 4.15, is similar to Theorem 4.8 but applies to transitions $\longrightarrow$ in general. Before stating and proving it, we need to characterize irreducible descriptions in general. This characterization, given in Definition 4.11, depends on the notions of potency and rank.

Definition 4.9. The *potency* of a program p in reaction to event e, denoted $pot(p, e)$, is the maximum number of $\texttt{emit}_{int}$ statements that can be executed in a reaction of p to e, i.e.,

$$pot(p, e) = pot'(bcast(p, e)),$$

where pot' is an auxiliary function that counts the maximum number of reachable $\texttt{emit}_{int}$ statements in the program resulting from the broadcast of event e to p.

Function pot' is defined by the following clauses:

(a) $pot'(\texttt{emit}_{int}(e)) = 1$.
(b) $pot'(\texttt{if mem}(id)\texttt{then } p_1 \texttt{ else } p_2) = \max\{pot'(p_1), pot'(p_2)\}$.
(c) $pot'(\texttt{loop } p_1) = pot'(p_1)$.
(d) $pot'(p_1 \texttt{ and } p_2) = pot'(p_1 \texttt{ or } p_2) = pot'(p_1) + pot'(p_2)$.
(e) If $p_1 \neq \texttt{break}, \texttt{await}_{ext}(e)$,

$$pot'(p_1; p_2) = pot'(p_1) + pot'(p_2)$$

$$pot'(p_1 \texttt{ @loop } p_2) = \begin{cases} pot'(p_1) & \text{if } (\dagger) \\ pot'(p_1) + pot'(p_2) & \text{otherwise,} \end{cases}$$

where (†) stands for: "a break or $\texttt{await}_{ext}$ occurs in all execution paths of p_1".
(f) If $p_1, p_2 \neq \texttt{break}$, $pot'(p_1 \texttt{ @and } p_2) = pot'(p_1) + pot'(p_2)$.
(g) If $p_1, p_2 \neq \texttt{break}$ and $p_1, p_2 \neq \texttt{@nop}$,

$$pot'(p_1 \texttt{ @or } p_2) = pot'(p_1) + pot'(p_2).$$

(h) Otherwise, if none of (a)–(g) applies, $pot'(\_) = 0$.

Definition 4.10. The *rank* of a description $\delta = \langle p, n, e \rangle$, denoted $rank(\delta)$, is a pair of nonnegative integers $\langle i, j \rangle$ such that

$$i = pot(p, e) \quad \text{and} \quad j = \begin{cases} n & \text{if } e = \varepsilon \\ n + 1 & \text{otherwise.} \end{cases}$$

Intuitively, the rank of a description δ is a measure of the maximum amount of "work" (transitions) required to transform δ into an irreducible description, in the following sense.

Definition 4.11. A description δ is *irreducible* (in symbols, $\delta_\#$) iff it is nested-irreducible and its $rank(\delta)$ is $\langle i, 0 \rangle$, for some $i \geq 0$.

An irreducible description $\delta_\# = \langle p, n, e \rangle$ serves as a normal form for transitions $\longrightarrow$ in general. Such description cannot be advanced by $\xrightarrow[nst]{}$, as it is nested-irreducible, and neither by $\xrightarrow[out]{push}$ nor $\xrightarrow[out]{pop}$, as the second coordinate of its rank is 0, which implies $e = \varepsilon$ and $n = 0$.

The next two lemmas establish that a single application of $\xrightarrow[out]{}$ or $\xrightarrow[nst]{}$ either preserves or decreases the rank of the input description. All rank comparisons assume lexicographic order, i.e., if $rank(\delta) = \langle i, j \rangle$ and $rank(\delta') = \langle i', j' \rangle$ then $rank(\delta) > rank(\delta')$ iff $i > i'$ or $i = i$ and $j > j'$. The proof of Lemma 4.12 follows directly from **push** and **pop** and from Definitions 4.9 and 4.10. The proof of Lemma 4.13, however, is by induction on the structure of $\xrightarrow[nst]{}$ derivations.

Lemma 4.12.

(a) If $\delta \xrightarrow[out]{push} \delta'$ then $rank(\delta) = rank(\delta')$.
(b) If $\delta \xrightarrow[out]{pop} \delta'$ then $rank(\delta) > rank(\delta')$.

Lemma 4.13. *If $\delta \xrightarrow[nst]{} \delta'$ then $rank(\delta) \geq rank(\delta')$.*

The next theorem is a generalization of Lemma 4.13 for $\xrightarrow[nst]{*}$. Its proof follows from the lemma by induction on i.

Theorem 4.14. *If $\delta \xrightarrow[nst]{*} \delta'$ then $rank(\delta) \geq rank(\delta')$.*

We now state and prove the main result of this section, Theorem 4.15, the termination theorem for $\xrightarrow{*}$. The idea of the proof is that a sufficiently large sequence of $\xrightarrow[nst]{}$ and $\xrightarrow[out]{}$ transitions eventually decreases the rank of the current description until an irreducible description is reached. This irreducible description is the final result of the reaction.

Theorem 4.15 (Termination)**.** *For any δ, there is a $\delta'_\#$ such that $\delta \xrightarrow{*} \delta'_\#$.*

Proof. By lexicographic induction on $rank(\delta)$. Let $\delta = \langle p, n, e \rangle$ and $rank(\delta) = \langle i, j \rangle$.

For the basis, suppose $\langle i, j \rangle = \langle 0, 0 \rangle$. Then δ cannot be advanced by $\xrightarrow[out]{}$, as $j = 0$ implies $e = \varepsilon$ and $n = 0$. If δ is nested-irreducible, the theorem is trivially true, as $\delta \xrightarrow[nst]{0} \delta_{\#nst}$ and $\delta_\#$. If δ is not nested-irreducible then, by Theorem 4.8, $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$, for some $\delta'_{\#nst}$. By Theorem 4.14, $rank(\delta) \geq rank(\delta')$, which implies $rank(\delta') = \langle 0, 0 \rangle$, and so $\delta'_\#$.

For the inductive step, suppose $\langle i, j \rangle > \langle 0, 0 \rangle$. Then, depending on whether or not δ is nested-irreducible, there are two cases.

Case 1. δ is nested-irreducible. If $j = 0$, by Definition 4.11, $\delta_\#$, and so $\delta \xrightarrow{0} \delta_\#$. If $j > 0$, there are two subcases:

Case 1.1. $e \neq \varepsilon$. Then, by **push** and by Theorem 4.8, there are δ'_1 and $\delta'_{\#nst} = \langle p', n + 1, e' \rangle$ such that $\delta \xrightarrow[out]{push} \delta'_1 \xrightarrow[nst]{*} \delta'_{\#nst}$. Thus, by Lemma 4.12 and by Theorem 4.14,

$$rank(\delta) = rank(\delta'_1) = \langle i, j \rangle \geq rank(\delta') = \langle i', j' \rangle.$$

If $e' = \varepsilon$, then $i = i'$ and $j = j'$, and the rest of this proof is similar to that of Case 1.2.2 below. Otherwise, if $e' \neq \varepsilon$, then $i > i'$, as an $\texttt{emit}_{int}(e')$ was consumed by the nested transitions. Thus, $rank(\delta) > rank(\delta')$. By the induction hypothesis, $\delta' \xrightarrow{*} \delta''_\#$, for some $\delta''_\#$. Therefore, $\delta \xrightarrow{*} \delta''_\#$.

Case 1.2. $e = \varepsilon$. Then, as $j > 0$, $\delta \xrightarrow[out]{pop} \delta'$, for some δ'. By Lemma 4.12, $rank(\delta) > rank(\delta')$. Hence, by the induction hypothesis, $\delta' \xrightarrow{*} \delta''_{\#}$, for some $\delta''_{\#}$. And so, $\delta \xrightarrow{*} \delta''_{\#}$.

Case 2. δ is not nested-irreducible. Then $e = \varepsilon$ and, by Theorems 4.8 and 4.14, there is a $\delta'_{\#nst}$ such that $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$ with $rank(\delta) \geq rank(\delta'_{\#nst})$. The rest of this proof is similar to that of Case 1 above. □

4.3 Memory Boundedness

As Céu has no mechanism for heap allocation, unbounded iteration, or general recursion, the maximum memory usage of a given Céu program is determined solely by the length of its code, the number of variables it uses, and the size of the event stack that it requires to run. The code length and the number of variables used are easily determined by code inspection. The maximum size of the event stack during a reaction of program p to external event e corresponds to $pot(p, e)$, i.e., to the maximum number of internal events that p may emit in reaction to e. If p may react to external events $e_1, \ldots, e_n$ then, in the worst case, its event stack will need to store $\max\{pot(p, e_1), \ldots, pot(p, e_n)\}$ events.

5 Related Work

Céu follows the lineage of imperative synchronous languages initiated by Esterel [9]. These languages typically define time as a discrete sequence of logical "ticks" in which multiple simultaneous input events can be active [18]. The presence of multiple inputs requires careful static analysis to detect and reject programs with *causality cycles* and *schizophrenia problems* [5]. In contrast, Céu defines time as a discrete sequence of reactions to unique input events, which is a prerequisite for the concurrency checks that enable safe shared-memory concurrency, as discussed in Section 2.2.

In most synchronous languages, the behavior of external and internal events is equivalent. However, in Céu, internal events introduce stack-based micro reactions within external reactions, providing more fine-grained control for intra-reaction execution. This allows for memory-bounded subroutines that can execute multiple times during the same external reaction. The synchronous languages Statecharts [21] and Statemate [12] also distinguish internal from external events. Although the descriptions suggest a stack-based semantics, we are not aware of formalizations or more precision for a deeper comparison with Céu.

Like Céu, many other synchronous languages [2, 8, 11, 13, 22] rely on lexical scheduling to preserve determinism. In contrast, in Esterel, the execution order for operations within a reaction is non-deterministic: *"if there is no control dependency, as in* `(call f1()||call f2())`*, the order is unspecified and it would be an error to rely on it"* [6]. For this reason, Esterel, does not support shared-memory concurrency: *"if a variable is written by some thread, then it can neither be read nor be written by concurrent threads"* [6]. Considering the constant and low-level interactions with the underlying architecture in embedded systems (e.g., direct port manipulation), we believe that it is advantageous to embrace lexical scheduling as part of the language specification as a pragmatic design decision to enforce determinism. However, since Céu statically detects trails not sharing memory, an optimized scheduler could exploit real parallelism in such cases.

Regarding the integration with *C* language-based environments, Céu supports a finalization mechanism for external resources. In addition, Céu also tracks pointers representing resources that cross *C* boundaries and forces the programmer to provide associated finalizers. As far as we know, this extra safety level is unique to Céu among synchronous languages.

6 Conclusion

The programming language Céu aims to offer a concurrent, safe, and realistic alternative to C for embedded soft real-time systems, such as sensor networks and multimedia systems. Céu inherits the synchronous and imperative mindset of Esterel but adopts a simpler semantics with fine-grained execution control, which makes the language fully deterministic. In addition, its stack-based execution for internal events provides a limited but memory-bounded form of subroutines. Céu also provides a finalization mechanism for resources when interacting with the external environment.

In this paper, we proposed a small-step operational semantics for Céu and proved that under it reactions are deterministic, terminate in finite time, and use bounded memory, i.e., that for a given arbitrary timeline of input events, multiple executions of the same program always react in bounded time and arrive at the same final finite memory state.

A Detailed Proofs

Lemma 4.1. *If* $\delta \xrightarrow[out]{} \delta_1$ *and* $\delta \xrightarrow[out]{} \delta_2$ *then* $\delta_1 = \delta_2$.

Proof. The lemma is vacuously true if δ cannot be advanced by $\xrightarrow[out]{}$ transitions. Suppose that is not the case and let $\delta = \langle p, n, e\rangle$, $\delta_1 = \langle p_1, n_1, e_1\rangle$ and $\delta_2 = \langle p_2, n_2, e_2\rangle$. Then, there are two possibilities.

Case 1. $e \neq \varepsilon$. Both transitions are applications of **push**. Hence $p_1 = p_2 = bcast(p, e)$, $n_1 = n_2 = n + 1$, and $e_1 = e_2 = \varepsilon$.

Case 2. $e = \varepsilon$. Both transitions are applications of **pop**. Hence $p_1 = p_2 = p$, $n_1 = n_2 = n - 1$, and $e_1 = e_2 = \varepsilon$. □

Lemma 4.2. *If* $\delta \xrightarrow[nst]{} \delta_1$ *and* $\delta \xrightarrow[nst]{} \delta_2$ *then* $\delta_1 = \delta_2$.

Proof. By induction on the structure of $\xrightarrow[nst]{}$ derivations. The lemma is vacuously true if δ cannot be advanced by $\xrightarrow[nst]{}$ transitions. Suppose that is not the case and let $\delta = \langle p, n, e\rangle$, $\delta_1 = \langle p_1, n_1, e_1\rangle$ and $\delta_2 = \langle p_2, n_2, e_2\rangle$. Then, by the hypothesis of the lemma, there are derivations π_1 and π_2 such that

$$\pi_1 \Vdash \langle p, n, e\rangle \xrightarrow[nst]{} \langle p_1, n_1, e_1\rangle$$
$$\pi_2 \Vdash \langle p, n, e\rangle \xrightarrow[nst]{} \langle p_2, n_2, e_2\rangle$$

i.e., the conclusion of π_1 is $\langle p, n, e\rangle \xrightarrow[nst]{} \langle p_1, n_1, e_1\rangle$ and the conclusion of π_2 is $\langle p, n, e\rangle \xrightarrow[nst]{} \langle p_2, n_2, e_2\rangle$.

By definition of $\xrightarrow[nst]{}$, we have that $e = \varepsilon$ and $n_1 = n_2 = n$. It remains to be shown that $p_1 = p_2$ and $e_1 = e_2$.

Depending on the structure of program p, the following 11 cases are possible. (Note that p cannot be an $\texttt{await}_{ext}$, $\texttt{await}_{int}$, `break`, `every`, `fin`, or `@nop` statement as there is no $\xrightarrow[nst]{}$ rule to transition such programs.)

Case 1. $p = \texttt{mem}(id)$. Then derivations π_1 and π_2 are instances of rule **mem**, i.e., their conclusions are obtained by an application of this rule. Hence $p_1 = p_2 = \texttt{@nop}$ and $e_1 = e_2 = \varepsilon$.

Case 2. $p = \texttt{emit}_{int}(e')$. Then π_1 and π_2 are instances of **emit-int**. Hence $p_1 = p_2 = \texttt{@canrun}(n)$ and $e_1 = e_2 = e'$.

Case 3. $p = \texttt{@canrun}(n)$. Then π_1 and π_2 are instances of **canrun**. Hence $p_1 = p_2 = \texttt{@nop}$ and $e_1 = e_2 = \varepsilon$.

Case 4. $p = \texttt{if}\ \texttt{mem}(id)\ \texttt{then}\ p'\ \texttt{else}\ p''$. There are two subcases.

Case 4.1. $\mathit{eval}(\texttt{mem}(id))$. Then π_1 and π_2 are instances of **if-true**. Hence $p_1 = p_2 = p'$ and $e_1 = e_2 = \varepsilon$.

Case 4.2. $\neg\, \mathit{eval}(\texttt{mem}(id))$. Then π_1 and π_2 are instances of **if-false**. Hence $p_1 = p_2 = p''$ and $e_1 = e_2 = \varepsilon$.

Case 5. $p = p'; p''$. There are three subcases.

Case 5.1. $p' = \texttt{@nop}$. Then π_1 and π_2 are instances of **seq-nop**. Hence $p_1 = p_2 = p''$ and $e_1 = e_2 = \varepsilon$.

Case 5.2. $p' = \texttt{break}$. Then π_1 and π_2 are instances of **seq-brk**. Hence $p_1 = p_2 = \texttt{break}$ and $e_1 = e_2 = \varepsilon$.

Case 5.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then π_1 and π_2 are instances of **seq-adv**. Thus there are derivations π_1' and π_2' such that

$$\pi_1' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1', n, e_1'\rangle$$
$$\pi_2' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_2', n, e_2'\rangle$$

for some p_1', p_2', e_1', and e_2'. By the induction hypothesis, $p_1' = p_2'$ and $e_1' = e_2'$. Hence $p_1 = p_1'; p'' = p_2'; p'' = p_2$ and $e_1 = e_1' = e_2' = e_2$.

Case 6. $p = \texttt{loop}\, p'$. Then π_1 and π_2 are instances of **loop-expd**. Hence $p_1 = p_2 = p'\ \texttt{@loop}\, p'$ and $e_1 = e_2 = \varepsilon$.

Case 7. $p = p'\ \texttt{@loop}\, p''$. There are three subcases.

Case 7.1. $p' = \texttt{@nop}$. Then π_1 and π_2 are instances of **loop-nop**. Hence $p_1 = p_2 = \texttt{loop}\, p''$ and $e_1 = e_2 = \varepsilon$.

Case 7.2. $p' = \texttt{break}$. Then π_1 and π_2 are instances of **loop-brk**. Hence $p_1 = p_2 = \texttt{@nop}$ and $e_1 = e_2 = \varepsilon$.

Case 7.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then π_1 and π_2 are instances of **loop-adv**. Thus there are derivations π_1' and π_2' such that

$$\pi_1' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1', n, e_1'\rangle$$
$$\pi_2' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_2', n, e_2'\rangle$$

for some p_1', p_2', e_1', and e_2'. By the induction hypothesis, $p_1' = p_2'$ and $e_1' = e_2'$. Hence $p_1 = p_1'\ \texttt{@loop}\, p'' = p_2'\ \texttt{@loop}\, p'' = p_2$ and $e_1 = e_1' = e_2' = e_2$.

Case 8. $p = p'$ `and` p''. Then π_1 and π_2 are instances of **and-expd**. Hence $p_1 = p_2 = p'\ \texttt{@and}\ (\texttt{@canrun}(n); p'')$ and $e_1 = e_2 = \varepsilon$.

Case 9. $p = p'\ \texttt{@and}\ p''$. There are two subcases.

Case 9.1. $\neg\, \mathit{isblocked}(p', n)$. There are three subcases.

Case 9.1.1. $p' = \texttt{@nop}$. Then π_1 and π_2 are instances of **and-nop1**. Hence $p_1 = p_2 = p''$ and $e_1 = e_2 = \varepsilon$.

Case 9.1.2. $p' = \texttt{break}$. Then π_1 and π_2 are instances of **and-brk1**. Hence $p_1 = p_2 = \mathit{clear}(p''); \texttt{break}$ and $e_1 = e_2 = \varepsilon$.

Case 9.1.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then π_1 and π_2 are instances of **and-adv1**. Thus there are derivations π_1' and π_2' such that

$$\pi_1' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1', n, e_1'\rangle$$
$$\pi_2' \Vdash \langle p', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_2', n, e_2'\rangle$$

for some p_1', p_2', e_1', e_2'. By the induction hypothesis, $p_1' = p_2'$ and $e_1' = e_2'$. Hence $p_1 = p_1'$ `and` $p'' = p_2'$ `and` $p'' = p_2$ and $e_1 = e_1' = e_2' = e_2$.

Case 9.2. $\mathit{isblocked}(p', n)$. There are three subcases.

Case 9.2.1. $p'' = \texttt{@nop}$. Then π_1 and π_2 are instances of **and-nop2**. Hence $p_1 = p_2 = p'$ and $e_1 = e_2 = \varepsilon$.

Case 9.2.2. $p'' = \texttt{break}$. Then π_1 and π_2 are instances of **and-brk2**. Hence $p_1 = p_2 = \mathit{clear}(p'); \texttt{break}$ and $e_1 = e_2 = \varepsilon$.

Case 9.2.3. $p'' \neq \texttt{@nop}, \texttt{break}$. Then π_1 and π_2 are instances of **and-adv2**. Thus there are derivations π_1'' and π_2'' such that

$$\pi_1'' \Vdash \langle p'', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1'', n, e_1''\rangle$$
$$\pi_2'' \Vdash \langle p'', n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_2'', n, e_2''\rangle$$

for some p_1'', p_2'', e_1'', and e_2''. By the induction hypothesis, $p_1'' = p_2''$ and $e_1'' = e_2''$. Hence $p_1 = p'$ `and` $p_1'' = p'$ `and` $p_2'' = p_2$ and $e_1 = e_1'' = e_2'' = e_2$.

Case 10. $p = p'$ `or` p''. Then π_1 and π_2 are instances of **or-expd**. Hence $p_1 = p_2 = p'\ \texttt{@or}\ (\texttt{@canrun}(n); p'')$ and $e_1 = e_2 = \varepsilon$.

Case 11. $p = p'\ \texttt{@or}\ p''$. There are two subcases.

Case 11.1. $\neg\, \mathit{isblocked}(p', n)$. There are three subcases.

Case 11.1.1. $p' = \texttt{@nop}$. Then π_1 and π_2 are instances of **or-nop1**. Hence $p_1 = p_2 = \mathit{clear}(p'')$ and $e_1 = e_2 = \varepsilon$.

Case 11.1.2. $p' = \texttt{break}$. Similar to Case 9.1.2.

Case 11.1.3. $p' \neq \texttt{@nop}, \texttt{break}$. Similar to Case 9.1.3.

Case 11.2. $\mathit{isblocked}(p', n)$. There are three subcases.

Case 11.2.1. $p'' = \texttt{@nop}$. Then π_1 and π_2 are instances of **or-nop1**. Hence $p_1 = p_2 = \mathit{clear}(p')$ and $e_1 = e_2 = \varepsilon$.

Case 11.2.2. $p'' = \texttt{break}$. Similar to Case 9.2.2.

Case 11.2.3. $p'' \neq \texttt{@nop}, \texttt{break}$. Similar to Case 9.2.3.

□

Theorem 4.3 (Determinism). *$\delta \xrightarrow{i} \delta_1$ and $\delta \xrightarrow{i} \delta_2$ implies $\delta_1 = \delta_2$.*

Proof. By induction on i. The theorem is trivially true if $i = 0$ and follows directly from Lemmas 4.1 and 4.2 for $i = 1$. Suppose

$$\delta \xrightarrow{1} \delta_1' \xrightarrow{i-1} \delta_1 \quad \text{and} \quad \delta \xrightarrow{1} \delta_2' \xrightarrow{i-1} \delta_2 ,$$

for some $i > 1$, δ_1' and δ_2'. There are two possibilities.

Case 1. $\delta \xrightarrow[out]{1} \delta_1'$ and $\delta \xrightarrow[out]{1} \delta_2'$. Then, by Lemma 4.1, $\delta_1' = \delta_2'$, and by the induction hypothesis, $\delta_1 = \delta_2$.

Case 2. $\delta \xrightarrow[nst]{1} \delta_1'$ and $\delta \xrightarrow[nst]{1} \delta_2'$. Then, by Lemma 4.2, $\delta_1' = \delta_2'$, and by the induction hypothesis, $\delta_1 = \delta_2$. □

Proposition 4.5. *If $\delta \xrightarrow[nst]{i} \delta'_{\#nst}$ then, for all $k \neq i$, there is no $\delta''_{\#nst}$ such that $\delta \xrightarrow[nst]{k} \delta''_{\#nst}$.*

Proof. By contradiction on the hypothesis that there is such k. Let $\delta \xrightarrow[nst]{i} \delta'_{\#nst}$, for some $i \geq 0$. There are two cases.

Case 1. Suppose there are $k > i$ and $\delta''_{\#nst}$ such that $\delta \xrightarrow[nst]{k} \delta''$. Then, by definition of $\xrightarrow[nst]{k}$,

$$\delta \xrightarrow[nst]{i} \delta' \xrightarrow[nst]{i+1} \delta_1' \xrightarrow[nst]{i+2} \cdots \xrightarrow[nst]{k} \delta''. \tag{1}$$

Since $\delta' = \langle p', n, e' \rangle$ is nested-irreducible, $e' = \varepsilon$ or $p = \texttt{@nop}, \texttt{break}$ or $isblocked(p', n)$. In any of these cases, by the definition of $\xrightarrow[nst]{}$, there is no δ_1' such that $\delta' \xrightarrow[nst]{1} \delta_1'$, which contradicts (1). Therefore, no such k can exist.

Case 2. Suppose there are $k < i$ and $\delta''_{\#nst}$ such that $\delta \xrightarrow[nst]{k} \delta''$. Then, since $i > k$, by Case 1, δ' cannot exist, which is absurd. Therefore, the assumption that there is such k is false. □

Lemma 4.6.

If $\langle p_1, n, e \rangle \xrightarrow[nst]{i} \langle p_1', n, e' \rangle$, for any p_2:

(a) $\langle p_1; p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1'; p_2, n, e' \rangle$.
(b) $\langle p_1 \texttt{ @loop } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1' \texttt{ @loop } p_2, n, e' \rangle$.
(c) $\langle p_1 \texttt{ @and } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1' \texttt{ @and } p_2, n, e' \rangle$.
(d) $\langle p_1 \texttt{ @or } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1' \texttt{ @or } p_2, n, e' \rangle$.

If $\langle p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_2', n, e' \rangle$, for any p_1 such that $isblocked(p_1, n)$:

(a) $\langle p_1 \texttt{ @and } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1 \texttt{ @and } p_2', n, e' \rangle$.
(b) $\langle p_1 \texttt{ @or } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1 \texttt{ @or } p_2', n, e' \rangle$.

Proof. By induction on i.

(a) The lemma is trivially true for $i = 0$, as $p_1 = p_1'$, and follows directly from **seq-adv** for $i = 1$. Suppose

$$\langle p_1, n, e \rangle \xrightarrow[nst]{1} \langle p_1'', n, e'' \rangle \xrightarrow[nst]{i-1} \langle p_1', n, e' \rangle , \tag{2}$$

for some $i > 1$. Then $\langle p_1'', n, e'' \rangle$ is not nested-irreducible, i.e., $e = \varepsilon$ and $p \neq \texttt{@nop}, \texttt{break}$ and $\neg\, isblocked(p_1'', n)$. By (2) and by **seq-adv**,

$$\langle p_1; p_2, n, e \rangle \xrightarrow[nst]{1} \langle p_1''; p_2, n, e'' \rangle . \tag{3}$$

From (2), by the induction hypothesis,

$$\langle p_1''; p_2, n, e'' \rangle \xrightarrow[nst]{i-1} \langle p_1'; p_2, n, e' \rangle . \tag{4}$$

From (3) and (4),

$$\langle p_1; p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1'; p_2, n, e' \rangle .$$

(b) Similar to item (a).
(c) Similar to item (a).
(d) Similar to item (a).
(e) The lemma is trivially true for $i = 0$, as $p_2 = p_2'$, and follows directly from **and-adv2** for $i = 1$. Suppose

$$\langle p_2, n, e \rangle \xrightarrow[nst]{1} \langle p_2'', n, e'' \rangle \xrightarrow[nst]{i-1} \langle p_2', n, e' \rangle , \tag{5}$$

for some $i > 1$. Then $\langle p_2'', n, e'' \rangle$ is not nested-irreducible. By (5) and by **and-adv2**,

$$\langle p_1 \texttt{ @and } p_2, n, e \rangle \xrightarrow[nst]{1} \langle p_1 \texttt{ @and } p_2'', n, e'' \rangle . \tag{6}$$

From (5), by the induction hypothesis,

$$\langle p_1 \texttt{ @and } p_2'', n, e'' \rangle \xrightarrow[nst]{i-1} \langle p_1 \texttt{ @or } p_2', n, e' \rangle . \tag{7}$$

From (6) and (7),

$$\langle p_1 \texttt{ @and } p_2, n, e \rangle \xrightarrow[nst]{i} \langle p_1 \texttt{ @and } p_2', n, e' \rangle .$$

(f) Similar to item (a). □

Theorem 4.8. *For any δ there is a $\delta'_{\#nst}$ such that $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$.*

Proof. By induction on the structure of programs. Let $\delta = \langle p, n, \varepsilon \rangle$. The theorem is trivially true if δ is nested-irreducible, as by definition $\delta \xrightarrow[nst]{0} \delta$. Suppose that is not the case. Then, depending on the structure of p, there are 11 possibilities. In each one of them, we show that such $\delta'_{\#nst}$ indeed exists.

Case 1. $p = \texttt{mem}(id)$. Then, by **mem**,

$$\langle \texttt{mem}(id), n, \varepsilon \rangle \xrightarrow[nst]{1} \langle \texttt{@nop}, n, \varepsilon \rangle_{\#nst} .$$

Case 2. $p = \texttt{emit}_{int}(e)$. Then, by **emit-int**,

$$\langle \texttt{emit}_{int}(e), n, \varepsilon \rangle \xrightarrow[nst]{1} \langle \texttt{@canrun}(n), n, e \rangle_{\#nst} .$$

Case 3. $p = \texttt{@canrun}(n)$. Then, by **can-run**,

$$\langle \texttt{@canrun}(n), n, \varepsilon \rangle \xrightarrow[nst]{1} \langle \texttt{@nop}, n, \varepsilon \rangle_{\#nst} .$$

Case 4. $p = \texttt{if mem}(id) \texttt{ then } p' \texttt{ else } p''$. There are two subcases.

Case 4.1. $eval(\texttt{mem}(id))$. Then, by **if-true** and by the induction hypothesis, there is a δ' such that

$$\langle \texttt{if mem}(id) \texttt{ then } p' \texttt{ else } p'', n, \varepsilon \rangle \xrightarrow[nst]{1} \langle p', n, e \rangle \xrightarrow[nst]{*} \delta'_{\#nst} .$$

Case 4.2. $\neg\, eval(\texttt{mem}(id))$. Similar to Case 4.1.

Case 5. $p = p'; p''$. There are three subcases.

Case 5.1. $p' = \texttt{@nop}$. Then, by **seq-nop** and by the induction hypothesis, there is a δ' such that

$$\langle \texttt{@nop}; p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle p'', n, e\rangle \xrightarrow[nst]{*} \delta'_{\#nst} .$$

Case 5.2. $p' = \texttt{break}$. Then, by **seq-brk**,

$$\langle \texttt{break}; p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \texttt{break}, n, \varepsilon\rangle_{\#nst} .$$

Case 5.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then, by the induction hypothesis, there are p'_1 and e such that

$$\langle p', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1, n, e\rangle_{\#nst} .$$

By item (a) of Lemma 4.6,

$$\langle p'; p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1; p'', n, e\rangle . \tag{8}$$

It remains to be shown that $\langle p'_1; p'', n, e\rangle$ is nested-irreducible. There are four possibilities following from the fact that the simpler $\langle p'_1, n, e\rangle$ is nested-irreducible.

Case 5.3.1. $e \neq \varepsilon$. Then, by the definition of $\#nst$, description $\langle p'_1; p'', n, e\rangle$ is nested-irreducible.

Case 5.3.2. $p'_1 = \texttt{@nop}$. From (8),

$$\langle p'; p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{@nop}; p'', n, e\rangle .$$

From this point on, this case is similar to Case 5.1.

Case 5.3.3. $p'_1 = \texttt{break}$. From (8),

$$\langle p'; p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{break}; p'', n, e\rangle .$$

From this point on, this case is similar to Case 5.2.

Case 5.3.4. $isblocked(p'_1, n)$. Then, by definition,

$$isblocked(p'_1; p'', n) = isblocked(p'_1, n) = true .$$

Hence from (8) and by the definition $\#nst$, description $\langle p'_1; p'', n, e\rangle$ is nested-irreducible.

Case 6. $p = \texttt{loop}\, p'$. Then, by item (b) of Assumption 4.7,

$$\langle \texttt{loop}\, p', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1, n, e\rangle , \tag{9}$$

for some e and p'_1 such that either $p'_1 = \texttt{break}\ \texttt{@loop}\, p'$ or $isblocked(p'_1, n)$.

Case 6.1. $p'_1 = \texttt{break}\ \texttt{@loop}\, p'$. From (9), by **loop-brk**,

$$\langle \texttt{loop}\, p', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{break}\ \texttt{@loop}\, p', n, e\rangle \xrightarrow[nst]{1} \langle \texttt{@nop}, n, e\rangle_{\#nst} .$$

Case 6.2. $isblocked(p'_1, n)$. Hence from (9) and by the definition of $\#nst$, $\langle p'_1, n, e\rangle_{\#nst}$.

Case 7. $p = p'\ \texttt{@loop}\, p''$. There are three subcases.

Case 7.1. $p' = \texttt{@nop}$. Then, by **loop-nop**,

$$\langle \texttt{@nop}\ \texttt{@loop}\, p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \texttt{loop}\, p'', n, \varepsilon\rangle .$$

From this point on, this case is similar to Case 6.

Case 7.2. $p' = \texttt{break}$. Then, by **loop-brk**,

$$\langle \texttt{break}\ \texttt{@loop}\, p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \texttt{@nop}, n, \varepsilon\rangle_{\#nst} .$$

Case 7.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then, by the induction hypothesis, there are p'_1 and e such that

$$\langle p', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1, n, e\rangle_{\#nst} .$$

By item (b) of Lemma 4.6,

$$\langle p'\ \texttt{@loop}\, p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1\ \texttt{@loop}\, p'', n, e\rangle .$$

It remains to be show that $\langle p'_1\ \texttt{@loop}\, p'', n, e\rangle$ is nested-irreducible. The rest of this proof is similar to that of Case 5.3.

Case 8. $p = p'$ and p''. Then, by **and-expd**,

$$\langle p' \texttt{ and } p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle p'\ \texttt{@and}\ (\texttt{@canrun}(n); p''), n, \varepsilon\rangle .$$

From this point on, this case is similar to Case 9.

Case 9. $p = p'\ \texttt{@and}\ p''$. There are two subcases.

Case 9.1. $\neg\, isblocked(p', n)$. There are three subcases.

Case 9.1.1. $p' = \texttt{@nop}$. Then, by **and-nop1** and by the induction hypothesis, there is a δ' such that

$$\langle \texttt{@nop}\ \texttt{@and}\ p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \delta'_{\#nst} .$$

Case 9.1.2. $p' = \texttt{break}$. Then, by **and-brk1**,

$$\langle \texttt{break}\ \texttt{@and}\ p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle clear(p''); \texttt{break}, n, \varepsilon\rangle . \tag{10}$$

From (10), by item (a) of Assumption 4.7 and by **seq-nop**,

$$\langle clear(p''); \texttt{break}, n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{@nop}; \texttt{break}, n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \texttt{break}, n, \varepsilon\rangle_{\#nst} .$$

Case 9.1.3. $p' \neq \texttt{@nop}, \texttt{break}$. Then, by the induction hypothesis, there are p'_1 and e such that

$$\langle p', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1, n, e\rangle_{\#nst} .$$

By item (c) of Lemma 4.6,

$$\langle p'\ \texttt{@and}\ p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p'_1\ \texttt{@and}\ p'', n, e\rangle .$$

It remains to be show that $\langle p'_1\ \texttt{@and}\ p'', n, e\rangle$ leads to an nested-irreducible description. There are four possibilities following from the fact that the simpler $\langle p'_1, n, e\rangle$ is nested-irreducible.

1. If $e \neq \varepsilon$ then, by definition, $\langle p'_1\ \texttt{@and}\ p'', n, e\rangle_{\#nst}$.
2. If $p'_1 = \texttt{@nop}$, this case is similar to Case 9.1.1.
3. If $p'_1 = \texttt{break}$, this case is similar to Case 9.1.2.
4. If $isblocked(p'_1, n)$, this case is similar to Case 9.2.

Case 9.2. $isblocked(p', n)$. There are three subcases.

Case 9.2.1. $p'' = \texttt{@nop}$. Then, by **and-nop2**,

$$\langle p'\ \texttt{@and}\ \texttt{@nop}, n, \varepsilon\rangle \xrightarrow[nst]{1} \langle p', n, \varepsilon\rangle_{\#nst} .$$

Case 9.2.2. $p'' = \texttt{break}$. Then, by **and-brk2**,

$$\langle p'\ \texttt{@and}\ \texttt{break}, n, \varepsilon\rangle \xrightarrow[nst]{1} \langle clear(p'); \texttt{break}, n, \varepsilon\rangle .$$

From this point on, this case is similar to Case 9.1.2.

Case 9.2.3. $p'' \neq \texttt{@nop}, \texttt{break}$. Then, by the induction hypothesis, there are p_1'' and e such that

$$\langle p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p_1'', n, e\rangle_{\#nst}\,.$$

By item (a) of Lemma 4.6,

$$\langle p' \texttt{ @and } p'', n, \varepsilon\rangle \xrightarrow[nst]{*} \langle p' \texttt{ @and } p_1'', n, e\rangle\,.$$

It remains to be show that $\langle p' \texttt{ @and } p_1'', n, e\rangle$ leads to an nested-irreducible description. There are four possibilities following from the fact that the simpler $\langle p_1'', n, e\rangle$ is nested-irreducible.

1. If $e \neq \varepsilon$ then, by definition, $\langle p' \texttt{ @and } p_1'', n, e\rangle_{\#nst}$.
2. If $p_1'' = \texttt{@nop}$, this case is similar to Case 9.2.1.
3. If $p_1'' = \texttt{break}$, this case is similar to Case 9.2.2.
4. If $\mathit{isblocked}(p_1'', n)$ then, as both sides are blocked, by definition, $\langle p' \texttt{ @and } p_1'', n, e\rangle_{\#nst}$.

Case 10. $p = p' \texttt{ or } p''$. Then, by **or-expd**,

$$\langle p' \texttt{ or } p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle p' \texttt{ @or (@canrun}(n); p''), n, \varepsilon\rangle\,.$$

From this point on, this case is similar to Case 11.

Case 11. $p = p' \texttt{ @or } p''$. There are two subcases.

Case 11.1. $\neg\, \mathit{isblocked}(p', n)$. There are three subcases.

Case 11.1.1. $p' = \texttt{@nop}$. Then, by **or-nop1**,

$$\langle \texttt{@nop @or } p'', n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \mathit{clear}(p''), n, \varepsilon\rangle\,. \tag{11}$$

From (11), by item (a) Assumption 4.7,

$$\langle \mathit{clear}(p''), n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{@nop}, n, \varepsilon\rangle_{\#nst}\,.$$

Case 11.1.2. $p' = \texttt{break}$. Similar to Case 9.1.2.

Case 11.1.3. $p' \neq \texttt{@nop}, \texttt{break}$. Similar to Case 9.1.3.

Case 11.2. $\mathit{isblocked}(p', n)$. There are three subcases.

Case 11.2.1. $p'' = \texttt{@nop}$. Then, by **or-nop2**,

$$\langle p' \texttt{ @or @nop}, n, \varepsilon\rangle \xrightarrow[nst]{1} \langle \mathit{clear}(p'), n, \varepsilon\rangle\,. \tag{12}$$

From (12), by item (a) of Assumption 4.7 and by definition of *clear*,

$$\langle \mathit{clear}(p'), n, \varepsilon\rangle \xrightarrow[nst]{*} \langle \texttt{@nop}, n, \varepsilon\rangle_{\#nst}\,.$$

Case 11.2.2. $p'' = \texttt{break}$. Similar to Case 9.2.2.

Case 11.2.3. $p'' \neq \texttt{@nop}, \texttt{break}$. Similar to Case 9.2.3. □

Lemma 4.12.

(a) If $\delta \xrightarrow[out]{push} \delta'$ *then* $\mathit{rank}(\delta) = \mathit{rank}(\delta')$.
(b) If $\delta \xrightarrow[out]{pop} \delta'$ *then* $\mathit{rank}(\delta) > \mathit{rank}(\delta')$.

Proof. Let $\delta = \langle p, n, e\rangle$, $\delta' = \langle p', n', e'\rangle$, $\mathit{rank}(\delta) = \langle i, j\rangle$, and $\mathit{rank}(\delta') = \langle i', j'\rangle$.

(a) Suppose $\langle p, n, e\rangle \xrightarrow[out]{push} \langle p', n', e'\rangle$. Then, by **push**, $e \neq \varepsilon$, $p' = \mathit{bcast}(p, e)$, $n' = n + 1$, and $e' = \varepsilon$. By Definition 4.10, $j = n + 1$, as $e \neq \varepsilon$, and $j' = n + 1$, as $e' = \varepsilon$ and $n' = n + 1$; hence $j = j'$. It remains to be shown that $i = i'$:

$$\begin{aligned}
i &= \mathit{pot}(p, e) && \text{by Definition 4.10}\\
&= \mathit{pot}'(\mathit{bcast}(p, e)) && \text{by Definition 4.9}\\
&= \mathit{pot}'(p') && \text{since } p' = \mathit{bcast}(p, e)\\
&= \mathit{pot}'(\mathit{bcast}(p', \varepsilon)) && \text{by definition of } \mathit{bcast}\\
&= \mathit{pot}'(\mathit{bcast}(p', e')) && \text{since } e' = \varepsilon\\
&= \mathit{pot}(p', e') && \text{by Definition 4.9}\\
&= i' && \text{by Definition 4.10}
\end{aligned}$$

Therefore, $\langle i, j\rangle = \langle i', j'\rangle$.

(b) Suppose $\langle p, n, e\rangle \xrightarrow[out]{pop} \langle p', n', e'\rangle$. Then, by **pop**, $p = p'$, $n > 0$, $n' = n - 1$, and $e = e' = \varepsilon$. By Definition 4.9, $\mathit{pot}(\mathit{bcast}(p, e)) = \mathit{pot}(\mathit{bcast}(p', e'))$; hence $i = i'$. And by Definition 4.10, $j = n$, as $e = \varepsilon$, and $j' = n - 1$, as $e' = \varepsilon$ and $n' = n - 1$; hence $j > j'$. Therefore, $\langle i, j\rangle > \langle i', j'\rangle$. □

Lemma 4.13. *If* $\delta \xrightarrow[nst]{} \delta'$ *then* $\mathit{rank}(\delta) \geq \mathit{rank}(\delta')$.

Proof. We proceed by induction on the structure of $\xrightarrow[nst]{}$ derivations. Let $\delta = \langle p, n, e\rangle$, $\delta' = \langle p', n', e'\rangle$, $\mathit{rank}(\delta) = \langle i, j\rangle$, and $\mathit{rank}(\delta') = \langle i', j'\rangle$. By the hypothesis of the lemma, there is a derivation π such that

$$\pi \Vdash \langle p, n, e\rangle \xrightarrow[nst]{} \langle p', n', e'\rangle\,.$$

By definition of $\xrightarrow[nst]{}$, $e = \varepsilon$ and $n = n'$. Depending on the structure of program p, there are 11 possibilities. In each one of them we show that $\mathit{rank}(\delta) \geq \mathit{rank}(\delta')$.

Case 1. $p = \texttt{mem}(id)$. Then π is an instance of **mem**. Hence $p' = \texttt{@nop}$ and $e' = \varepsilon$. Thus $\mathit{rank}(\delta) = \mathit{rank}(\delta') = \langle 0, n\rangle$.

Case 2. $p = \texttt{emit}_{int}(e_1)$. Then π is an instance of **emit-int**. Hence $p' = \texttt{@canrun}$ and $e' = e_1 \neq \varepsilon$. Thus

$$\mathit{rank}(\delta) = \langle 1, n\rangle > \langle 0, n + 1\rangle = \mathit{rank}(\delta')\,.$$

Case 3. $p = \texttt{@canrun}(n)$. Then π is an instance of **can-run**. Hence $p' = \texttt{@nop}$ and $e' = \varepsilon$. Thus

$$\mathit{rank}(\delta) = \mathit{rank}(\delta') = \langle 0, n\rangle\,.$$

Case 4. $p = \texttt{if } p \texttt{ then } p_1 \texttt{ else } p_2$. There are two subcases.

Case 4.1. $\mathit{eval}(\texttt{mem}(id))$. Then π is an instance of **if-true**. Hence $p' = p_1$ and $e' = \varepsilon$. Thus

$$\begin{aligned}
\mathit{rank}(\delta) &= \langle \max\{\mathit{pot}'(p_1), \mathit{pot}'(p_2)\}, n\rangle\\
&\geq \langle \mathit{pot}'(p_1), n\rangle = \mathit{rank}(\delta')\,.
\end{aligned}$$

Case 4.2. $\neg\, \mathit{eval}(\texttt{mem}(id))$. Similar to Case 4.1.

Case 5. $p = p_1; p_2$. There are three subcases.

Case 5.1. $p_1 = \texttt{@nop}$. Then π is an instance of **seq-nop**. Hence $p' = p_2$ and $e' = \varepsilon$. Thus

$$\begin{aligned}
\mathit{rank}(\delta) &= \langle \mathit{pot}'(p_1) + \mathit{pot}'(p_2), n\rangle\\
&\geq \langle \mathit{pot}'(p_2), n\rangle = \mathit{rank}(\delta')\,.
\end{aligned}$$

Case 5.2. $p_1 = \texttt{break}$. Then π is an instance of **seq-brk**. Hence $p' = p_1$ and $e' = \varepsilon$. Thus

$$rank(\delta) = rank(\delta') = \langle 0, n\rangle\,.$$

Case 5.3. $p_1 \neq \texttt{@nop}, \texttt{break}$. Then π is an instance of **seq-adv**. Hence there is a derivation π' such that

$$\pi' \Vdash \langle p_1, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1', n, e_1'\rangle\,,$$

for some p_1' and e_1'. Thus $p' = p_1'; p_2$ and $e' = e_1'$. By the induction hypothesis,

$$rank(\langle p_1, n, \varepsilon\rangle) \geq rank(\langle p_1', n, e_1'\rangle)\,. \tag{13}$$

There are two subcases.

Case 5.3.1. $e' = \varepsilon$ Then

$$rank(\delta) = \langle pot'(p_1) + pot'(p_2), n\rangle \text{ and}$$
$$rank(\delta') = \langle pot'(p_1') + pot'(p_2), n\rangle\,.$$

By (13), $pot'(p_1) \geq pot'(p_1')$. Thus

$$rank(\delta) \geq rank(\delta')\,.$$

Case 5.3.2. $e' \neq \varepsilon$. Then π' contains one application of **emit-int**, which consumes one $\texttt{emit}_{int}(e')$ statement from p_1 and implies $pot'(p_1) > pot'(p_1')$. Thus

$$rank(\delta) = \langle pot'(p_1) + pot'(p_2), n\rangle$$
$$> \langle pot'(p_1') + pot'(p_2), n+1\rangle = rank(\delta')\,.$$

Case 6. $p = \texttt{loop}\, p_1$. Then π is an instance of **loop-expd**. Hence $p' = p_1\ \texttt{@loop}\, p_1$ and $e' = \varepsilon$. By item (b) of Assumption 4.7, all execution paths of p_1 contain at least one occurrence of break or $\texttt{await}_{ext}$. Thus, by condition (†) in Definition 4.9,

$$rank(\delta) = rank(\delta') = \langle pot'(p_1), n\rangle\,.$$

Case 7. $p = p_1\ \texttt{@loop}\, p_2$. There are three subcases.

Case 7.1. $p_1 = \texttt{@nop}$. Similar to Case 5.1.

Case 7.2. $p_1 = \texttt{break}$. Similar to Case 5.2.

Case 7.3. $p_1 \neq \texttt{@nop}, \texttt{break}$. Then π is an instance of **loop-adv**. Hence there is a derivation π' such that

$$\pi' \Vdash \langle p_1, n, \varepsilon\rangle \xrightarrow[nst]{} \langle p_1', n, e_1'\rangle\,,$$

for some p_1' and e_1'. Thus $p' = p_1'\ \texttt{@loop}\ p_2$ and $e' = e_1'$. There are two subcases.

Case 7.3.1. $pot'(p) = pot'(p_1)$. Then every execution path of p_1 contains a break or $\texttt{await}_{ext}$ statement. A single $\xrightarrow[nst]{}$ cannot terminate the loop, since $p_1 \neq \texttt{break}$, nor can it consume an $\texttt{await}_{ext}$, which means that all execution paths in p_1' still contain a break or $\texttt{await}_{ext}$. Hence $pot'(p') = pot'(p_1')$. The rest of this proof is similar to that of Case 5.3.

Case 7.3.2. $pot'(p) = pot'(p_1) + pot'(p_2)$. Then some execution path in p_1 does not contain a break or $\texttt{await}_{ext}$ statement. Since $p_1 \neq \texttt{@nop}$, a single $\xrightarrow[nst]{}$ cannot restart the loop, which means that p_1' still contain some execution path in which a break or $\texttt{await}_{ext}$ does not occur. Hence $pot'(p') = pot'(p_1') + pot'(p_2)$. The rest of this proof is similar to that of Case 5.3.

Case 8. $p = p_1$ and p_2. Then π is an instance of **and-expd**. Hence $p' = p_1\ \texttt{@and}\ (\texttt{@canrun}(n); p_2)$ and $e' = \varepsilon$. Thus

$$rank(\delta) = rank(\delta') = \langle pot'(p_1) + pot'(p_2), n\rangle\,.$$

Case 9. $p = p_1\ \texttt{@and}\ p_2$. There are two subcases. stack level n.

Case 9.1. $\neg\, isblocked(p_1, n)$. There are three subcases.

Case 9.1.1. $p_1 = \texttt{@nop}$. Then π is an instance of **and-nop1**. Hence $p' = p_2$ and $e' = \varepsilon$. Thus

$$rank(\delta) = rank(\delta') = \langle 0 + pot'(p_2), n\rangle\,.$$

Case 9.1.2. $p_1 = \texttt{break}$. Then π is an instance of **and-brk1**. Hence $p' = clear(p_2)$; break and $e' = \varepsilon$. By item (a) of Assumption 4.7 and by the definition of *clear*, $clear(p_2)$ does not contain $\texttt{emit}_{int}$ statements. Thus

$$rank(\delta) = rank(\delta') = \langle 0, n\rangle\,.$$

Case 9.1.3. $p_1 \neq \texttt{@nop}, \texttt{break}$. Then π is an instance of **and-adv1**. As $p_1 \neq \texttt{break}$ and $p_2 \neq \texttt{break}$ (otherwise **and-brk2** would have taken precedence), the rest of this proof is similar to that of Case 5.3.

Case 9.2. $isblocked(p_1, n)$. Similar to Case 9.1

Case 10. $p = p_1\ \texttt{or}\ p_2$. Then π is an instance of **or-expd**. Hence $p' = p_1\ \texttt{@or}\ (\texttt{@canrun}(n); p_2)$ and $e' = \varepsilon$. Thus

$$rank(\delta) = rank(\delta') = \langle pot'(p_1) + pot'(p_2), n\rangle\,.$$

Case 11. $p = p_1\ \texttt{@or}\ p_2$. There are two subcases.

Case 11.1. $\neg\, isblocked(p_1, n)$. There are three subcases.

Case 11.1.1. $p_1 = \texttt{@nop}$. Then π is an instance of **or-nop1**. Hence $p' = clear(p_2)$ and $e' = \varepsilon$. By item (a) of Assumption 4.7 and by the definition of *clear*, p' does not contain $\texttt{emit}_{int}$ statements. Thus

$$rank(\delta) = rank(\delta') = \langle 0, n\rangle\,.$$

Case 11.1.2. $p_1 = \texttt{break}$. Similar to Case 9.1.2.

Case 11.1.3. $p_1 \neq \texttt{@nop}, \texttt{break}$. Similar to Case 9.1.3.

Case 11.2. $isblocked(p_1, n)$. Similar to Case 11.1. □

Theorem 4.14. *If* $\delta \xrightarrow[nst]{*} \delta'$ *then* $rank(\delta) \geq rank(\delta')$.

Proof. If $\delta \xrightarrow[nst]{*} \delta'$ then $\delta \xrightarrow[nst]{i} \delta'$, for some i. We proceed by induction on i. The theorem is trivially true for $i = 0$ and follows directly from Lemma 4.13 for $i = 1$. Suppose $\delta \xrightarrow[nst]{1} \delta_1' \xrightarrow[nst]{i-1} \delta'$, for some $i > 1$ and δ_1'. Thus, by Lemma 4.13 and by the induction hypothesis,

$$rank(\delta) \geq rank(\delta_1') \geq rank(\delta')\,.$$ □

Theorem 4.15 (Termination)**.** *For any* δ, *there is a* $\delta_\#'$ *such that* $\delta \xrightarrow{*} \delta_\#'$.

Proof. By lexicographic induction on $rank(\delta)$. Let $\delta = \langle p, n, e\rangle$ and $rank(\delta) = \langle i, j\rangle$.

Basis. If $\langle i, j\rangle = \langle 0, 0\rangle$ then δ cannot be advanced by $\xrightarrow[out]{}$, as $j = 0$ implies $e = \varepsilon$ and $n = 0$ (neither **push** nor **pop** can be applied). There are two possibilities: either δ is nested irreducible or it is not. In the first case, the theorem is trivially true, as $\delta \xrightarrow[nst]{0} \delta_{\#nst}$. Suppose δ is not nested irreducible. Then, by Theorem 4.8, $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$, for some $\delta'_{\#nst}$. By Theorem 4.14,

$$\langle i, j\rangle = \langle 0, 0\rangle \geq rank(\delta'),$$

which implies $rank(\delta') = \langle 0, 0\rangle$.

Induction. Let $\langle i, j\rangle \neq \langle 0, 0\rangle$. There are two subcases.

Case 1. δ is nested-irreducible. There are two subcases.

Case 1.1. $j = 0$. By Definition 4.11, $\delta_{\#}$. Thus $\delta \xrightarrow{0} \delta_{\#}$.

Case 1.2. $j > 0$. There are two subcases. event.

Case 1.2.1. $e \neq \varepsilon$. Then, by **push** and by Theorem 4.8, there are δ'_1 and $\delta'_{\#nst} = \langle p', n + 1, e'\rangle$ such that

$$\delta \xrightarrow[out]{push} \delta'_1 \xrightarrow[nst]{*} \delta'_{\#nst}.$$

Thus, by item (a) of Lemma 4.12 and by Theorem 4.14,

$$\begin{aligned} rank(\delta) = rank(\delta'_1) &= \langle i, j\rangle \\ &\geq rank(\delta') = \langle i', j'\rangle. \end{aligned}$$

If $e' = \varepsilon$, then $i = i'$ and $j = j'$, and the rest of this proof is similar to that of Case 1.2.2. Otherwise, if $e' \neq \varepsilon$ then $i > i'$, since an $\texttt{emit}_{int}(e')$ was consumed by the nested transitions. Thus,

$$rank(\delta) > rank(\delta').$$

By the induction hypothesis, $\delta' \xrightarrow{*} \delta''_{\#}$, for some $\delta''_{\#}$. Therefore, $\delta \xrightarrow{*} \delta''_{\#}$.

Case 1.2.2. $e = \varepsilon$. Then, since $j > 0$, $\delta \xrightarrow[out]{pop} \delta'$, for some δ''. By item (b) of Lemma 4.12,

$$rank(\delta) > rank(\delta').$$

Hence by the induction hypothesis, there is a $\delta''_{\#}$ such that $\delta' \xrightarrow{*} \delta''_{\#}$. Therefore, $\delta \xrightarrow{*} \delta''_{\#}$.

Case 2. δ is not nested-irreducible. Then $e = \varepsilon$ and, by Theorems 4.8 and 4.14, there is a $\delta'_{\#nst}$ such that $\delta \xrightarrow[nst]{*} \delta'_{\#nst}$ with $rank(\delta) \geq rank(\delta'_{\#nst})$. The rest of this proof is similar to that of Case 1. □

Acknowledgments

This work was supported by the Serrapilheira Institute (grant number Serra-1708-15612).

References

[1] A. Adya et al. Cooperative task management without manual stack management. In *Proceedings of ATEC'02*, pages 289–302. USENIX Association, 2002.

[2] S. Andalam, P. Roop, and A. Girault. Predictable multithreading of embedded applications using PRET-C. In *Proceeding of MEMOCODE'10*, pages 159–168. IEEE, 2010.

[3] A. Benveniste, P. Caspi, S. A. Edwards, N. Halbwachs, P. L. Guernic, and R. D. Simone. The synchronous languages twelve years later. In *Proceedings of the IEEE*, volume 91, pages 64–83, Jan 2003.

[4] G. Berry. Preemption in concurrent systems. In *FSTTCS*, volume 761 of *LNCS*, pages 72–93. Springer, 1993.

[5] G. Berry. *The Constructive Semantics of Pure Esterel (draft version 3).* Ecole des Mines de Paris and INRIA, 1999.

[6] G. Berry. *The Esterel-V5 Language Primer.* CMA and Inria, Sophia-Antipolis, France, June 2000. Version 5.10, Release 2.0.

[7] T. Bourke and A. Sowmya. Delays in esterel. page 55, 2009.

[8] F. Boussinot. Reactive c: An extension of c to program reactive systems. *Software: Practice and Experience*, 21(4):401–428, 1991.

[9] F. Boussinot and R. De Simone. The Esterel language. *Proceedings of the IEEE*, 79(9):1293–1304, Sep 1991.

[10] R. de Simone, J.-P. Talpin, and D. Potop-Butucaru. The synchronous hypothesis and synchronous languages. In R. Zurawski, editor, *Embedded Systems Handbook*. 2005.

[11] A. Dunkels, O. Schmidt, T. Voigt, and M. Ali. Protothreads: simplifying event-driven programming of memory-constrained embedded systems. In *Proceedings of SenSys'06*, pages 29–42. ACM, 2006.

[12] D. Harel and A. Naamad. The STATEMATE semantics of statecharts. *ACM Transactions on Software Engineering and Methodology*, 5(4):293–333, 1996.

[13] M. Karpinski and V. Cahill. High-level application development is realistic for wireless sensor networks. In *Proceedings of SECON'07*, pages 610–619, 2007.

[14] I. Maier, T. Rompf, and M. Odersky. Deprecating the observer pattern. Technical report, 2010.

[15] ORACLE. Java thread primitive deprecation. http://docs.oracle.com/javase/6/docs/technotes/guides/concurrency/threadPrimitiveDeprecation.html (accessed in Aug-2014), 2011.

[16] G. D. Plotkin. A structural approach to operational semantics. Technical Report 19, Computer Science Departement, Aarhus University, Aarhus, Denmark, 1981.

[17] G. Salvaneschi et al. Rescala: Bridging between object-oriented and functional style in reactive applications. In *Proceedings of Modularity'13*, pages 25–36. ACM, 2014.

[18] F. Sant'anna, R. Ierusalimschy, N. Rodriguez, S. Rossetto, and A. Branco. The design and implementation of the synchronous language cÉu. *ACM Trans. Embed. Comput. Syst.*, 16(4):98:1–98:26, July 2017.

[19] F. Sant'Anna, N. Rodriguez, R. Ierusalimschy, O. Landsiedel, and P. Tsigas. Safe System-level Concurrency on Resource-Constrained Nodes. In *Proceedings of SenSys'13*. ACM, 2013.

[20] R. Santos, G. Lima, F. Sant'Anna, and N. Rodriguez. Céu-Media: Local Inter-Media Synchronization Using Céu. In *Proceedings of WebMedia'16*, pages 143–150, New York, NY, USA, 2016. ACM.

[21] M. von der Beeck. A comparison of statecharts variants. In *Proceedings of FTRTFT'94*, pages 128–148. Springer, 1994.

[22] R. von Hanxleden. Syncchart in c: a proposal for light-weight, deterministic concurrency. In *Proceedings EMSOFT'09*, pages 225–234. ACM, 2009.