# The Semantics and Implementation of CÉU: a Synchronous Reactive Language based on Esterel

Francisco Sant'Anna, Departamento de Informática, PUC-Rio Adriano Branco, Departamento de Informática, PUC-Rio Roberto Ierusalimschy, Departamento de Informática, PUC-Rio Noemi Rodriguez, Departamento de Informática, PUC-Rio Silvana Rossetto, Departamento de Ciência da Computação, UFRJ

CÉU is a reactive language based on Esterel that targets constrained embedded platforms. Employing the synchronous programming model, it allows for a simple reasoning about shared-memory concurrency. Furthermore, its restricted semantics enables deterministic and memory-safe programs. In this work, we propose a formal semantics for CÉU focusing on its particular control mechanisms, such as parallel compositions, finalization, and stack-based internal events. We also present an implementation with two backends: one aiming for resource efficiency and interoperability with C, and another for dynamic reprogramming over the air

Additional Key Words and Phrases: Concurrency, Determinism, Embedded Systems, Esterel, Synchronous, Reactivity

## 1. INTRODUCTION

An established alternative to C in the field of embedded systems is the family of reactive synchronous languages [Benveniste et al. 2003]. Two major styles of synchronous languages have evolved: in the control-imperative style, programs are structured with control flow primitives, such as parallelism, repetition, and preemption; in the dataflow-declarative style, programs can be seen as graphs of values, in which a change to a value is propagated through its dependencies without explicit programming. Among the control-based languages, Esterel [Boussinot and de Simone 1991] influenced a number of other embedded languages, such as Reactive-C [Boussinot 1991], OSM [Kasten and Römer 2005], Sync-C [Von Hanxleden 2009], and PRET-C [Andalam et al. 2010].

CÉU is another language targeting embedded systems based on Esterel with some novel functionalities:

- Stack-based internal events, which provide a limited form of coroutines.
- A static temporal analysis and deterministic execution semantics that allows programs to safely share memory.
- A finalization mechanism for safe abortion of lines of execution.
- First-class synchronized timers.

CÉU shares limitations with Esterel and synchronous languages in general: computations that run in unbounded time (e.g., cryptography, image processing) do not fit the zero-delay hypothesis [Potop-Butucaru et al. 2005], and cannot be elegantly implemented.

In this work, we present a formal semantics of a small synchronous kernel that represents a subset of  $C\acute{E}U$  covering these new functionalities. We also present an implementation of  $C\acute{E}U$  with two backends: one aiming for efficiency and interoperability with C, and another based on a virtual machine that allows remote reprogramming.

In previous work [Sant'Anna et al. 2013; Branco et al. 2015], we employed CÉU in the context of constrained wireless sensor networks, developing a number of applications, protocols, and drivers. We evaluated the expressiveness of CÉU in comparison to event-driven code in C, and observed a reduction in source code size (around 25%) with a small increase in memory usage (around 5–10% for ROM and RAM) [Sant'Anna

```
// ESTEREL
                                                         // CEU
                                                         loop do
    loop
       abort
                                                            par/or do
                                                               par/and do
              await A
                                                     5
                                                                   await A:
                                                                with
                                                     6
              await B
                                                                   await B;
                                                     8
                                                               end
           emit 0
                                                               emit 0;
                                                     9
       when R
                                                    10
                                                            with
10
    end
                                                               await R;
11
                                                    11
                                                            end
                                                    12
                                                    13
```

Fig. 1. A control specification implemented in Esterel and CÉU: "Emit O after A and B, resetting each R"

et al. 2013]. Considering the VM backend, a simple application that blinks three LEDs periodically occupies less than 100 bytes and can be completely transmitted in 4 radio messages. In contrast, without the preinstalled VM with basic I/O functionality, a C-compiled version occupies more than 2 kbytes [Branco et al. 2015].

The rest of the paper is organized as follows: Section 2 gives an overview of CÉU, exposing its fundamental differences to Esterel. Section 3 presents a formal semantics for the control primitives of CÉU. Section 4 presents the C and VM implementation backends. Section 5 discusses other synchronous languages targeting embedded systems. Section 6 concludes the paper.

#### 2. OVERVIEW OF CÉU

CÉU is a synchronous reactive language based on Esterel [Boussinot and de Simone 1991] with support for multiple concurrent lines of execution known as *trails*. By reactive, we mean that programs are stimulated by the environment through input events that are broadcast to all awaiting trails. By synchronous, we mean that all trails at any given moment are either reacting to the current event or are awaiting another event; in other words, trails are never reacting to different events.

In the sections that follow, we review the main differences between CÉU and Esterel [Sant'Anna et al. 2013]: queue-based external events and stack-based internal events (Section 2.1), shared-memory concurrency and determinism (Section 2.2), safe abortion with finalization (Section 2.3), and first-class timers (Section 2.4).

Regarding the similarities, Figure 1 shows side-by-side the implementations in Esterel and CÉU for the following control specification [Berry 2000]: "Emit an output O as soon as two inputs A and B have occurred. Reset this behavior each time the input R occurs". The first phrase of the specification, awaiting and emitting the events, is translated almost identically in the two languages (lines 4–9, in both implementations), as Esterel's '||' and CÉU's par/and constructs are equivalent. For the second phrase, the reset behavior, the Esterel version uses a abort-when (lines 3–10), which serves the same purpose of CÉU's par/or (lines 3–12): the occurrence of event R aborts the awaiting statements in parallel and restarts the loop.

CÉU, following the Esterel mindset, has a strong imperative flavor, with explicit control flow through sequences, loops, parallels, and also assignments. Being designed for control-intensive applications, it provides support for concurrent lines of execution and broadcast communication through events. In addition, CÉU also employs the synchronous model, in which programs advance in a sequence of discrete reactions to external events. Internal computations within a reaction (e.g. expressions, assignments, and native calls) are considered to take no time in accordance with the synchronous hypothesis [Potop-Butucaru et al. 2005]. The await statements are the only ones that halt a running reaction and allow a program to advance in this notion of time. To ensure

```
// ESTEREL
input A;
             // external
                                                  input void A;
                                                                  // external (in uppercase)
signal B;
            // internal
                                              3
                                                  event void b;
                                                                  // internal (in lowercase)
                                                  par/and do
                                                      await A;
    await A:
                                                      emit b;
    emit B;
                                              6
    call f();
                                                       _f();
                                              7
                                              8
                                                      await b;
    await B;
                                              9
    call g();
                                              10
                                                       _g();
                                              11
```

Fig. 2. Internal signals (events) in Esterel and CÉU.

that reactions run in bounded time and programs always progress, loops are statically required to contain at least one await statement in all possible paths [Sant'Anna et al. 2013; Berry 2000].

#### 2.1. Queue-Based External Events and Stack-Based Internal Events

Esterel makes no semantic distinctions between internal and external signals, both having only the notion of either presence or absence during an entire reaction [Berry 1993]. In CÉU, external input events are unique within reactions and programs cannot emit them, resulting in an intrinsic queue-based handling. In contrast, programs can emit internal events but these follow a stack-based execution policy, similar to subroutine calls in typical programming languages [Sant'Anna et al. 2013]. Figure 2 illustrates the use of internal signals (events) in Esterel and CÉU. In the version in Esterel, on the occurrence of A, B is emitted and they both become active, resulting in the invocation of f() and g() in no particular order. In the version in CÉU, the occurrence of A makes the program behave as follows (with the stack contents in italics):

- (1) 1st trail awakes (line 5), emits b, and pauses. stack: [1st-trail]
- (2) 2nd trail awakes (line 9), calls \_g(), and terminates. stack: [1st-trail]
- (4) Both trails have terminated, so the par/and rejoins, and the program also terminates;

Internal events bring support for a limited form of subroutines, as depicted in Figure 3. The subroutine inc is defined as a loop (lines 3–6) that continuously awaits its identifying event (line 4), incrementing the value passed as reference (line 5). A trail in parallel (lines 8–11) invokes the subroutine in reaction to event A through an emit (line 10). Given the stacked execution for internal events, the calling trail pauses, the subroutine awakes (line 4), runs its body (yielding v=2), loops, and awaits the next "call" (line 4, again). Only after this sequence that the calling trail resumes and passes the assertion test (line 11).

On the one hand, this form of subroutines has a significant limitation that it cannot express recursive calls: an <code>emit</code> to itself is always ignored, given that a running body cannot be awaiting itself. On the other hand, this very same limitation brings some important safety properties to subroutines: first, they are guaranteed to react in bounded time; second, memory for locals is also bounded, not requiring data stacks. Also, this form of subroutines can use the other primitives of CÉU, such as parallel compositions and the <code>await</code> statement. In particular, they await keeping context information such as locals and the program counter, similarly to coroutines [Moura and Ierusalimschy 2009].

```
event int* inc; // subroutine 'inc'
   par/or do
3
        loop do
                    // definitions are loops
            var int* p = await inc;
4
            *p = *p + 1;
5
6
        end
    with
7
        var int v = 1:
8
        await A;
9
        emit inc => &v; // call 'inc'
10
        _assert(v==2); // after return
11
    end
12
```

Fig. 3. Subroutine inc is defined in a loop (lines 3-6), in parallel with the caller (lines 8-11).

Another distinction regarding event handling in comparison to CÉU is that Esterel supports same-cycle bi-directional communication [Edwards 1999], i.e., two threads can react to one another during the same cycle due to mutual signal dependency. CÉU imposes a restriction that an await to an internal event is only valid for the next reaction, i.e., if an await and emit occur simultaneously in parallel trails, the await does not awake. These delayed awaits avoid corner cases of instantaneous termination and re-execution of statements in the same reaction inside loops (known as schizophrenic statements) [Tardieu and De Simone 2003]. The example that follows relies on this restriction to avoid infinite execution:

Even though both sides of the par/or have an await statement to avoid instantaneous termination, the loop would restart instantaneously if the await could awake in the same reaction that reaches it.

In atypical scenarios requiring immediate awake, delayed awaits can be circumvented by manually copying or making transformations in the code to execute on awake. From our experience, in some cases we need to execute a block of code periodically from internal event requests, *including the current reaction*, as illustrated in the left of Figure 4. In this case, moving the await to the end of the loop makes the periodic code to also execute immediately (and then in reactions to each emit request). If the periodic emit depends on a condition, the code becomes more intricate because we need to copy the condition test to the periodic code, as illustrated in the right of Figure 4.

## 2.2. Shared-Memory Concurrency and Determinism

Embedded applications make extensive use of shared memory, such as for sharing resources through memory-mapped registers. Hence, an important goal of CÉU is to ensure a reliable behavior for programs with concurrent lines of execution sharing memory. Esterel is only deterministic with respect to reactive control: "the same sequence of inputs always produces the same sequence of outputs" [Berry 2000]. However, the execution order for operations with side-effects within a reaction is non-deterministic: "if there is no control dependency, as in <<call f1() || call f2()>>, the order is unspecified and it would be an error to rely on it" [Berry 2000]. Therefore, Esterel forbids

```
event void e;
                                                  event void e:
par do
                                                  var bool should_execute = false;
    loop do
                                                  par do
        <...>
                                                      loop do
                  // periodic request
        emit e:
                                                          <...>
   end
                                                          if < \ldots > then
with
                                                              should_execute = true;
                                                               emit e:
   loop do
        <...>
                  // code to execute
                                                          end
        await e; // await after
                                                      end
   end
                                                  with
end
                                                      loop do
                                                          if should_execute then
                                                              <...> // code to execute
                                                          end
                                                          await e:
                                                      end
```

Fig. 4. Examples that circumvent the *delayed await* restriction by post-fixing the await inside the loop (in the left), and by copying the condition test (in the right).

```
input void A, B;
                                                   input void A;
var int x = 1:
                                                   var int y = 1;
par/and do
                                                   par/and do
    await A;
                                                       await A;
    x = x + 1;
                                                       y = y + 1;
with
                                                    ith
                                                       await A;
    await B;
    x = x * 2;
                                                       y = y * 2;
```

Fig. 5. Shared-memory concurrency in CÉU: the code in the left is safe because the trails access x atomically in different reactions; the code in the right is unsafe because both trails access y in the same reaction.

sharing memory between lines of execution: "if a variable is written by some thread, then it can neither be read nor be written by concurrent threads" [Berry 2000].

Concurrency in CÉU is characterized when two or more trail segments in parallel execute during the same reaction chain. A trail segment is a sequence of statements followed by an <code>await</code> (or termination). In the program in the left of Figure 5, the two assignments to <code>x</code> can never run concurrently, because each trail segment reacts to a different input event and, according to the semantics of CÉU, cannot occur simultaneously. However, the program in the right is non-deterministic, because the two assignments to <code>y</code> occur in the same reaction to input <code>A</code>.

CÉU performs a temporal analysis at compile time and detects concurrent accesses to shared variables, as follows [Sant'Anna et al. 2013]: if a variable is written in a trail segment, then a concurrent trail segment cannot read or write to that variable, nor dereference a pointer of that variable type. An analogous policy is applied for pointers vs variables and pointers vs pointers, as well as for system calls with side effects (e.g., printf).

Regardless of the temporal analysis of CÉU, when multiple trails are active during the same reaction, they are scheduled in the order they appear in the program source code. Therefore, even though the program in the right of Figure 5 is suspicious, the assignments to y are both atomic and deterministic, i.e., after the reaction to A terminates, the value of y is 4 ((1+1)\*2). On the one hand, enforcing an execution order for concurrent operations may seen arbitrary and also precludes true parallelism. On the other hand, it provides a priority scheme for trails, and makes shared-memory concurrency more tractable. For constrained embedded development, we believe that deter-

Fig. 6. In Esterel, the execution order between f1 and f2 is unspecified, whereas in CÉU, \_f1 executes before \_f2 due to deterministic scheduling based on lexical order.

```
input void STOP, RETRANSMIT, SENDACK;
                                                          11
    par/or do
                                                                  var _pkt_t buffer:
2
                                                          12
        await STOP;
3
                                                          13
                                                                  <fill-buffer-info>
    with
                                                                  finalize
4
                                                          14
                                                                       _send_engueue(&buffer);
5
        loop do
                                                          15
            par/or do
6
                                                          16
                 await RETRANSMIT;
                                                                       _send_cancel(&buffer);
                                                          17
8
             with
                                                          18
                                                                  end
                 par/and do
                                                                  await SENDACK:
9
                                                          19
10
                     await 1min:
                                                          20
11
                 with
12
                      var _pkt_t buffer;
13
                      <fill-buffer-info>
14
                      _send_enqueue (&buffer);
15
                      await SENDACK:
16
                 end
             end
17
        end
18
19
    end
```

Fig. 7. The unsafe network protocol in the left, which does not compile, is extended with a finalization clause in the right to handle abortion properly.

ministic shared-memory concurrency is beneficial, given the extensive use of memory mapped ports for I/O and the lack of hardware support for real parallelism. Other synchronous embedded languages, such as *SOL* [Karpinski and Cahill 2007] and *PRET-C*[Andalam et al. 2010], made a similar design choice.

Figure 6 compares the two syntactically equivalent code fragments in Esterel and CéU to summarize the semantic difference regarding (non-)determinism. Even though the program in CéU executes deterministically, the compiler still issues a warning, because an apparently innocuous reordering of trails modifies the semantics of the program. Note that in Esterel multiple external events can coexist in the same reaction, which disallows a similar temporal analysis.

### 2.3. Safe Abortion with Finalization

The introductory example of Figure 1 illustrates how synchronous languages can abort awaiting lines of execution without tweaking them with synchronization primitives. In contrast, traditional (asynchronous) multi-threaded languages cannot express thread termination safely [Berry 1993; ORACLE 2011]. Still, handling abortion when dealing with external resources is challenging because they are not subject to the same synchronous execution discipline.

To illustrate threats related to abortion, consider the unsafe example, which does not compile, in the left of Figure 7, describing the state machine of a data collection protocol for sensor networks [Gnawali et al. 2009; Sant'Anna et al. 2013]. The input events STOP, RETRANSMIT, and SENDACK (line 1) represent the external interface of the protocol. The protocol has to transmit a packet every minute, unless it receives a RETRANSMIT

request to immediately re-transmit it, or a STOP request to terminate. The protocol is composed of two main trails: one simply monitors the stopping event (line 3); the other periodically transmits the packet (lines 5–18). The periodic transmission is a loop that starts two other trails (lines 6-17): one handles the immediate retransmission request (line 7); the other transmits the packet<sup>1</sup> and waits for a confirmation (lines 9–16). The actual transmission (lines 12-15) is enclosed with a par/and that takes at least one minute before looping, in accordance with the specification. Note that the call to \_send\_enqueue is asynchronous, handing to the radio driver a pointer to the lexicallyscoped packet. The driver makes the transmission in the background, holding the packet until it signals the application with the SENDACK to acknowledge completion. At any time, the client may request a retransmission (line 7), which terminates the par/or (line 6), aborts the ongoing transmission (line 14, if not idle), and restarts the loop (line 5). The client may also request to stop the whole protocol at any time (line 3). Therefore, if the sending trail is aborted by the STOP or RETRANSMIT requests, the packet buffer goes out of scope, leaving behind a dangling pointer in the radio driver, which will possibly transmit corrupted data.

The unsafe example in the left of Figure 7 does not compile because CÉU tracks the interaction of par/or compositions with local variables and stateful C functions (e.g., device drivers) in order to preserve safe abortion of trails [Sant'Anna et al. 2013; 2015]. In fact, CÉU enforces the programmer to write a *finalization* clause to accompany the stateful C call. The code in the right of Figure 7 properly cancels the packet transmission when the block of buffer goes out of scope, i.e., the finalization clause (after the with) executes automatically on external abortion.<sup>2</sup>

#### 2.4. First-Class Timers

Activities that involve reactions to *wall-clock time*<sup>3</sup> appear in typical patterns of embedded development, such as timeout watchdogs and sensor samplings. However, support for wall-clock time is somewhat low-level in existing languages, usually through timer callbacks or "sleep" blocking calls. Furthermore, in any concrete timer implementation, a requested timeout does not expire precisely without delays, a fact that is usually ignored in the development process. We define the difference between the requested timeout and the actual expiring time as the *residual delta time* (*delta*). Without explicit manipulation, the recurrent use of timed activities in sequence (or in a loop) may accumulate a considerable amount of deltas that can lead to incorrect behavior in programs.

The await statement of CÉU supports wall-clock time and handles deltas automatically [Sant'Anna et al. 2013], resulting in more robust applications. For the example in the left of Figure 8, suppose that after the first await request, the underlying system gets busy and takes 15ms to check for expiring awaits. The CÉU scheduler will notice that the await 10ms has not only already expired, but is delayed with delta=5ms. Then, the awaiting trail awakes, sets v=1, and invokes await 1ms. As the current delta is higher than the requested timeout (i.e. 5ms > 1ms), the trail is rescheduled for execution, now with delta=4ms.

CÉU also takes into account the fact that time is a physical quantity that can be added and compared. For instance, for the program in the right of Figure 8, although the scheduler cannot guarantee that the first trail terminates exactly in 11ms, it can

 $<sup>^1</sup>$ The underline prefix marks (e.g., <code>\_send\_enqueue</code>) make interactions with external C libraries explicit and are required in CÉU.

<sup>&</sup>lt;sup>2</sup>Note that the compiler only enforces the programmer to write the finalization clause, but cannot check if it actually handles the resource properly.

<sup>&</sup>lt;sup>3</sup>By wall-clock time we mean the passage of time from the real world, measured in hours, minutes, etc.

Fig. 8. First-class timers in CÉU.

```
// primary expressions
p ::= mem(id)
                                    (any memory access to 'id')
                                    (await event 'id')
(emit event 'id')
      await(id)
      emit (id)
                                    (loop escape)
     break
                                     // compound expressions
      if mem(id) then p else p
                                   (conditional)
                                    (sequence)
      p ; p
      loop p
                                    (repetition)
      p and p
                                    (par/and)
      p or p
                                    (par/or)
     fin p
                                    (finalization)
                                    \ensuremath{//} derived by semantic rules
      awaiting(id,n)
                                    (awaiting 'id' since sequence number 'n')
                                    (emitting on stack level 'n')
      emitting(n)
     p @ loop p
                                    (unwinded loop)
```

Fig. 9. Reduced syntax of CÉU.

at least ensure that the program always terminates with v=1: Given that any non-awaiting sequence is considered to take no time in the synchronous model, the first trail is guaranteed to terminate before the second trail, because 10+1<12. A similar program in a language without first-class support for timers would depend on the execution timings for the code marked as <...>, making the reasoning about the execution behavior more difficult.

# 3. FORMAL SEMANTICS

In this section, we introduce a reduced syntax of  $C\acute{E}U$  and propose an operational semantics to formally describe the language. We describe a small synchronous kernel with broadcast communication highlighting the main differences to Esterel, in particular the stack-based execution for internal events. For the sake of simplicity, we focus on the control aspects of the language, leaving out side effects and C calls (which behave like in conventional imperative languages).

# 3.1. Abstract Syntax

Figure 9 shows the BNF-like syntax for a subset of CÉU that is sufficient to describe all semantic peculiarities of the language. Except for fin and all semantic-derived expressions (i.e., awating, emitting, and p @ loop; p), which are discussed further, all other expressions map to their counterparts in the concrete language.

The mem(id) primitive represents all accesses, assignments, and C function calls that affect a memory location identified by id. As the challenging parts of CÉU reside on its control structures, we are not concerned here with a precise semantics for side effects, but only with their occurrences in programs. The special notation nop is used to represent an innocuous mem expression (it can be thought as a synonym for  $mem(\epsilon)$ ,

where  $\epsilon$  is an unused identifier). Note that mem and await/emit expressions do not share identifiers, i.e., an identifier is either a variable or an event.

### 3.2. Operational Semantics

The core of our semantics is a relation that, given a sequence number n identifying the current reaction chain, maps a program p and a stack of events S in a single step to a modified program and stack:

$$\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle$$
 (relation-inner)

where

$$S, S' \in id^*$$
 (sequence of event identifiers:  $[id_{top}, ..., id_{bottom}]$ )  
 $p, p' \in P$  (program as described in Figure 9)  
 $n \in \mathbb{N}$  (unique identifier for the reaction chain)

At the beginning of a reaction chain, the stack is initialized with the occurring external event ext (S = [ext]), but emit expressions can push new events on top of it (we discuss how they are popped further). The ever-increasing sequence number n prevents that awaiting expressions awake in the same reaction they are reached (the *delayed awaits* as explained in Section 2.1).

We describe this relation with a set of small-step structural semantics rules, which are built in such a way that at most one transition is possible at any time, resulting in deterministic reaction chains. The transition rules for the primary expressions are as follows:

$$\langle S, \ await(id) \rangle \xrightarrow{n} \langle S, \ awaiting(id, n+1) \rangle \qquad \textbf{(await)}$$
 
$$\langle id: S, \ awaiting(id, m) \rangle \xrightarrow{n} \langle id: S, \ nop \rangle, \ m <= n \qquad \textbf{(awake)}$$
 
$$\langle S, \ emit(id) \rangle \xrightarrow{n} \langle id: S, \ emitting(|S|) \rangle \qquad \textbf{(emit)}$$
 
$$\langle S, \ emitting(|S|) \rangle \xrightarrow{n} \langle S, \ nop \rangle \qquad \textbf{(pop)}$$

An await is simply transformed into an awaiting (rule  $\mathbf{await}$ ) as an artifact to remember the external sequence number n+1 it can awake: an awaiting can only transit to a nop (rule  $\mathbf{awake}$ ) if its referred event id matches the top of the stack and it was reached in a previous reaction (i.e., sequence number m < n). An emit transits to an emitting holding the current stack level (|S| stands for the stack length), and pushing the referred event on the stack (rule  $\mathbf{emit}$ ). With the new stack level |S|+1, the emitting(|S|) itself cannot transit, as rule  $\mathbf{pop}$  expects its parameter to match the current stack level. This trick provides the desired stack-based semantics for internal events.

Proceeding to compound expressions, the rules for conditionals and sequences are straightforward:

$$\frac{val(id, n) \neq 0}{\langle S, (if \ mem(id) \ then \ p \ else \ q) \rangle \xrightarrow[n]{} \langle S, p \rangle}$$
 (if-true)

$$\frac{val(id,n) = 0}{\langle S, (if \ mem(id) \ then \ p \ else \ q) \rangle \xrightarrow[n]{} \langle S, q \rangle} \ \ \textbf{(if-false)}$$

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, (p; q) \rangle \xrightarrow{n} \langle S', (p'; q) \rangle}$$
 (seq-adv)

$$\langle S, (mem(id); q) \rangle \xrightarrow{n} \langle S, q \rangle$$
 (seq-nop)

$$\langle S, (break \; ; \; q) \rangle \xrightarrow{n} \langle S, break \rangle$$
 (seq-brk)

Given that our semantics focuses on control, rules **if-true** and **if-false** are the only to query mem expressions. The "magical" function val receives a memory identifier and the current reaction sequence number, returning the current memory value. Although the value here is arbitrary, it is unique in a reaction chain, because a given expression can execute only once within it (remember that loops must contain awaits which, from rule **await**, cannot awake in the same reaction they are reached).

The rules for loops are analogous to sequences, but use '@' as separators to properly bind breaks to their enclosing loops:

$$\langle S, (loop \ p) \rangle \xrightarrow{n} \langle S, (p \ @ \ loop \ p) \rangle$$
 (loop-expd)

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, (p @ loop q) \rangle \xrightarrow{n} \langle S', (p' @ loop q) \rangle}$$
 (loop-adv)

$$\langle S, (mem(id) @ loop p) \rangle \xrightarrow{n} \langle S, loop p \rangle$$
 (loop-nop)

$$\langle S, (break @ loop p) \rangle \xrightarrow{n} \langle S, nop \rangle$$
 (loop-brk)

When a program encounters a loop, it first expands its body in sequence with itself (rule **loop-expd**). Rules **loop-adv** and **loop-nop** are similar to rules **seq-adv** and **seq-nop**, advancing the loop until they reach a mem(id). However, what follows the loop is the loop itself (rule **loop-nop**). Note that if we used ';' as a separator in loops, rules **loop-brk** and **seq-brk** would conflict. Rule **loop-brk** escapes the enclosing loop, transforming everything into a nop.

```
isBlocked(n,a:S,awaiting(b,m)) = (a \neq b \lor m = n) \\ isBlocked(n,S,emitting(s)) = (|S| \neq s) \\ isBlocked(n,S,(p;q)) = isBlocked(n,S,p) \\ isBlocked(n,S,(p@loop q)) = isBlocked(n,S,p) \\ isBlocked(n,S,(p and q)) = isBlocked(n,S,p) \land isBlocked(n,S,q) \\ isBlocked(n,S,(p or q)) = isBlocked(n,S,p) \land isBlocked(n,S,q) \\ isBlocked(n,S,\_) = false \ (mem,await,emit,break,if,loop)
```

Fig. 10. The recursive predicate isBlocked is true only if all branches in parallel are hanged in awaiting or emitting expressions that cannot transit.

Proceeding to parallel compositions, the semantic rules for and and or always force transitions on their left branches p to occur before their right branches q:

$$\frac{\langle S,p\rangle \xrightarrow{n} \langle S',p'\rangle}{\langle S,(p\ and\ q)\rangle \xrightarrow{n} \langle S',(p'\ and\ q)\rangle} \quad \textbf{(and-adv1)}$$

$$\frac{isBlocked(n,S,p)\ ,\ \langle S,q\rangle \xrightarrow{n} \langle S',q'\rangle}{\langle S,(p\ and\ q)\rangle \xrightarrow{n} \langle S',(p\ and\ q')\rangle} \quad \textbf{(and-adv2)}$$

$$\frac{\langle S,p\rangle \xrightarrow{n} \langle S',p'\rangle}{\langle S,(p\ or\ q)\rangle \xrightarrow{n} \langle S',(p'\ or\ q)\rangle} \quad \textbf{(or-adv1)}$$

$$\frac{isBlocked(n,S,p)\ ,\ \langle S,q\rangle \xrightarrow{n} \langle S',q'\rangle}{\langle S(p\ or\ q)\rangle \xrightarrow{n} \langle S',(p\ or\ q')\rangle} \quad \textbf{(or-adv2)}$$

The deterministic behavior of the semantics relies on the isBlocked predicate, which is defined in Figure 10 and used in rules **and-adv2** and **or-adv2**. These rules require the left branch p to be blocked in order to allow the right transition from q to q'. Basically, the isBlocked predicate determines that an expression becomes blocked when all of its trails in parallel hang in awaiting and emitting expressions that cannot advance.

For a parallel *and*, if one of the sides terminates, the composition is simply substituted by the other side (rules **and-nop1** and **and-nop2**). The last two rules **and-brk1** and **and-brk2** deal with a *break* in each of the sides in parallel. To escape the innermost loop, a *break* aborts the other side by applying the *clear* function (to be described

further):

$$\langle S, (mem(id) \ and \ q) \rangle \xrightarrow{n} \langle S, q \rangle$$
 (and-nop1)  
 $\langle S, (p \ and \ mem(id)) \rangle \xrightarrow{n} \langle S, p \rangle$  (and-nop2)  
 $\langle S, (break \ and \ q) \rangle \xrightarrow{n} \langle S, (clear(q) \ ; \ break) \rangle$  (and-brk1)

$$\frac{isBlocked(n,S,p)}{\langle S,(p\ and\ break)\rangle\ \stackrel{}{\underset{n}{\longrightarrow}}\ \langle S,(clear(p)\ ;\ break)\rangle}\ \ \textbf{(and-brk2)}$$

For a parallel *or*, if one of the sides terminates, the whole composition terminates, also applying the *clear* function to the aborted side (rules **or-nop1** and **or-nop2**). Finally, a *break* (rules **or-brk1** and **or-brk2**) behaves in the same way as in a parallel *and*:

$$\langle S, (mem(id) \ or \ q) \rangle \xrightarrow{n} \langle S, clear(q) \rangle$$
 (or-nop1)

$$\frac{isBlocked(n,S,p)}{\langle S, (p \ or \ mem(id)) \rangle \ \underset{n}{\longrightarrow} \ \langle S, clear(p) \rangle} \qquad \textbf{(or-nop2)}$$

$$\langle S, (break\ or\ q) \xrightarrow{n} \langle S, (clear(q)\ ;\ break) \rangle$$
 (or-brk1)

$$\frac{isBlocked(n,S,p)}{\langle S, (p \ or \ break) \rangle \xrightarrow[n]{} \langle S, (clear(p) \ ; \ break) \rangle} \ \ \textbf{(or-brk2)}$$

The clear function, defined in Figure 11, concatenates all active fin bodies of the side being aborted, so that they execute before the composition rejoins. Note that there are no transition rules for fin expressions. This is because once reached, a fin expression halts and will only execute when it is aborted by a trail in parallel and is expanded by the clear function. In Section 3.3.3, we show how to map a finalization block in the concrete language to a fin in the formal semantics. Note that there is a syntactic restriction that a fin body can only contain mem expressions, i.e., they are guaranteed to execute entirely within a reaction chain.

A reaction chain eventually blocks in *awaiting* and *emitting* expressions in parallel trails. If all trails hangs only in *awaiting* expressions, it means that the program cannot advance in the current reaction chain. However, *emitting* expressions are pending in lower stack indexes and should eventually resume in the ongoing reaction (see rule **pop**). Therefore, we define another relation that behaves as **relation-inner** (presented above) if the program can advance, and, otherwise, pops the stack:

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle} \qquad \frac{isBlocked(n, s : S, p)}{\langle s : S, p \rangle \xrightarrow{n} \langle S, p \rangle}$$
 (relation-outer)

$$\begin{aligned} clear(fin\ p) &= p \\ clear(p\ ;\ q) &= clear(p) \\ clear(p\ @\ loop\ q)) &= clear(p) \\ clear(p\ and\ q) &= clear(p)\ ;\ clear(q) \\ clear(p\ or\ q) &= clear(p)\ ;\ clear(q) \\ clear(\_) &= nop \end{aligned}$$

Fig. 11. The function clear extracts fin expressions in parallel and put their bodies in sequence.

To describe a *reaction chain* in CÉU, i.e., how a program behaves in reaction to a single external event, we use the reflexive transitive closure of **relation-outer**:

$$\langle S, p \rangle \xrightarrow{\stackrel{*}{\longrightarrow}} \langle S', p' \rangle$$

Finally, to describe the complete execution of a program, we trigger multiple "invocations" of reaction chains in sequence:

$$\langle [e1], p \rangle \xrightarrow{\cong} \langle [], p' \rangle$$

$$\langle [e2], p' \rangle \xrightarrow{\cong} \langle [], p'' \rangle$$

$$\langle [e3], p'' \rangle \xrightarrow{\cong} \langle [], p''' \rangle$$

Each invocation starts with an external event at the top of the stack and finishes with a modified program and an empty stack. After each invocation, we increment the sequence number.

#### 3.3. Concrete Language Mapping

Most statements from CÉU ("concrete CÉU") map directly to those presented in the reduced syntax of Figure 9 ("abstract CÉU"). For instance, the if in the concrete language behaves exactly like the if in the formal semantics. However, there are some significant mismatches between the concrete and abstract CÉU, and we discuss appropriate mappings in this section. Again, we are not considering side-effects, which are all mapped to the mem semantic construct.

3.3.1. await and emit. The concrete await and emit primitives support communication of values between them. In the two-step translation of Figure 12, we start with the concrete program in CÉU, which communicates the value 1 between the emit and await in parallel (left-most code). In the intermediate translation, we include the shared variable  $e_{-}$  to hold the value being communicated between the two trails in order to simplify the emit. Finally, we convert the program into the equivalent in the abstract syntax, translating side-effect statements into mem expressions. External events require a similar translation, i.e., each external event has a corresponding variable that is explicitly set by the environment before each reaction chain.

3.3.2. First-class Timers. To encompass first-class timers, we introduce a special external TICK event that is intercalated with each other event occurrence in an application (e.g. e1, e2):

```
par/or do
                                par/or do
                                                                 <...> ; mem ; emit(e)
                                                                          or
 <...>
                                  <...>
                                  e_ = 1;
 emit e => 1;
                                                                  await(e); mem; mem
                                  emit e;
 v = await e;
                                 with
  _printf("%d\n",v);
                                  await e;
                                  v = e_{-};
                                   -printf("%d\n",v);
```

Fig. 12. Two-step translation from concrete to abstract emit and await expressions.

```
dt = await 10ms;
                               var int tot = 10000; // 10ms
                                loop do
                                                                       loop(
                                                                           await (TICK);
                                    await TICK;
                                    tot = tot - TICK_{:}
                                                                           mem;
                                    if tot <= 0 then</pre>
                                                                           if mem then
                                        dt = tot;
                                                                                mem;
                                        break;
                                                                                break
                                                                           else
                                                                                nop
```

Fig. 13. Two-step translation from concrete to abstract timer.

$$\langle [TICK], p \rangle \xrightarrow{*} \langle [], p' \rangle$$

$$\langle [e1], p' \rangle \xrightarrow{*} \langle [], p'' \rangle$$

$$\langle [TICK], p'' \rangle \xrightarrow{*} \langle [], p''' \rangle$$

$$\langle [e2], p''' \rangle \xrightarrow{*} \langle [], p'''' \rangle$$

The TICK event has an associated variable TICK\_ carrying the wall-clock time elapsed between the two occurrences, as depicted by the two-step translation of Figure 13. In the concrete program in the left, the variable dt will hold the residual delta time (as described in Section 2.4) after awaking from the timer. In the first step of the translation, we expand the await 10ms to a loop that decrements the elapsed number of microseconds for each occurrence of TICK. When the variable tot reaches zero, we escape the loop setting the variable dt to contain the appropriate delta. In the last step, we convert the program to the abstract syntax.

3.3.3. Finalization Blocks. The biggest mismatch between concrete and abstract Céu is regarding finalization blocks, which require more complex modifications in the program for a proper mapping using the fin expressions. In the three-step translation of Figure 14, we start with a concrete program (CODE-1) that uses a finalize to safely release the reference to ptr kept after the call to hold. In the translation, we first need to catch the do-end termination to run the finalization code. For this, we translate the block into a par/or (CODE-2) with the original body in parallel with a fin to run the finalization code. This way, the fin body executes whenever the par/or terminates, either normally (after the await B) or aborted from an outer composition. However, the fin still (incorrectly) executes even if the call to hold is not reached in the body due to an abort before awaking from the await A. To deal with this issue, for each fin we need a corresponding flag to keep track of code that needs to be finalized (CODE-3). The

```
par/or do
do
                                                                                  mem;
 var int* ptr = <...>;
                             var int* ptr = <...>;
                                                      par/or do
  await A;
                             await A;
                                                        var int* ptr = <...>;
                                                                                     mem:
  finalize
                             _hold(ptr);
                                                        await A;
                                                                                     await(A);
    _hold(ptr);
                             await B:
                                                        _hold(ptr);
                                                                                     mem:
  with
                           with
                                                                                     mem;
                             { fin
                                                        await B;
                                                                                     await(B);
    _release(ptr);
 end
                                 _release(ptr); }
                                                      with
                                                                                  or
 await B;
                                                                                     fin
                           end
                                                         { fin
end
                                                             if f then
                                                                                       if mem then
                                                               _release(ptr);
                                                                                         mem
                                                             end }
                                                                                       else
                                                      end
                                                                                         nop
// CODE-1
                           // CODE-2
                                                       // CODE-3
                                                                                     CODE-4
```

Fig. 14. Three-step translation from concrete to abstract finalization.

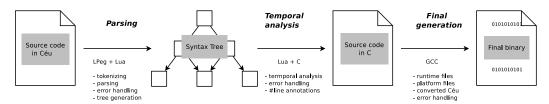


Fig. 15. Compilation process: from the source code in CÉU to the final binary.

flag is initially set to false, avoiding the finalization code to execute. Only after the call to hold that we set the flag to true and enable the fin body to execute. The complete translation substitutes the side-effect operations with mem expressions (CODE-4).

#### 4. IMPLEMENTATION

The compilation process of a program in Céu is composed of three main phases, as illustrated in Figure 15:

Parsing. The parser of CÉU is written in LPeg [Ierusalimschy 2009], a pattern matching library that also recognize grammars, making it possible to write the to-kenizer and grammar with the same tool. The source code is then converted to an abstract syntax tree (AST) to be used in further phases. This phase may be aborted due to syntax errors in the CÉU source file.

Temporal Analysis. This phase detects inconsistencies in Céu programs, such as unbounded loops, suspicious accesses to shared memory, and also "classical" semantic analysis, such as building a symbol table for checking variable declarations. This phase outputs code in C, given the way Céu is tied to C by design. Some type checking is delayed to the last phase to take advantage of gcc's error handling. Therefore, we annotate the C output file with #line pragmas matching the original file in Céu.

Code Generation. The final phase packs the generated C file with the CÉU runtime and platform-dependent functionality, compiling them with gcc and generating the final binary. The CÉU runtime comprehends the scheduler, timer management, and the external C API. The platform files include libraries for I/O and bindings to invoke the CÉU scheduler on external events.

```
Stmts I=\{.\} O=\{A\}
     input void A, B;
                                                                          Dcl_y I={.} O={.}
ParOr I={.} O={A,B}
     var int y;
    par/or do
       await A;
                                                                4
                                                                                Stmts I=\{.\} O=\{A\}
                                                                                    Await_A I=\{.\} O=\{A\}
        v = 1:
                                                                5
                                                                                Set_y I=\{A\} O=\{A\}
Stmts I=\{.\} O=\{B\}
     with
                                                                6
       await B;
                                                                7
                                                                                     Await_B I=\{.\} O={B}
Set_y I=\{B\} O={B}
                                                                8
       y = 2;
     end
                                                                9
                                                                          Await_A I=\{A,B\} O=\{A\}
10
     await A:
                                                               10
                                                                          Set_y I=\{A\} O=\{A\}
    y = 3;
11
                                                               11
```

Fig. 16. A program with a corresponding AST describing the sets I and O. The program is safe because accesses to y in parallel have no intersections for I.

# 4.1. Temporal Analysis for Shared-Memory Concurrency

The compile-time *temporal analysis* phase detects inconsistencies in CÉU programs. Here, we focus on the algorithm that detects suspicious access to shared variables, as discussed in Section 2.2.

For each node representing a statement in the program AST, we keep the set of events I (for incoming) that can lead to the execution of the node, and also the set of events O (for outgoing) that can terminate the node.

A node inherits the set *I* from its direct parent and calculates *O* according to its type:

- Nodes that represent expressions, assignments, C calls, and declarations simply reproduce O = I, as they do not await;
- —An await e statement has  $O = \{e\}$ .
- A break statement has  $O = \{\}$  as it escapes the innermost loop and never terminates, i.e., never proceeds to the statement immediately following it (see also loop below);
- A sequence node (;) modifies each of its children to have  $I_n = O_{n-1}$ . The first child inherits I from its parent node, and the set O for the sequence node is copied from its last child, i.e.,  $O = O_n$ .
- —A loop node includes its body's O on its own I ( $I = I \cup O_{body}$ ), as the loop is also reached from its own body. The union of all break statements' O forms the set O for a loop.
- —An if node has  $O = O_{true} \cup O_{false}$ .
- A parallel composition may terminate from any of its branches, hence  $O = O_1 \cup ... \cup O_n$ .

With all sets calculated, we take all pairs of nodes that perform side effects and are in parallel branches, comparing their sets I for intersections. For each pair, if the intersection is not the empty set, we mark both nodes as suspicious.

The example in the left of Figure 16 has a corresponding AST, in the right of the figure, with the sets I and O for each node. The event . (dot) represents the "boot" reaction. The assignments to y in parallel (lines 5,8 in the code) have an empty intersection of I (lines 6,9 in the AST), hence, they do not conflict. Note that although the accesses in lines 5,11 in the code (lines 6,11 in the AST) do have an intersection, they are not in parallel and are also safe.

# 4.2. Memory Layout

CÉU favors a fine-grained use of trails, being common the use of trails that await a single event. For this reason, CÉU does not allocate per-trail stacks; instead, all data resides in fixed memory slots—this is true for the program variables as well as for temporary values and runtime flags. Memory for trails in parallel must coexist, while statements in sequence can reuse it. Translating this idea to *C* is straightforward [Kas-

```
input int A, B, C;
                                                 union {
                                                                         sequence
                                                     int a_1;
                                                                           do_1
    var int a = await A;
                                                     int b_2;
                                                                           do 2
                                                     struct {
end
                                                                           par/and
do
                                                        int _and_3: 1;
    var int b = await B;
                                                         int _and_4: 1;
end
par/and do
                                                } MEM ;
    await B;
with
    await C;
end
```

Fig. 17. A program with blocks in sequence and in parallel, with corresponding memory layout.

ten and Römer 2005; Bernauer and Römer 2013]: memory for blocks in sequence are packed in a struct, while blocks in parallel, in a union. CÉU reserves a single static block of memory to hold all memory slots, whose size is the maximum the program uses at a given time. A given position in the memory may hold different data (with variable sizes) during runtime. As an example, Figure 17 shows a program with corresponding memory layout. Each variable is assigned a unique id (e.g. a\_1) so that variables with the same name can be distinguished. The do-end blocks in sequence are packed in a union, given that their variables cannot be in scope at the same time, e.g., MEM.a\_1 and MEM.b\_2 can safely share the same memory slot. The example also illustrates the presence of runtime flags related to the parallel composition, which also reside in reusable slots in the static memory.

### 4.3. Trail Allocation

Each line of execution in Céu needs to carry associated data, such as which event it is awaiting and which code to execute when it awakes. The compiler statically infers the maximum number of trails a program can have at the same time and creates a static vector to hold the runtime information about them. Like normal variables, trails that cannot be active at the same time can share slots in the static memory vector.

At any given moment, a trail can be awaiting in one of the following states: INACTIVE, STACKED, FIN, or in any of the events defined in the program:

All terminated or not-yet-started trails stay in the INACTIVE state and are ignored by the scheduler. A STACKED trail holds an associated stack level and is delayed until the scheduler runtime reaches that level again. A FIN trail represents a hanged finalization block which is only scheduled when its corresponding block goes out of scope. A trail waiting for an event stays in the state of the corresponding event, also holding the minimum sequence number (seqno) in which it can awake. In concrete terms, a trail is represented by the following struct:

```
struct trail.t {
    state_t evt;
    label.t lbl;
    union {
        unsigned char seqno;
        stack_t stk;
    };
};
```

```
input void A;
                                                                    void dispatch (trail_t* t) {
                                    enum {
event void e;
                                2
                                      Main = 1,
                                                     ln 3
                                                                2
                                                                      switch (t->lbl) {
                                                     ln 7
// TRAIL 0 - lbl Main
                                3
                                      Awake_e,
                                                                3
                                                                        case Main:
par/and do
                                      And_chk,
                                                     ln 8,15
                                                                             activate TRAIL 1
                                                                4
  // TRAIL 0 — lbl Main
                                      And_sub_2, //
                                                     ln 10
                                                                          TRLS[1].evt = STACKED;
                                5
                                                                5
                                                                          TRLS[1].lbl = And_sub_2;
 await e;
                                      Awake_A_1, //
                                                     ln 12
                                6
                                                                6
                                                     ln 14
                                                                          TRLS[1].stk = cur_stack;
  // TRAIL 0 - 1bl Awake_e
                                      Emit_cont, //
                                7
                                                                7
    TRAIL 0 - lbl And-chk
                                      And_out.
                                                  // ln 17
                                8
                                                                8
                                      Awake_A_2
                                                 // ln 19
                                                                          // code in the 1st trail
with
                                9
                                                                9
     TRAIL 1 - 1bl And_sub_2
                                10
                                                                10
                                                                           // await e;
                                                                          TRLS[0].evt = EVT_e;
 await A;
                                11
                                                                11
  // TRATI 1 - 1bl Awake_A_1
                                    trail_t TRLS[2] = {
                                                                          TRLS[0].1b1 = Awake_e;
                                12
                                                                12
                                        STACKED, Main, 0 };
                                                                          TRLS[0].seq = cur_seqno;
 emit e;
                                13
                                                                13
  // TRAIL 1 - 1bl Emit_cont
                                        INACTIVE, 0,
                                                         0 };
                                                                          break:
                                14
                                                                14
     TRAIL 1 - 1bl And_chk
                                    };
                                15
                                                                15
end
                                16
                                                                16
                                                                        case And_sub_2:
// TRAIL 0 - lbl And_out
                                17
                                                                17
                                                                           // await A;
                                                                           TRLS[1].evt = EVT_A;
await A;
                                18
                                                                18
// TRAIL 0 - 1bl Awake_A_2
                                                                          TRLS[1].lbl = Awake_A_1;
                                19
                                                                19
                                20
                                                                20
                                                                          TRLS[1].seq = cur_seqno;
                                21
                                                                21
                                                                          break:
                                22
                                                                22
                                                                        <...> // other labels
                                23
                                                                23
                                24
                                                                24
                                25
                                                                25
```

Fig. 18. Static allocation of trails, entry-point labels, and dispatch loop.

The field evt holds the state of the trail (or the event it is awaiting); the field 1b1 holds the entry point in the code to execute when the trail is scheduled; the third field depends on the evt field and may hold the seqno for an event, or the stack level stk for a STACKED state.

The size of state\_t depends on the number of events in the application; for an application with less than 253 events (plus the 3 states), one byte is enough. The size of label\_t depends primarily on the number of await statements in the application—each await splits the code in two segments and requires a unique entry point in the code for its continuation. Additionally, split & join points for parallel compositions, emit continuations, and finalization blocks also require labels. The seqno could eventually overflow during execution (i.e., every 256 reactions). However, given that the scheduler traverses all trails on every reaction, it can adjust them to properly handle overflows (actually, 2 bits to hold the seqno is already enough). The size of stack\_t depends on the maximum depth of nested emissions and is bounded to the maximum number of trails. In the worst case, a trail emits an event that awakes another trail, which emits an event that awakes another trail, and so on. The last trail cannot awake any trail, because they are all hanged in the STACKED state.

In the context of embedded systems, the size of trail\_t is typically only 3 bytes (1 byte for each field), imposing a negligible memory overhead even for trails that only await a single event and terminate. For instance, the CTP collection protocol ported to  $C \not\equiv U$  reaches eight simultaneous lines of execution with an overhead of 2% in comparison to the original version in nesC [Gay et al. 2003] (a dialect of C for event-driven programming) [Sant'Anna et al. 2013].

# 4.4. Code Generation and Scheduling

In the final generated code in C, each trail label representing an entry point becomes a *switch case* with the associated code to execute. Figure 18 illustrates the generation process. For the program in the left of the figure, the compiler extracts the entry points and associated trails, e.g., the label Awake\_e will execute on TRAIL-0 (line 7). For each statement that pauses (emit and await), resumes (par/and, par/or, and finalize), or

aborts (par/or and break), the compiler splits the trail into segments with associated entry points. The entry points translate to an enum in the generated code (lines 1-10, in the middle of the figure). The state of trails translate to a vector of type trail\_t with the maximum number of simultaneous trails (lines 12-15). On initialization, TRAIL-0 is set to execute the Main entry point (line 13), while all others are set to INACTIVE (in the example, only one, in line 14).

The scheduler executes in two passes: In the *broadcast* pass, the scheduler sets all trails that are waiting for the current event to STACKED in the current stack level. In the *dispatch* pass, the scheduler executes each trail that is STACKED to run in the current level, setting it immediately to INACTIVE.

During the dispatch pass, if a trail executes and emits an internal event, the scheduler increments the stack level and re-executes the two passes. After all trails are properly dispatched, the scheduler decrements the stack level and resumes the previous execution. For the first reaction chain, the scheduler starts from the *dispatch* pass, given that the Main label is the only one that can be active at the stack level 0 (line 13, in the middle of Figure 18).

The code the right of the Figure 18 dispatches a trail according to the current label to execute. For the first reaction, it executes the Main label in TRAIL-0. When the Main label reaches the par/and, it stacks TRAIL-1 (lines 4–7) and proceeds to the code in TRAIL-0 (lines 9–14), respecting the deterministic execution order. The code sets the running TRAIL-0 to await EVT\_e on label Awake\_e, and then halts with a break. The next iteration of dispatch takes TRAIL-1 and executes its registered label And\_sub\_2 (lines 16–21), which sets TRAIL-1 to await EVT\_A and also halts.

Regarding abortion and finalization, when a par/or terminates, the scheduler makes a *broadcast* pass for the FIN event, but limited to the range of trails covered by the terminating par/or. Trails that do not match the FIN are set to INACTIVE, as they have to be aborted. Given that trails in parallel are allocated in subsequent slots in the static vector TRLS, this pass only aborts the desirable trails. The subsequent *dispatch* pass executes the finalization code. Escaping a loop that contains parallel compositions also triggers the same abortion process.

# 4.5. The External C API

As a reactive language, the execution of a program in  $C\acute{E}U$  is guided entirely by the occurrence of external events. From the implementation perspective, there are three external sources of input into programs, which are all exposed as functions in a C API:

ceu\_go\_init():. initializes the program (e.g. trails) and executes the "boot" reaction (i.e., the Main label).

ceu\_go\_event(id,param):. executes the reaction for the received event id and associated parameter.

ceu\_go\_wclock(us):. increments the current time in microseconds and runs a reaction if any timer expires.

Given the semantics of CÉU, the functions are guaranteed to take a bounded time to execute. They also return a status code that says if the CÉU program has terminated after the reactions. Further calls to the API have no effect on terminated programs.

The bindings for the specific platforms are responsible for calling the functions in the API in the order that better suit their requirements. As an example, it is possible to set different priorities for events that occur concurrently (i.e. while a reaction chain is running). However, a binding must never interleave or run multiple functions in parallel. This would break the CÉU sequential/discrete semantics of time.

As an example, Figure 19 shows our binding for *TinyOS* [Hill et al. 2000], which maps callbacks to input events in CÉU. The file ceu.h (included in line 3) contains all

```
implementation
2
3
        #include "ceu.h"
        #include "ceu.c"
        event void Boot.booted () {
6
            ceu_sys_init();
    #ifdef CEU_WCLOCKS
            call Timer.startPeriodic(10);
9
    #endif
10
11
        }
12
    #ifdef CEU_WCLOCKS
13
        event void Timer.fired () {
14
            int dt = 10000;
15
            ceu_sys_go(EVT__WCLOCK, &dt);
16
17
    #endif
18
19
    #ifdef _EVT_PHOTO_READDONE
20
        event void Photo.readDone (uint16_t val) {
21
            ceu_sys_go(EVT_PHOTO_READDONE, &val);
22
23
24
    #endif
25
    #ifdef EVT RADIO SENDDONE
26
        event void RadioSend.sendDone (message_t* msg) {
27
28
            ceu_sys_go(EVT_RADIO_SENDDONE, &msg);
29
30
    #endif
31
    #ifdef _EVT_RADIO_RECEIVE
32
33
        event message_t* RadioReceive.receive (message_t* msg) {
34
            ceu_sys_go(EVT_RADIO_RECEIVE, &msg);
35
            return msg;
36
37
    #endif
38
39
        <...> // other events
```

Fig. 19. The TinyOS binding for Céu.

definitions for the compiled CÉU program, which are further queried through #ifdef's. The file ceu.c (included in line 4) contains the main loop of CÉU pointing to the labels defined in the program. The callback Boot.booted (lines 6–11) is called by TinyOS on startup, so we initialize CÉU inside it (line 7). If the CÉU program uses timers, we also start a periodic timer (lines 8–10) that triggers callback Timer.fired (lines 13–18) every 10 milliseconds and advances the wall-clock time of CÉU (line 16)<sup>4</sup>. The remaining lines map pre-defined TinyOS events that can be used in CÉU programs, such as the light sensor (lines 20–24) and the radio transceiver (lines 26–37).

## 4.6. The Terra Virtual Machine

# 5. RELATED WORK

(As subsecoes serao retiradas do texto ao final...)

## 5.1. Semantics

CÉU has a strong influence from Esterel [Boussinot and de Simone 1991] and embraces the disciplined synchronous-reactive model with support for lexical composi-

 $<sup>\</sup>overline{^{4}\text{We also}}$  offer a mechanism to start the underlying timer on demand to avoid the "battery unfriendly" 10ms polling.

tion of lines of execution. However, there are fundamental semantic differences that prevents the design of CÉU as pure extensions to Esterel (i.e., on top of its formal semantics). In particular, Esterel has a notion of time similar to that of digital circuits in which multiple signals can be active at a clock tick. In fact, Esterel is also used in hardware design. In CÉU, instead of clock ticks, the occurrence of a single external event that defines a time unit. CÉU also distinguishes external events from stack-based internal events, which provides a limited form of coroutines that supports reactive statements (e.g., await and par/or).

The event-driven approach [Ousterhout 1996] is well known and popular in many software communities, such as web frameworks in the client side (e.g., jQuery [Chaffer 2009] and Rx.js [Liberty and Betts 2011]) and server side (e.g., Node.js [Tilkov and Vinoski 2010] and SEDA [Welsh et al. 2001]), GUI toolkits (e.g., Tcl/Tk [Ousterhout 1991] and Java Swing [Eckstein et al. 1998]), and Games [Nystrom 2014]. Like  $C\acute{E}U$ , event-driven programming is essentially synchronous, i.e., events go through a queue and are dispatched sequentially and atomically to prevent race conditions. We believe that for software design, this approach is more familiar to programmers and simplifies the reasoning about concurrency. For instance, the uniqueness of external events in  $C\acute{E}U$  is a prerequisite for the temporal analysis that enables safe shared-memory concurrency.

A number of synchronous languages have been designed to interoperate with C, such as  $Reactive\ C$  [Boussinot 1991], Protothreads [Dunkels et al. 2006],  $PRET\-C$  [Andalam et al. 2010] and SC [Von Hanxleden 2009]. They offer Esterel-like parallel compositions with communication via shared variables, relying on deterministic scheduling to preserve determinism. However, it is the responsibility of the programmer to specify the execution order for threads, based on either explicit priorities, or source code lexical order (like CEU). These languages have a tick-based notion of time similar to Esterel, which prevents the event-based temporal analysis of CEU.

*URBI* [Baillie 2005] is a reactive scripting language with a rich set of control constructs for time management, event-driven communication, and concurrency. Concurrency is based on stackful coroutines, diverging from our goals regarding resource efficiency and static bounds for memory and execution time.

## 5.2. Implementation

Esterel has different compilation backends that synthesizes to software and also to hardware circuits [Dayaratne et al. 2005; Edwards 2003]. Among the software-based approaches, SAXO-RT [Closse et al. 2002] is the closest to our implementation with respect to trail allocation and scheduling: The compiler slices programs into "control points" (analogous to our "entry points") and rearranges them into a directed acyclic graph respecting the constructive semantics of Esterel. Then, it flattens the graph into sequential code in C suitable for static scheduling.

# 5.3. Virtual Machines

# 6. FINAL REMARKS

# **REFERENCES**

Sidharta Andalam and others. 2010. Predictable multithreading of embedded applications using PRET-C. In *Proceeding of MEMOCODE'10*. IEEE, 159–168.

Jean-Christophe Baillie. 2005. Urbi: Towards a universal robotic low-level programming language. In *International Conference on Intelligent Robots and Systems*. IEEE, 820–825.

Albert Benveniste and others. 2003. The synchronous languages twelve years later. In *Proceedings of the IEEE*, Vol. 91. 64–83.

Alexander Bernauer and Kay Römer. 2013. A Comprehensive Compiler-Assisted Thread Abstraction for Resource-Constrained Systems. In *Proceedings of IPSN'13*. Philadelphia, USA.

- Gérard Berry. 1993. Preemption in Concurrent Systems.. In FSTTCS (LNCS), Vol. 761. Springer, 72-93.
- G. Berry. 2000. The Esterel-V5 Language Primer. CMA and Inria, Sophia-Antipolis, France. Version 5.10, Release 2.0.
- Frédéric Boussinot. 1991. Reactive C: An extension of C to program reactive systems. Software: Practice and Experience 21, 4 (1991), 401–428.
- F. Boussinot and R. de Simone. 1991. The Esterel language. Proc. IEEE 79, 9 (Sep 1991), 1293-1304.
- Adriano Branco, Francisco Sant'anna, Roberto Ierusalimschy, Noemi Rodriguez, and Silvana Rossetto. 2015. Terra: Flexibility and Safety in Wireless Sensor Networks. *ACM Trans. Sen. Netw.* 11, 4, Article 59 (Sept. 2015), 27 pages. DOI: http://dx.doi.org/10.1145/2811267
- Jonathan Chaffer. 2009. Learning JQuery 1.3: Better Interaction and Web Development with Simple JavaScript Techniques. Packt Publishing Ltd.
- Etienne Closse and others. 2002. Saxo–RT: Interpreting esterel semantic on a sequential execution structure. *Electronic Notes in Theoretical Computer Science* 65, 5 (2002), 80–94.
- Sajeewa Dayaratne and others. 2005. Direct Execution of Esterel Using Reactive Microprocessors. In *Proceedings of SLAP'05*.
- Dunkels and others. 2006. Protothreads: simplifying event-driven programming of memory-constrained embedded systems. In *Proceedings of SenSys'06*. ACM, 29–42.
- Robert Eckstein, Marc Loy, and Dave Wood. 1998. Java swing. O'Reilly & Associates, Inc.
- Stephen A Edwards. 1999. Compiling Esterel into sequential code. In 7th International Workshop on Hardware / Software Codesign. ACM, 147–151.
- Stephen A Edwards. 2003. Tutorial: Compiling concurrent languages for sequential processors. ACM Transactions on Design Automation of Electronic Systems 8, 2 (2003), 141–187.
- David Gay and others. 2003. The nesC Language: A Holistic Approach to Networked Embedded Systems. In PLDI'03. 1–11.
- Omprakash Gnawali and others, 2009. Collection tree protocol. In Proceedings of SenSys'09. ACM, 1-14.
- Hill and others. 2000. System architecture directions for networked sensors.  $SIGPLAN\ Notices\ 35$  (November 2000), 93–104. Issue 11.
- Roberto Ierusalimschy. 2009. A text pattern-matching tool based on Parsing Expression Grammars. Softw. Pract. Exper. 39 (March 2009), 221–258. Issue 3.
- Marcin Karpinski and Vinny Cahill. 2007. High-Level Application Development is Realistic for Wireless Sensor Networks. In *Proceedings of SECON'07*. 610–619.
- Oliver Kasten and Kay Römer. 2005. Beyond Event Handlers: Programming Wireless Sensors with Attributed State Machines. In *Proceedings of IPSN '05*. 45–52.
- Jesse Liberty and Paul Betts. 2011. Reactive extensions for javascript. In *Programming Reactive Extensions and LINQ*. Springer, 111–124.
- Ana Lúcia De Moura and Roberto Ierusalimschy. 2009. Revisiting coroutines. ACM TOPLAS 31, 2 (Feb. 2009), 6:1–6:31.
- Robert Nystrom. 2014. Game Programming Patterns. Genever Benning.
- ORACLE. 2011. Java Thread Primitive Deprecation. http://docs.oracle.com/javase/6/docs/technotes/guides/concurrency/threadPrimitiveDeprecation.html (accessed in Aug-2014). (2011).
- John Ousterhout. 1996. Why Threads Are A Bad Idea (for most purposes). (January 1996).
- John K Ousterhout. 1991. An X11 Toolkit Based on the Tcl Language.. In USENIX Winter. 105-116.
- Dumitru Potop-Butucaru and others. 2005. The Synchronous Hypothesis and Synchronous Languages. In *Embedded Systems Handbook*, R. Zurawski (Ed.).
- Francisco Sant'Anna and others. 2013. Safe System-level Concurrency on Resource-Constrained Nodes. In *Proceedings of SenSys'13*. ACM.
- Francisco Sant'Anna and others. 2015. Structured Synchronous Reactive Programming with Céu. In *Proceedings of Modularity'15*. to appear.
- Olivier Tardieu and Robert De Simone. 2003. Instantaneous termination in pure Esterel. In *Static Analysis*. Springer, 91–108.
- Stefan Tilkov and Steve Vinoski. 2010. Node.js: Using JavaScript to build high-performance network programs. *IEEE Internet Computing* 6 (2010), 80–83.
- Reinhard Von Hanxleden. 2009. SyncCharts in C: a proposal for light-weight, deterministic concurrency. In Proceedings of the seventh ACM international conference on Embedded software. ACM, 225–234.
- Matt Welsh, David Culler, and Eric Brewer. 2001. SEDA: an architecture for well-conditioned, scalable internet services. SIGOPS Oper. Syst. Rev. 35, 5 (2001), 230–243.