The Semantics and Implementation of CÉU: a Synchronous Reactive Language based on Esterel

Francisco Sant'Anna, Departamento de Informática, PUC-Rio Adriano Branco, Departamento de Informática, PUC-Rio Roberto Ierusalimschy, Departamento de Informática, PUC-Rio Noemi Rodriguez, Departamento de Informática, PUC-Rio Silvana Rossetto, Departamento de Ciência da Computação, UFRJ

CÉU is a reactive language based on Esterel that targets constrained embedded platforms. Employing the synchronous programming model, it allows for a simple reasoning about shared-memory concurrency. Furthermore, its restricted semantics enables deterministic and memory-safe programs. In this work, we propose a formal semantics for CÉU focusing on its particular control mechanisms, such as parallel compositions, finalization, and stack-based internal events. We also present an implementation with two backends: one aiming for resource efficiency and interoperability with C, and another for code dissemination in sensor networks

Additional Key Words and Phrases: Concurrency, Determinism, Embedded Systems, Esterel, Synchronous, Reactivity

1. INTRODUCTION

- Relevancia de Esterel
- Ceu
 - Foco inicial em "constrained WSNs"
 - Movendo para outros dominios? (jogos, artigo Mod'15)
 - Usos: SenSys, Terra, sala de aula, GSoC
- Limitacoes do modelo sincrono
- Semantica e Implementacao (nesse artigo, somente subset estatico)

2. OVERVIEW OF CÉU

CÉU is a synchronous reactive language based on Esterel [Boussinot and de Simone 1991] with support for multiple concurrent lines of execution known as *trails*. By reactive, we mean that programs are stimulated by the environment through input events that are broadcast to all awaiting trails. By synchronous, we mean that all trails at any given time are either reacting to the current event or are awaiting another event; in other words, trails are never reacting to different events.

Figure 1 shows side-by-side the implementations in Esterel and CÉU for the following control specification [Berry 2000]: "Emit an output O as soon as two inputs A and B have occurred. Reset this behavior each time the input R occurs". The first phrase of the specification, awaiting and emitting the events, is translated almost identically in the two languages (lines 4–9, in both implementations), as Esterel's '||' and CÉU's par/and constructs are equivalent. For the second phrase, the reset behavior, the Esterel version uses a abort-when (lines 3–10), which serves the same purpose of CÉU's par/or (lines 3–12): the occurrence of event R aborts the awaiting statements in parallel and restarts the loop.

CÉU, following the Esterel mindset, has a strong imperative flavor, with explicit control flow through sequences, loops, parallels, and also assignments. Being designed for control-intensive applications, it provides support for concurrent lines of execution and broadcast communication through events. In the synchronous model, programs advance in a sequence of discrete reactions to external events. Internal computations within a reaction (e.g. expressions, assignments, and native calls) are considered to take no time in accordance with the synchronous hypothesis [Potop-Butucaru et al.

```
// ESTEREL
                                                        // CEU
                                                        loop do
    loop
       abort
                                                            par/or do
                                                               par/and do
              await A
                                                     5
                                                                  await A:
                                                               with
                                                     6
6
              await B
                                                                  await B;
                                                               end
                                                     8
           emit 0
                                                               emit 0;
                                                    9
       when R
                                                    10
                                                            with
10
    end
                                                               await R;
11
                                                    11
                                                            end
                                                    12
                                                    13
                                                        end
```

Fig. 1. A control specification implemented in Esterel and CÉU: "Emit O after A and B, resetting each R"

2005]. The await statements are the only ones that halt a running reaction and allow a program to advance in this notion of time. To ensure that reactions run in bounded time and programs always progress, loops are statically required to contain at least one await statement in all possible paths [Sant'Anna et al. 2013; Berry 2000].

In the sections that follow, we review the main differences between CÉU and Esterel [Sant'Anna et al. 2013]: deterministic execution (Section 2.1), safe abortion with finalization (Section 2.2), first-class timers (Section 2.3), and stack-based internal events (Section 2.4).

2.1. Deterministic Execution

Esterel is only deterministic with respect to reactive control: "the same sequence of inputs always produces the same sequence of outputs" [Berry 2000]. However, the execution order for operations with side-effects within a reaction is non-deterministic: "if there is no control dependency, as in "call f1() || call f2()", the order is unspecified and it would be an error to rely on it" [Berry 2000]. In CÉU, when multiple trails are active at a time, they are scheduled in the order they appear in the program source code. Hence, CÉU is deterministic also with respect to the order of execution of side effects within a reaction. Figure 2 compares the two syntactically equivalent code fragments in Esterel and CÉU to illustrate the semantic difference regarding (non-)determinism.

On the one hand, enforcing an execution order for concurrent operations may seen arbitrary and also precludes true parallelism. On the other hand, it provides a priority scheme for trails, and makes shared-memory concurrency more tractable. In contrast, Esterel precludes support for shared memory: "if a variable is written by some thread, then it can neither be read nor be written by concurrent threads" [Berry 2000]. For constrained embedded development, we believe that deterministic shared-memory concurrency is beneficial, given the extensive use of memory mapped ports for I/O and the lack of hardware support for real parallelism. Other embedded languages made a similar design choice [Karpinski and Cahill 2007; Andalam et al. 2010].

Fig. 2. In Esterel, the execution order between f1 and f2 is unspecified, whereas in CÉU, _f1 executes before _f2.

```
input void STOP, RETRANSMIT, SENDACK;
                                                          11
2
    par/or do
                                                          12
                                                                   var _pkt_t buffer;
3
        await STOP;
                                                          13
                                                                   <fill-buffer-info>
                                                          14
                                                                   finalize
5
        loop do
                                                          15
                                                                        _send_engueue(&buffer);
            par/or do
6
                                                          16
                 await RETRANSMIT;
                                                                        _send_cancel(&buffer);
                                                          17
7
             with
8
                                                          18
                                                                   end
                 par/and do
                                                                   await SENDACK;
9
                                                          19
10
                     await 1min;
                 with
11
                      var _pkt_t buffer:
12
                      <fill-buffer-info>
13
                      send enqueue (&buffer):
14
                      await SENDACK:
15
16
                 end
17
             end
        end
18
    end
19
```

Fig. 3. A network protocol in CÉU extended with a finalization clause to cancel a transmission.

2.2. Safe Abortion with Finalization

The introductory example of Figure 1 illustrates how synchronous languages can abort awaiting lines of execution without tweaking them with synchronization primitives. In contrast, traditional (asynchronous) multi-threaded languages cannot express thread termination safely [Berry 1993; ORACLE 2011]. However, handling abortion when dealing with external resources can be challenging, given that external entities are not subject to the same synchronous execution discipline.

The example in the left of Figure 3 relies on hierarchical par/or and par/and compositions to describe the state machine of a data collection protocol for sensor networks [Gnawali et al. 2009; Sant'Anna et al. 2013]. The input events STOP, RETRANSMIT, and SENDACK (line 1) represent the external interface of the protocol with a client application. The protocol has two trails: one monitors the stopping event (line 3); the other periodically transmits a packet (lines 5–18). The periodic transmission is a loop that starts two other trails (lines 6-17): one handles an immediate retransmission request (line 7); the other transmits the packet awaiting for a confirmation (lines 9–16). The actual transmission (lines 12-15) is enclosed with a par/and that takes at least one minute before looping, to avoid flooding the network with packets. At any time, the client may request a retransmission (line 7), which terminates the par/or (line 6), aborts the ongoing transmission (line 14, if not idle), and restarts the loop (line 5). The client may also request to stop the whole protocol at any time (line 3). Note that the buffer packet (line 12) is a local variable whose address is passed to function _send_enqueue for transmission. The call enqueues the pointer in the radio driver, which holds it up to the emission of SENDACK acknowledging the packet transmission. In the meantime, if the sending trail is be aborted by the STOP or RETRANSMIT requests, the packet buffer goes out of scope, leaving behind a dangling pointer in the radio driver.

The CÉU compiler tracks the interaction of par/or compositions with local variables and stateful C functions (e.g., device drivers) in order to preserve safe abortion of trails. For instance, CÉU refuses to compile the program in the left of Figure 3, enforcing the programmer to write a *finalization* clause to accompany the stateful C call [Sant'Anna et al. 2013]. The code in the right of Figure 3 properly cancels the packet transmission

Fig. 4. First-class timers in CÉU.

when the block of buffer goes out of scope, i.e., the finalization clause (after the with) executes automatically on external abortion.¹

2.3. First-class Timers

Activities that involve reactions to *wall-clock time*² appear in typical patterns of embedded development, such as timeouts and sensor sampling. However, support for wall-clock time is somewhat low-level in existing languages, usually through timer callbacks or "sleep" blocking calls. However, in any concrete system implementation, a requested timeout does not expire precisely with zero-delay, a fact that is usually ignored in the development process. We define the difference between the requested timeout and the actual expiring time as the *residual delta time (delta)*. Without explicit manipulation, the recurrent use of timed activities in sequence (or in a loop) may accumulate a considerable amount of deltas that can lead to incorrect behavior in programs.

The await statement of CÉU supports wall-clock time and handles deltas automatically, resulting in more robust applications. For the example in the left of Figure 4, suppose that after the first await request, the underlying system gets busy and takes 15ms to check for expiring awaits. The CÉU scheduler will notice that the await 10ms has not only already expired, but is delayed with delta=5ms. Then, the awaiting trail awakes, sets v=1, and invokes await 1ms. As the current delta is higher than the requested timeout (i.e. 5ms > 1ms), the trail is rescheduled for execution, now with delta=4ms.

CÉU also takes into account the fact that time is a physical quantity that can be added and compared. For instance, for the program in the right of Figure 4, although the scheduler cannot guarantee that the first trail terminates exactly in 11ms, it can at least ensure that the program always terminates with v=1: Given that any non-awaiting sequence is considered to take no time in the synchronous model, the first trail is guaranteed to terminate before the second trail, because 10+1<12. A similar program in a language without first-class support for timers, would depend on the execution timings for the code marked as <...>, making the reasoning about the execution behavior more difficult.

2.4. Internal Events

Esterel makes no semantic distinctions between internal and external signals, both having only the notion of either presence or absence during the entire reaction [Berry 1993]. In CÉU, however, internal events follow a stack-based execution policy, similar to subroutine calls in typical programming languages. Figure 5 illustrates the use of internal signals (events) in Esterel and CÉU. In the version in Esterel, on the occur-

¹Note that the compiler only enforces the programmer to write the finalization clause, but cannot check if it actually handles the resource properly.

 $^{^2}$ By wall-clock time we mean the passage of time from the real world, measured in hours, minutes, etc.

rence of A, B is emitted and they are both become active, resulting in the invocation of f() and g() in no particular order. In the version in CÉU, the occurrence of A makes the program behave as follows (with the stack contents in italics):

- 1st trail awakes (line 5), emits b, and pauses. stack: [1st]
- (2) 2nd trail awakes (line 9), calls _f(1), and terminates. stack: [1st]
- (3) 1st trail (on top of the stack) resumes, calls _f(2), and terminates. stack: []
- (4) Both trails have terminated, so the par/and rejoins, and the program also terminates;

Internal events bring support for a limited form of subroutines, as depicted in Figure 6. The subroutine inc is defined as a loop (lines 3-6) that continuously awaits its identifying event (line 4), incrementing the value passed as reference (line 5). A trail in parallel (lines 8-11) invokes the subroutine in reaction to event 4 through an emit (line 10). Given the stacked execution for internal events, the calling trail pauses, the subroutine awakes (line 4), runs its body (yielding v=2), loops, and awaits the next "call" (line 4, again). Only after this sequence that the calling trail resumes and passes the assertion test.

On the one hand, this form of subroutines has a significant limitation that it cannot express recursive calls: an <code>emit</code> to itself is always ignored, given that a running body cannot be awaiting itself. On the other hand, this very same limitation brings some important safety properties to subroutines: first, they are guaranteed to react in bounded time; second, memory for locals is also bounded, not requiring data stacks. Also, this form of subroutines can use the other primitives of CÉU, such as parallel compositions and the <code>await</code> statement. In particular, they await keeping context information such as locals and the program counter, just like coroutines [Moura and Ierusalimschy 2009].

3. FORMAL SEMANTICS

In this section, we introduce a reduced syntax of $C \not\equiv U$ and propose an operational semantics in order to formally describe the language. For the sake of simplicity, we focus on the control aspects of the language, leaving out side effects and C calls (which behave like in any conventional imperative language).

3.1. Abstract Syntax

Figure 7 shows the BNF-like syntax for a subset of CÉU that is sufficient to describe all semantic peculiarities of the language. The mem(id) primitive represents all accesses, assignments, and C function calls that affect a memory location identified by id. As the challenging parts of CÉU reside on its control structures, we are not concerned here with a precise semantics for side effects, but only with their occurrences in programs. The special notation nop is used to represent an innocuous mem expression (it can

```
// ESTEREL
                                                   / CEU
input A;
            // external
                                                 input void A; // external (in uppercase)
                                                 event void b; // internal (in lowercase)
signal B:
           // internal
                                              3
                                                 par/and do
                                              4
    await A;
                                              5
                                                      await A;
    emit B;
                                              6
                                                      emit b;
    call f():
                                              7
                                                      _f();
                                                 with
                                              8
                                                      await b;
    await B;
                                              9
    call g();
                                             10
                                                      _g();
                                                 end
]]
```

Fig. 5. Internal signals (events) in Esterel and Céu.

```
event int* inc; // subroutine 'inc'
   par/or do
       loop do
                    // definitions are loops
            var int* p = await inc;
4
            *p = *p + 1;
5
6
       end
   with
7
        var int v = 1:
8
        await A;
9
        emit inc => &v; // call 'inc'
10
        _assert(v==2); // after return
11
   end
12
```

Fig. 6. Subroutine inc is defined in a loop (lines 3-6), in parallel with the caller (lines 8-11).

```
// primary expressions
p ::= mem(id)
                                   (any memory access to 'id')
                                   (await event 'id')
    | await(id)
                                   (emit event 'id')
    | emit(id)
    | break
                                   (loop escape)
                                  // compound expressions
    | if mem(id) then p else p
                                  (conditional)
                                   (sequence)
    | p ; p
    | loop p
                                   (repetition)
    | p and p
                                   (par/and)
    | p or p
                                   (par/or)
    | fin p
                                   (finalization)
                                  // derived by semantic rules
    | awaiting(id,n)
                                   (awaiting 'id' since sequence number 'n')
    | emitting(n)
                                   (emitting on stack level 'n')
                                   (unwinded loop)
    | p @ loop p
```

Fig. 7. Reduced syntax of CÉU.

be thought as a synonym for $mem(\epsilon)$, where ϵ is an unused identifier). Except for the fin and semantic-derived expressions, which are discussed further, the other expressions map to their counterparts in the concrete language in Figure $\ref{fig:prop}$. Note that mem expressions cannot share identifiers with await/emit expressions.

3.2. Operational Semantics

The core of our semantics is a relation that, given a sequence number n identifying the current reaction chain, maps a program p and a stack of events S in a single step to a modified program and stack:

$$\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle$$

where

$$S, S' \in id^*$$
 (sequence of event identifiers : $[id_{top}, ..., id_1]$)
 $p, p' \in P$ (as described in Figure 7)
 $n \in \mathbb{N}$ (univocally identifies a reaction chain)

At the beginning of a reaction chain, the stack is initialized with the occurring external event ext (S = [ext]), but emit expressions can push new events on top of it (we discuss how they are popped further).

We describe this relation with a set of *small-step* structural semantics rules, which are built in such a way that at most one transition is possible at any time, resulting in

deterministic reaction chains. The transition rules for the primary expressions are as follows:

$$\langle S, \ await(id) \rangle \xrightarrow{n} \langle S, \ awaiting(id, n) \rangle \qquad \textbf{(await)}$$

$$\langle id: S, \ awaiting(id, m) \rangle \xrightarrow{n} \langle id: S, \ nop \rangle, \ m < n \qquad \textbf{(awaiting)}$$

$$\langle S, \ emit(id) \rangle \xrightarrow{n} \langle id: S, \ emitting(|S|) \rangle \qquad \textbf{(emit)}$$

$$\langle S, \ emitting(|S|) \rangle \xrightarrow{n} \langle S, \ nop \rangle \qquad \textbf{(emitting)}$$

An await is simply transformed into an awaiting that remembers the current external sequence number n (rule **await**). An awaiting can only transit to a nop (rule **awaiting**) if its referred event id matches the top of the stack and its sequence number is smaller than the current one (m < n). An emit transits to an emitting holding the current stack level (|S| stands for the stack length), and pushing the referred event on the stack (in rule **emit**). With the new stack level |S|+1, the emitting(|S|) itself cannot transit, as rule **emitting** expects its parameter to match the current stack level. This trick provides the desired stack-based semantics for internal events.

Proceeding to compound expressions, the rules for conditionals and sequences are straightforward:

$$\frac{val(id,n) \neq 0}{\langle S, (if \ mem(id) \ then \ p \ else \ q) \rangle \xrightarrow{n} \langle S, p \rangle} \quad \textbf{(if-true)}$$

$$\frac{val(id,n) = 0}{\langle S, (if \ mem(id) \ then \ p \ else \ q) \rangle \xrightarrow{n} \langle S, q \rangle} \quad \textbf{(if-false)}$$

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, (p \ ; \ q) \rangle \xrightarrow{n} \langle S', (p' \ ; \ q) \rangle} \quad \textbf{(seq-adv)}$$

$$\langle S, (mem(id) \ ; \ q) \rangle \xrightarrow{n} \langle S, q \rangle \quad \textbf{(seq-nop)}$$

$$\langle S, (break \ ; \ q) \rangle \xrightarrow{n} \langle S, break \rangle \quad \textbf{(seq-brk)}$$

Given that our semantics focuses on control, rules **if-true** and **if-false** are the only to query mem expressions. The "magical" function val receives the memory identifier and current reaction sequence number, returning the current memory value. Although the value is arbitrary, it is unique in a reaction chain, because a given expression can execute only once within it (remember that loops must contain awaits which, from rule **await**, cannot awake in the same reaction they are reached).

The rules for loops are analogous to sequences, but use '@' as separators to properly bind breaks to their enclosing loops:

$$\langle S, (loop \ p) \rangle \xrightarrow{n} \langle S, (p \ @ \ loop \ p) \rangle$$
 (loop-expd)

$$\frac{\langle S,p\rangle \ \stackrel{\longrightarrow}{\longrightarrow} \ \langle S',p'\rangle}{\langle S,(p @ loop \ q)\rangle \ \stackrel{\longrightarrow}{\longrightarrow} \ \langle S',(p' @ loop \ q)\rangle} \ \textbf{(loop-adv)}$$

$$\langle S, (mem(id) \ @ \ loop \ p) \rangle \ \xrightarrow[n]{} \ \langle S, loop \ p \rangle \ \textbf{(loop-nop)}$$

$$\langle S, (break @ loop p) \rangle \xrightarrow{n} \langle S, nop \rangle$$
 (loop-brk)

When a program first encounters a loop, it first expands its body in sequence with itself (rule **loop-expd**). Rules **loop-adv** and **loop-nop** are similar to rules **seq-adv** and **seq-nop**, advancing the loop until they reach a mem(id). However, what follows the loop is the loop itself (rule **loop-nop**). Note that if we used ';' as a separator in loops, rules **loop-brk** and **seq-brk** would conflict. Rule **loop-brk** escapes the enclosing loop, transforming everything into a nop.

The rules for parallel and compositions force transitions on the left branch p to occur before transitions on the right branch q (rules **and-adv1** and **and-adv2**). Then, if one of the sides terminates, the composition is simply substituted by the other side (rules **and-nop1** and **and-nop2**):

```
isBlocked(n,a:S,awaiting(b,m)) = (a \neq b \lor m = n) \\ isBlocked(n,S,emitting(s)) = (|S| \neq s) \\ isBlocked(n,S,(p;q)) = isBlocked(n,S,p) \\ isBlocked(n,S,(p@loop q)) = isBlocked(n,S,p) \\ isBlocked(n,S,(p and q)) = isBlocked(n,S,p) \land isBlocked(n,S,q) \\ isBlocked(n,S,(p or q)) = isBlocked(n,S,p) \land isBlocked(n,S,q) \\ isBlocked(n,S,\_) = false \ (mem,await, emit,break,if,loop)
```

Fig. 8. The recursive predicate isBlocked is true only if all branches in parallel are hanged in awaiting or emitting expressions that cannot transit.

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, (p \ and \ q) \rangle \xrightarrow{n} \langle S', (p' \ and \ q) \rangle} \qquad \textbf{(and-adv1)}$$

$$\frac{isBlocked(n, S, p) \ , \ \langle S, q \rangle \xrightarrow{n} \langle S', q' \rangle}{\langle S, (p \ and \ q) \rangle \xrightarrow{n} \langle S', (p \ and \ q') \rangle} \qquad \textbf{(and-adv2)}$$

$$\langle S, (mem(id) \ and \ q) \rangle \xrightarrow{n} \langle S, q \rangle \qquad \textbf{(and-nop1)}$$

$$\langle S, (p \ and \ mem(id)) \rangle \xrightarrow{n} \langle S, p \rangle \qquad \textbf{(and-nop2)}$$

$$\langle S, (break \ and \ q) \rangle \xrightarrow{n} \langle S, (clear(q) \ ; \ break) \rangle \qquad \textbf{(and-brk1)}$$

$$\frac{isBlocked(n, S, p)}{\langle S, (p \ and \ break) \rangle \xrightarrow{n} \langle S, (clear(p) \ ; \ break) \rangle} \qquad \textbf{(and-brk2)}$$

The deterministic behavior of the semantics relies on the isBlocked predicate, defined in Figure 8 and used in rule **and-adv2**, requiring the left branch p to be blocked in order to allow the right transition from q to q'. An expression becomes blocked when all of its trails in parallel hang in awaiting and emitting expressions.

The last two rules **and-brk1** and **and-brk2** deal with a break in each of the sides in parallel. A break should terminate the whole composition in order to escape the innermost loop (aborting the other side). The clear function in the rules, defined in Figure 9, concatenates all active fin bodies of the side being aborted (to execute before the and rejoins). Note that there are no transition rules for fin expressions. This is because once reached, an fin expression only executes when it is aborted by a trail in

$$\begin{aligned} clear(fin\ p) &= p \\ clear(p\ ;\ q) &= clear(p) \\ clear(p\ @\ loop\ q)) &= clear(p) \\ clear(p\ and\ q) &= clear(p)\ ;\ clear(q) \\ clear(p\ or\ q) &= clear(p)\ ;\ clear(q) \\ clear(_) &= mem(id) \end{aligned}$$

Fig. 9. The function clear extracts fin expressions in parallel and put their bodies in sequence.

parallel. In Section 3.3.3, we show how an fin is mapped to a finalization block in the concrete language. Note that there is a syntactic restriction that an fin body cannot emit or await—they are guaranteed to completely execute within a reaction chain.

Most rules for parallel *or* compositions are similar to *and* compositions:

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, (p \ or \ q) \rangle \xrightarrow{n} \langle S', (p' \ or \ q) \rangle} \qquad \textbf{(or-adv1)}$$

$$\frac{isBlocked(n, S, p) \ , \ \langle S, q \rangle \xrightarrow{n} \langle S', q' \rangle}{\langle S(p \ or \ q) \rangle \xrightarrow{n} \langle S', (p \ or \ q') \rangle} \qquad \textbf{(or-adv2)}$$

$$\langle S, (mem(id) \ or \ q) \rangle \xrightarrow{n} \langle S, clear(q) \rangle \qquad \textbf{(or-nop1)}$$

$$\frac{isBlocked(n, S, p)}{\langle S, (p \ or \ mem(id)) \rangle \xrightarrow{n} \langle S, clear(p) \rangle} \qquad \textbf{(or-nop2)}$$

$$\langle S, (break \ or \ q) \xrightarrow{n} \langle S, (clear(q) \ ; \ break) \rangle \qquad \textbf{(or-brk1)}$$

$$\frac{isBlocked(n,S,p)}{\langle S,(p\ or\ break)\rangle\ \stackrel{\longrightarrow}{\longrightarrow}\ \langle S,(clear(p)\ ;\ break)\rangle}\ \textbf{(or-brk2)}$$

For a parallel or, the rules **or-nop1** and **or-nop2** must terminate the composition, and also apply the function clear to the aborted side, in order to properly finalize it.

A reaction chain eventually blocks in *awaiting* and *emitting* expressions in parallel trails. If all trails hangs only in *awaiting* expressions, it means that the program cannot advance in the current reaction chain. However, *emitting* expressions should resume their continuations of previous *emit* in the ongoing reaction, they are just hanged in lower stack indexes (see rule **emit**). Therefore, we define another relation that behaves as the previous if the program is not blocked, and, otherwise, pops the stack:

$$\frac{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle}{\langle S, p \rangle \xrightarrow{n} \langle S', p' \rangle} \qquad \qquad \frac{isBlocked(n, \ s:S, \ p)}{\langle s:S, p \rangle \xrightarrow{n} \langle S, p \rangle}$$

To describe a *reaction chain* in CÉU, i.e., how a program behaves in reaction to a single external event, we use the reflexive transitive closure of this relation:

$$\langle S, p \rangle \stackrel{*}{\Longrightarrow} \langle S', p' \rangle$$

Finally, to describe the complete execution of a program, we need multiple "invocations" of reaction chains, incrementing the sequence number:

$$\langle [e1], p \rangle \xrightarrow{\cong} \langle [], p' \rangle$$

 $\langle [e2], p' \rangle \xrightarrow{\cong} \langle [], p'' \rangle$

Each invocation starts with an external event at the top of the stack and finishes with a modified program and an empty stack. After each invocation, the sequence number is incremented.

3.3. Concrete Language Mapping

Although the reduced syntax presented in Figure 7 is similar to the concrete language in Figure ??, there are some significant mismatches between CÉU and the formal semantics that require some clarification. In this section, we describe an informal mapping between the two.

Most statements from CÉU map directly to the formal semantics, e.g., if $\mapsto if$, ';', \mapsto ';', $\downarrow loop \mapsto loop$, $\downarrow l$

3.3.1. await and emit. The await and emit primitives of CÉU are slightly more complex in comparison to the formal semantics, as they support communication of values between emits and awaits. In the two-step translation below, we start with the program in CÉU, which communicates the value 1 between the emit and await in parallel (leftmost code). In the intermediate translation, we include the shared variable e_to hold the value being communicated between the two trails in order to simplify the emit. Finally, we convert the program into the equivalent in the formal semantics, translating side-effect statements into mem expressions:

Note that a similar translation is required for external events, i.e., each external event has a corresponding variable that is explicitly set by the environment before each reaction chain.

3.3.2. First-class Timers. To encompass first-class timers, we need a special TICK event that should be intercalated with each other event occurrence in an application (e.g. e1, e2):

$$\langle [TICK], p \rangle \xrightarrow{*} \langle [], p' \rangle$$

$$\langle [e1], p' \rangle \xrightarrow{*} \langle [], p'' \rangle$$

$$\langle [TICK], p'' \rangle \xrightarrow{*} \langle [], p''' \rangle$$

$$\langle [e2], p''' \rangle \xrightarrow{*} \langle [], p'''' \rangle$$

The TICK event has an associated variable TICK_ (as illustrated in the previous section) with the time elapsed between the two occurrences of external events.

The translation in two steps from a timer await to the semantics is as follows:

```
var int tot = 10000;
dt = await 10ms;
                                                                   mem;
                              loop do
                                                                   loop(
                                                                        await (TICK);
                                  await TICK;
                                  tot = tot - TICK_;
                                                                        mem;
                                  if tot <= 0 then
                                                                       if mem then
                                      dt = tot;
                                                                            mem;
                                      break:
                                                                           break
                                  end
                                                                        else
                              end
                                                                            nop
```

3.3.3. Finalization Blocks. The biggest mismatch between CÉU and the formal semantics is regarding finalization blocks, which require more complex modifications in the program for a proper mapping using the fin semantic construct. The code that follows uses a finalize to safely release the reference to ptr kept after the call to hold:

In the translation to the semantics, the first required modification is to catch the do-end termination to run the finalization code. For this, we translate the block into a par/or with the original body in parallel with a fin to run the finalization code:

In this intermediate code (mixing the syntaxes), the fin body will execute whenever the par/or terminates, either normally (after the await B) or aborted from an outer composition (rules **and-brk1**, **and-brk2**, **or-nop1**, **or-nop2**, **or-brk1**, and **or-brk2** in the semantics). However, the fin will also (incorrectly) execute even if the call to hold is not reached in the body due to an abort before awaking from the await A. To deal

with this issue, for each fin we need a corresponding flag to keep track of code that needs to be finalized:

```
2
    par/or do
         var int* ptr = <...>;
3
         await A;
4
         _hold(ptr);
5
         f_{-} = 1;
6
         await B;
7
    with
8
         { fin
9
10
             if f_ then
11
                  _release(ptr);
             end }
12
    end
13
```

The flag is initially set to false (line 1), avoiding the finalization code to execute (lines 9-12). Only after the call to hold (line 5) that we set the flag to true (line 6) and enable the fin body to execute. The complete translation from the original example in CÉU is as follows:

4. IMPLEMENTATION

The compilation process of a program in CÉU is composed of three main phases, as illustrated in Figure 10:

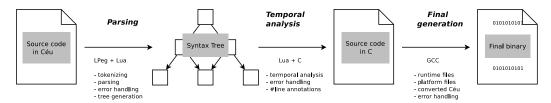


Fig. 10. Compilation process: from the source code in CÉU to the final binary.

Parsing. The parser of Céu is written in *LPeg* [Ierusalimschy 2009], a pattern matching library that also recognize grammars, making it possible to write the to-kenizer and grammar with the same tool. The source code is then converted to an *abstract syntax tree* (AST) to be used in further phases. This phase may be aborted due to syntax errors in the Céu source file.

Temporal analysis. This phase detects inconsistencies in CÉU programs, such as unbounded loops and the forms of non-determinism. It also makes some "classical" semantic analysis, such as building a symbol table for checking variable declarations. However, most of type checking is delayed to the last phase to take advantage

of GCC's error handling. Therefore, this phase needs to annotate the C output with #line pragmas that match the original file in CÉU. This phase must output code in C, given how tied CÉU is to C by design.

Final generation. The final phase packs the generated C file with the CÉU runtime and platform-dependent functionality, compiling them with gcc and generating the final binary. The CÉU runtime includes the scheduler, timer management, and the external C API. The platform files include libraries for I/O and bindings to invoke the CÉU scheduler on external events.

In the sections that follow, we discuss the most sensible parts of the compiler considering our design, such as the temporal analysis, runtime scheduler, and the external API.

4.1. Temporal Analysis

As introduced, the *temporal analysis* phase detects inconsistencies in Céu programs. Here, we focus on the algorithm that detects non-deterministic access to variables, as presented in Section ??.

For each node representing a statement in the program AST, we keep the set of events *I* (for *incoming*) that can lead to the execution of the node, and also the set of events *O* (for *outgoing*) that can terminate the node.

A node inherits the set *I* from its direct parent and calculates *O* according to its type:

- Nodes that represent expressions, assignments, C calls, and declarations simply reproduce O = I, as they do not await;
- An await e statement has $O = \{e\}$.
- A break statement has $O = \{\}$ as it escapes the innermost loop and never terminate, i.e., never proceeds to the statement immediately following it (see also loop below);
- A sequence node (;) modifies each of its children to have $I_n = O_{n-1}$. The first child inherits I from the sequence parent, and the set O for the sequence node is copied from its last child, i.e., $O = O_n$.
- —A loop node includes its body's O on its own I ($I = I \cup O_{body}$), as the loop is also reached from its own body. The union of all break statements' O forms the set O for a loop.
- —An if node has $O = O_{true} \cup O_{false}$.
- A parallel composition (par/and / par/or) may terminate from any of its branches, hence $O = O_1 \cup ... \cup O_n$.

With all sets calculated, any two nodes that perform side effects and are in parallel branches can have their *I* sets compared for intersections. If the intersection is not the empty set, they are marked as suspicious (see Section ??).

Figure 11 reproduces the second code of Figure 2 and shows the corresponding AST with the sets I and O for each node. The event . (dot) represents the "boot" reaction. The assignments to y in parallel (lines 5,8 in the code) have an empty intersection of I (lines 6,9 in the AST), hence, they do not conflict. Note that although the accesses in lines 5, 11 in the code (lines 6,11 in the AST) do have an intersection, they are not in parallel and are also safe.

4.2. Memory Layout

CÉU favors a fine-grained use of trails, being common the use of trails that await a single event. For this reason, CÉU does not allocate per-trail stacks; instead, all data resides in fixed memory slots—this is true for the program variables as well as for temporary values and flags needed during runtime. Memory for trails in parallel must coexist, while statements in sequence can reuse it. CÉU reserves a single static block

```
input void A, B;
                                                                      Stmts I=\{.\} O=\{A\}
                                                                           Dcl_y I={.} O={.}
ParOr I={.} O={A,B}
     var int y;
                                                                  2
    par/or do
                                                                  3
       await A;
                                                                                Stmts I=\{.\} O=\{A\}
                                                                  4
                                                                                  Await_A I=\{.\} O=\{A\}
       v = 1:
                                                                  5
                                                                                Set_y I=\{A\} O=\{A\}
Stmts I=\{.\} O=\{B\}
     with
                                                                  6
      await B;
                                                                  7
                                                                                     Await_B I={.} O={B}
Set_y I={B} O={B}
       v = 2:
                                                                  8
    end
                                                                  9
                                                                           Await_A I=\{A,B\} O=\{A\}
    await A;
                                                                 10
10
                                                                           Set_y I=\{A\} O=\{A\}
    y = 3;
11
                                                                 11
```

Fig. 11. A program with a corresponding AST describing the sets I and O. The program is safe because accesses to y in parallel have no intersections for I.

```
input int A, B, C;
                                                   union {
    var int a = await A;
                                                       int b_2;
                                                                             par/and
end
                                                       struct {
do
                                                           u8 _and_3: 1;
    var int b = await B;
                                                           u8 _and_4: 1;
                                                       };
par/and do
                                                   } MEM ;
   await B;
    await C;
```

Fig. 12. A program with blocks in sequence and in parallel, with corresponding memory layout.

of memory to hold all memory slots, whose size is the maximum the program uses at a given time. A given position in the memory may hold different data (with variable sizes) during runtime.

Translating this idea to C is straightforward [Kasten and Römer 2005; Bernauer and Römer 2013]: memory for blocks in sequence are packed in a struct, while blocks in parallel, in a union. As an example, Figure 12 shows a program with corresponding memory layout. Each variable is assigned a unique id (e.g. a_1) so that variables with the same name can be distinguished. The do-end blocks in sequence are packed in a union, given that their variables cannot be in scope at the same time, e.g., MEM.a_1 and MEM.b_2 can safely share the same memory address. The example also illustrates the presence of runtime flags related to the parallel composition, which also reside in reusable slots in the static memory.

4.3. Trail Allocation

The compiler extracts the maximum number of trails a program can have at the same time and creates a static vector to hold runtime information about them. Again, trails that cannot be active at the same time can share memory slots in the static vector.

At any given moment, a trail can be awaiting in one of the following states: INACTIVE, STACKED, FIN, or in any event defined in the program:

```
input void A;
                                                     enum {
    event void e;
                                                      Main = 1,
                                                                      // ln
    // TRAIL 0 - 1bl Main
                                                       Awake_e,
                                                                     // ln
   par/and do
                                                       ParAnd_chk,
                                                                        ln
         TRAIL 0 - 1bl Main
                                                       ParAnd_sub_2,
                                                                     // ln 10
                                                       Awake_A_1,
      await e;
      // TRAIL 0 - lbl Awake_e
                                                                     // ln 14
                                                       Emit_e_cont,
        TRAIL 0 - 1bl ParAnd-chk
                                                      ParAnd_out,
                                                                     // ln 17
8
                                                      Awake_A_2
                                                                     // ln 19
9
    with
         TRAIL 1 - 1bl ParAnd_sub_2
10
     await A;
11
      // TRATI 1 - 1bl Awake_A_1
12
13
      emit e;
        TRAIL 1 - 1bl Emit_e_cont
14
         TRAIL 1 - 1bl ParAnd_chk
15
16
    end
17
    // TRAIL 0 - 1bl ParAnd_out
18
    await A;
    // TRAIL 0 - 1bl Awake_A_2
19
```

Fig. 13. Static allocation of trails and entry-point labels.

All terminated or not-yet-started trails stay in the INACTIVE state and are ignored by the scheduler. A STACKED trail holds its associated stack level and is delayed until the scheduler runtime level reaches that value again. A FIN trail represents a hanged finalization block which is only scheduled when its corresponding block goes out of scope. A trail waiting for an event stays in the state of the corresponding event, also holding the sequence number (*seqno*) in which it started awaiting. A trail is represented by the following struct:

```
struct trail.t {
    state.t evt;
    label.t lbl;
    union {
        unsigned char seqno;
        stack.t stk;
    };
};
```

The field evt holds the state of the trail (or the event it is awaiting); the field 1b1 holds the entry point in the code to execute when the trail is scheduled; the third field depends on the evt field and may hold the seqno for an event, or the stack level stk for a STACKED state.

The size of state_t depends on the number of events in the application; for an application with less than 253 events (plus the 3 states), one byte is enough. The size of label_t depends primarily on the number of await statements in the application—each await splits the code in two and requires a unique entry point in the code for its continuation. Additionally, split & join points for parallel compositions, emit continuations, and finalization blocks also require labels. The seqno will eventually overflow during execution (every 256 reactions). However, given that the scheduler traverses all trails in each reaction, it can adjust them to properly handle overflows (actually 2 bits to hold the seqno would be already enough). The stack size depends on the maximum depth of nested emissions and is bounded to the maximum number of trails, e.g., a trail emits an event that awakes another trail, which emits an event that awakes another trail, and so on—the last trail cannot awake any trail, because they will be all hanged in a STACKED state. In WSNs applications, the size of trail_t is typically only 3 bytes (1 byte for each field).

4.3.1. Code Generation. The example in Figure 13 illustrates how trails and labels are statically allocated in a program. The program has a maximum of 2 trails, because the

```
while (<...>) {
                                  // scheduler main loop
       trail_t* trail = <...>
2
                                  // choose next trail
       switch (trail->lbl) {
3
          case Main:
4
              // activate TRAIL 1 to run next
5
             TRLS[1].evt = STACKED;
TRLS[1].lbl = ParAnd_sub_2; // 2nd trail of par/and
6
             TRLS[1].stk = current_stack;
8
9
              // code in the 1st trail of par/and
10
              // await e;
11
             TRLS[0].evt = EVT_e;
12
             TRLS[0].lbl = Awake_e;
13
              TRLS[0].seq = current_seqno;
14
             break:
15
16
17
          case ParAnd_sub_2:
18
              // await A;
              TRLS[1].evt = EVT_A;
19
20
              TRLS[1].lbl = Awake_A_1;
21
              TRLS[1].seq = current_seqno;
22
                 break;
23
24
            <...> // other labels
25
26
    }
```

Fig. 14. Generated code for the program of Figure 13.

par/and (line 4) can reuse $TRAIL\ 0$, and the join point (line 16) can reuse both $TRAIL\ 0$ and $TRAIL\ 1$. Each label is associated with a unique identifier in the enum. The static vector to hold the two trails in the example is defined as

```
trail.t TRLS[2];
```

In the final generated C code, each label becomes a *switch case* working as the entry point to execute its associated code. Figure 14 shows the corresponding code for the program of Figure 13. The program is initialized with all trails set to INACTIVE. Then, the scheduler executes the Main label in the first trail. When the Main label reaches the par/and, it "stacks" the 2nd trail of the par/and to run on $TRAIL\ 1$ (line 5-8) and proceeds to the code in the 1st trail (lines 10-15), respecting the deterministic execution order. The code sets the running $TRAIL\ 0$ to await EVT_e on label Awake_e, and then halts with a break. The next iteration of the scheduler takes $TRAIL\ 1$ and executes its registered label ParAnd_sub_2 (lines 17-22), which sets $TRAIL\ 1$ to await EVT_A and also halts.

Regarding cancellation, trails in parallel are always allocated in subsequent slots in the static vector TRLS. Therefore, when a par/or terminates, the scheduler sequentially searches and executes FIN trails within the range of the par/or, and then clears all of them to INACTIVE at once. Given that finalization blocks cannot contain await statements, the whole process is guaranteed to terminate in bounded time. Escaping a loop that contains parallel compositions also trigger the same process.

4.4. The External ${\it C}$ API

As a reactive language, the execution of a program in CÉU is guided entirely by the occurrence of external events. From the implementation perspective, there are three external sources of input into programs, which are all exposed as functions in a C API:

ceu_go_init(): initializes the program (e.g. trails) and executes the "boot" reaction (i.e., the Main label).

```
implementation
2
3
        #include "ceu.h"
        #include "ceu.c"
        event void Boot.booted () {
6
            ceu_go_init();
    #ifdef CEU_WCLOCKS
            call Timer.startPeriodic(10);
9
    #endif
10
11
12
    #ifdef CEU_WCLOCKS
13
        event void Timer.fired () {
14
            ceu_go_wclock(10000);
15
16
17
    #endif
18
    #ifdef _EVT_PHOTO_READDONE
19
        event void Photo.readDone (uint16_t val) {
20
            ceu_go_event(EVT_PHOTO_READDONE, (void*) val);
21
22
23
    #endif
24
    #ifdef _EVT_RADIO_SENDDONE
25
26
        event void RadioSend.sendDone (message_t* msg) {
27
            ceu_go_event (EVT_RADIO_SENDDONE, msg);
28
29
    #endif
30
    #ifdef _EVT_RADIO_RECEIVE
31
32
        event message_t* RadioReceive.receive (message_t* msg) {
33
            ceu_go_event(EVT_RADIO_RECEIVE, msg);
            return msg;
34
35
    #endif
36
37
               // other events
39
```

Fig. 15. The TinyOS binding for CÉU.

ceu_go_event(id,param):. executes the reaction for the received event id and associated parameter.

ceu_go_wclock(us):. increments the current time in microseconds and runs a reaction if any timer expires.

Given the semantics of CÉU, the functions are guaranteed to take a bounded time to execute. They also return a status code that says if the CÉU program has terminated after the reactions. Further calls to the API have no effect on terminated programs.

The bindings for the specific platforms are responsible for calling the functions in the API in the order that better suit their requirements. As an example, it is possible to set different priorities for events that occur concurrently (i.e. while a reaction chain is running). However, a binding must never interleave or run multiple functions in parallel. This would break the CÉU sequential/discrete semantics of time.

As an example, Figure 15 shows our binding for *TinyOS* which maps *nesC* callbacks to input events in Céu. The file ceu.h (included in line 3) contains all definitions for the compiled Céu program, which are further queried through #ifdef's. The file ceu.c (included in line 4) contains the main loop of Céu pointing to the labels defined in the program. The callback Boot.booted (lines 6-11) is called by TinyOS on mote startup, so we initialize Céu inside it (line 7). If the Céu program uses timers, we also start

a periodic timer (lines 8-10) that triggers callback Timer.fired (lines 13-17) every 10 milliseconds and advances the wall-clock time of CÉU (line 15)³. The remaining lines map pre-defined TinyOS events that can be used in CÉU programs, such as the light sensor (lines 19-23) and the radio transceiver (lines 25-36).

REFERENCES

- Sidharta Andalam and others. 2010. Predictable multithreading of embedded applications using PRET-C. In *Proceeding of MEMOCODE'10*. IEEE, 159–168.
- Alexander Bernauer and Kay Römer. 2013. A Comprehensive Compiler-Assisted Thread Abstraction for Resource-Constrained Systems. In *Proceedings of IPSN'13*. Philadelphia, USA.
- Gérard Berry. 1993. Preemption in Concurrent Systems.. In FSTTCS (LNCS), Vol. 761. Springer, 72-93.
- G. Berry. 2000. The Esterel-V5 Language Primer. CMA and Inria, Sophia-Antipolis, France. Version 5.10, Release 2.0.
- F. Boussinot and R. de Simone. 1991. The Esterel language. Proc. IEEE 79, 9 (Sep 1991), 1293-1304.
- Omprakash Gnawali and others. 2009. Collection tree protocol. In Proceedings of SenSys'09. ACM, 1-14.
- Roberto Ierusalimschy. 2009. A text pattern-matching tool based on Parsing Expression Grammars. *Softw. Pract. Exper.* 39 (March 2009), 221–258. Issue 3.
- Marcin Karpinski and Vinny Cahill. 2007. High-Level Application Development is Realistic for Wireless Sensor Networks. In *Proceedings of SECON'07*. 610–619.
- Oliver Kasten and Kay Römer. 2005. Beyond Event Handlers: Programming Wireless Sensors with Attributed State Machines. In *Proceedings of IPSN '05*. 45–52.
- Ana Lúcia De Moura and Roberto Ierusalimschy. 2009. Revisiting coroutines. *ACM TOPLAS* 31, 2 (Feb. 2009), 6:1–6:31.
- ORACLE. 2011. Java Thread Primitive Deprecation. http://docs.oracle.com/javase/6/docs/technotes/guides/concurrency/threadPrimitiveDeprecation.html (accessed in Aug-2014). (2011).
- Dumitru Potop-Butucaru and others. 2005. The Synchronous Hypothesis and Synchronous Languages. In *Embedded Systems Handbook*, R. Zurawski (Ed.).
- Francisco Sant'Anna and others. 2013. Safe System-level Concurrency on Resource-Constrained Nodes. In *Proceedings of SenSys'13*. ACM.

 $[\]overline{^{3}}$ We also offer a mechanism to start the underlying timer on demand to avoid the "battery unfriendly" 10ms polling.