Transparent Standby for Low-Power, Resource-Constrained Embedded Systems

A Programming Language-Based Approach





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- 2. Target **constrained** embedded architectures that form the IoT.
- 3. Provide standby mechanisms at the **programming language** level that scale to all applications.
- 4. Support **transparent**/non-intrusive standby mechanisms that reduce barriers of adoption.

(standby, constrained, programming language, transparent)

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Enforce idle states of execution

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Infer deepest sleeping mode

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- Infer deepest sleeping mode
- Put device to sleep

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- Put device to sleep
- Only awake from interrupts

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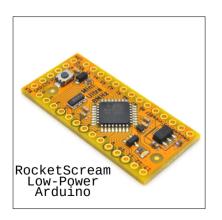


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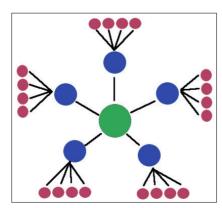
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- Enforce idle states of execution
 - Céu enforces a reactive model of execution
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Transparent Standby for Low-Power, Resource-Constrained Embedded Systems

A Programming Language-Based Approach





Francisco Sant'Anna francisco@ime.uerj.br @_fsantanna

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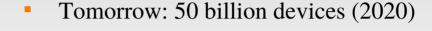
Standby Efficiency for IoT

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- Network standby is one of the six fronts on IEA/G20's Energy Efficiency Action Plan
 - https://www.iea-4e.org/projects/g20













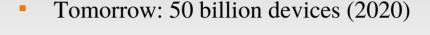
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Tomorrow: 50 billion devices (2020)













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Challenges: Pollution, Autonomy





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Opportunity: Effective "standby"



Challenges: Pollution, Autonomy



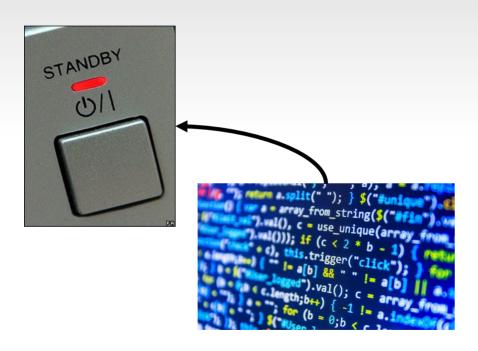




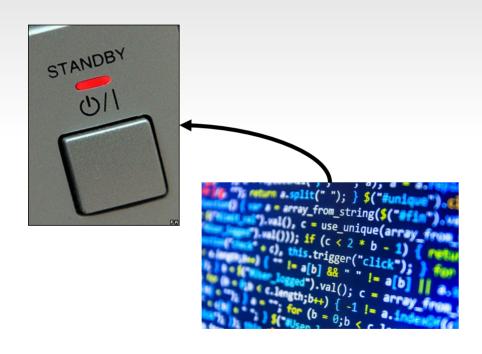


30-50% economy with existing technologies

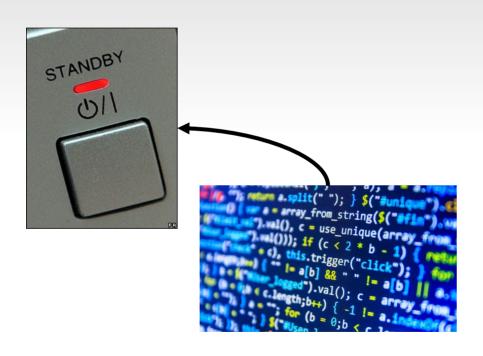


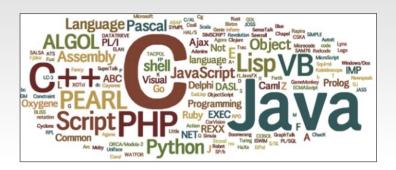


... which is written in a language



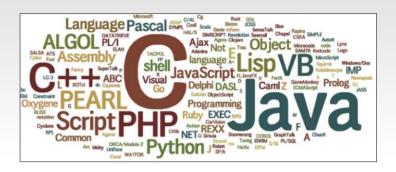
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STANDBY

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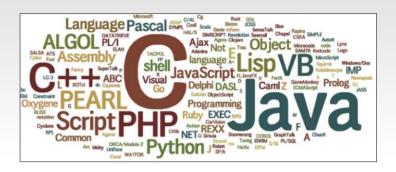
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 - always yields the same outcome for a given timeline

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 - QoS (e.g., resolution, frame rate, accuracy)
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In Practice...

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code AnalogRead (void) -> int do
    PM_SET(PM_ADC, 1);
    do finalize with
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    end
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    int value = await ADC_DONE;
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bitSet(ADCSRA, ADIE);

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 $ADMUX = 0 \times 40 \mid (A0\&0 \times 07);$

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async/isr ADC vect num do
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      sleep 1(<...>)
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    } else if (PM_GET(PM_ADC)) {
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