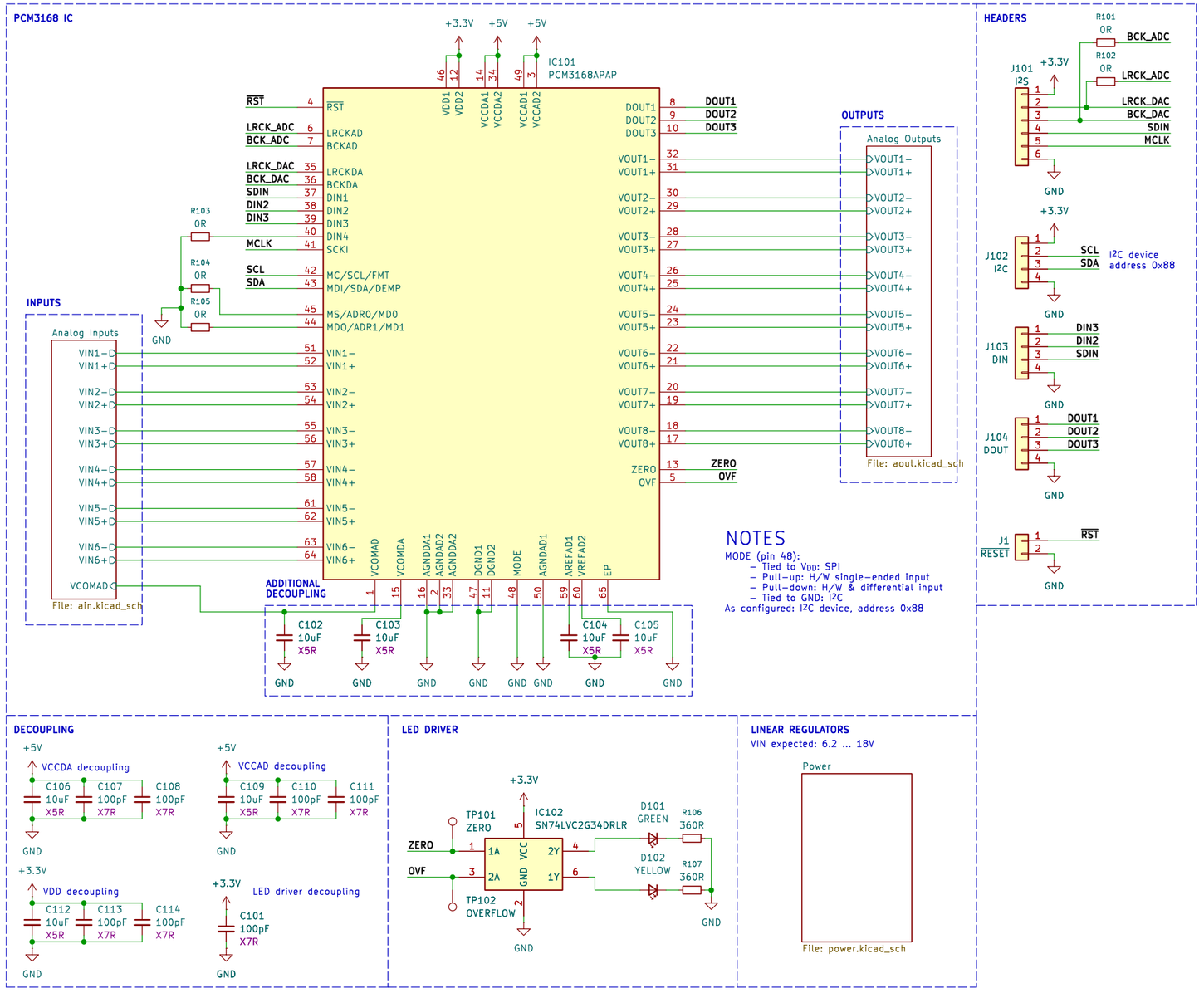


Schematic based on the TIDA-00609 devboard

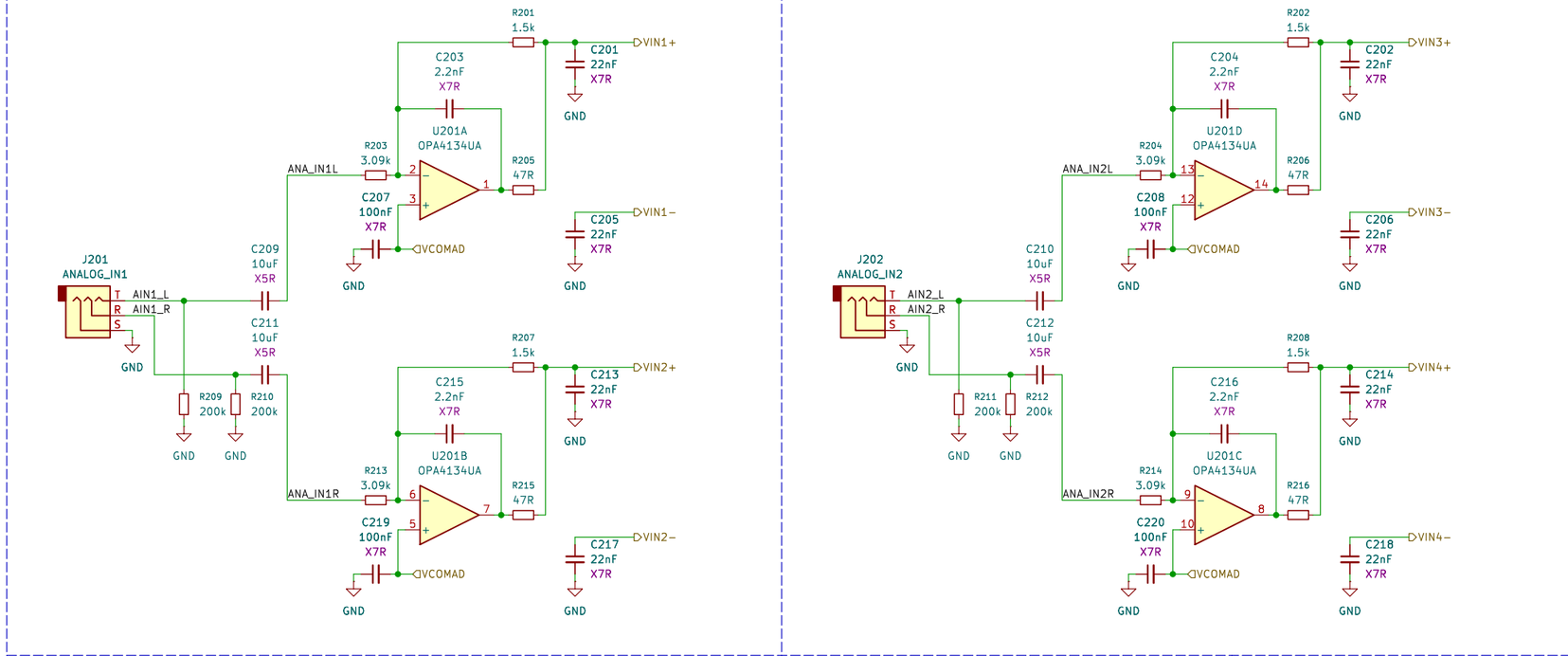
Sheet: /
File: PCM3168APAP-breakout.kicad_sch

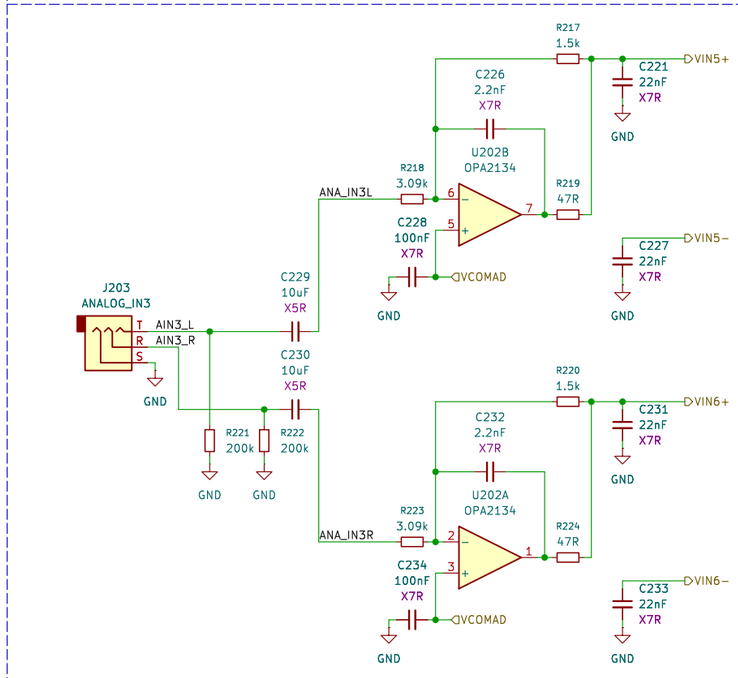
Title: PCM3168PAP Evaluation Board

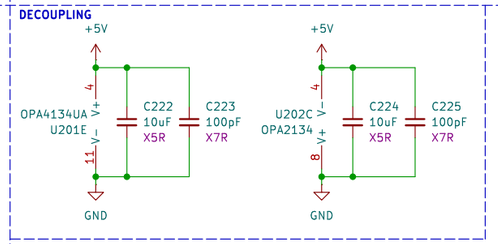
Size: A3	Date: 2026-01-13	**Rev: 0.1**
KiCad E.D.A. 9.0.7		Id: 1/4

BUFFER / ANTI-ALIASING LPF

As configured:
Analog Input (from Jack) = 2 Vrms
Gain = 0.5;
f-3 dB = 48kHz;
Output = 1 Vrms







See Figure 60 from PCM3168APAP datasheet, p. 54
All inputs configured with Buffer and Anti-Aliasing LPF for Single-Ended ADC Input

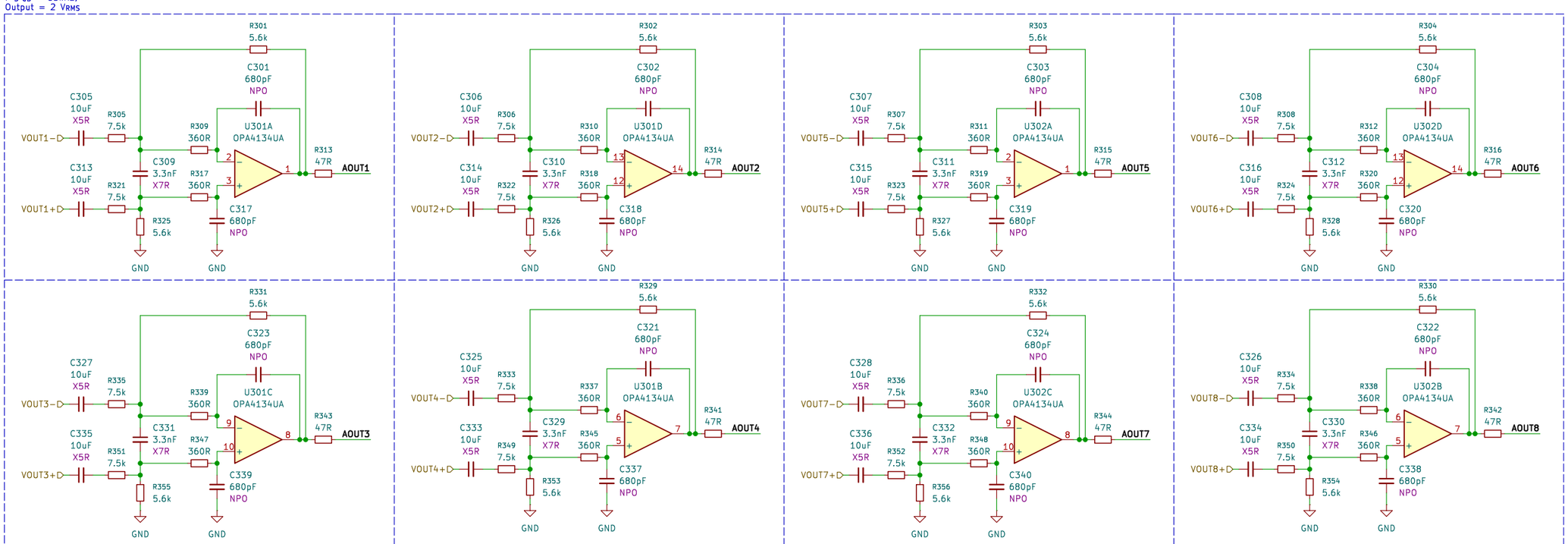
Sheet: /Analog Inputs/
File: ain.kicad_sch

Title: PCM3168APAP Analog Input

Size: A3	Date: 2026-01-13	**Rev: 0.1**
KiCad E.D.A. 9.0.7		Id: 2/4

AC-COUPLED DAC OUTPUT BUFFER

As configured:
Analog Input (from DAC) = 4 Vpp
Gain = 0.747:
f-3 dB = 53kHz:
Output = 2 Vrms

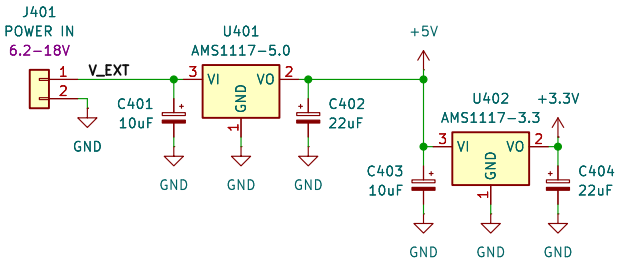


See Figure 61 from PCM3168APAP datasheet, p. 54
Post-LPF and Differential to Single-Ended Buffer for DAC Output

Sheet: /Analog Outputs/
File: aout.kicad_sch

Title: PCM3168APAP Analog Outputs

Size: A3	Date: 2026-01-13	**Rev: 0.1**
KiCad E.D.A. 9.0.7		Id: 3/4



Sheet: /Power/		
File: power.kicad_sch		
Title:		
Size: A4	Date: 2026–01–13	**Rev: 0.1**
KiCad E.D.A. 9.0.7		Id: 4/4