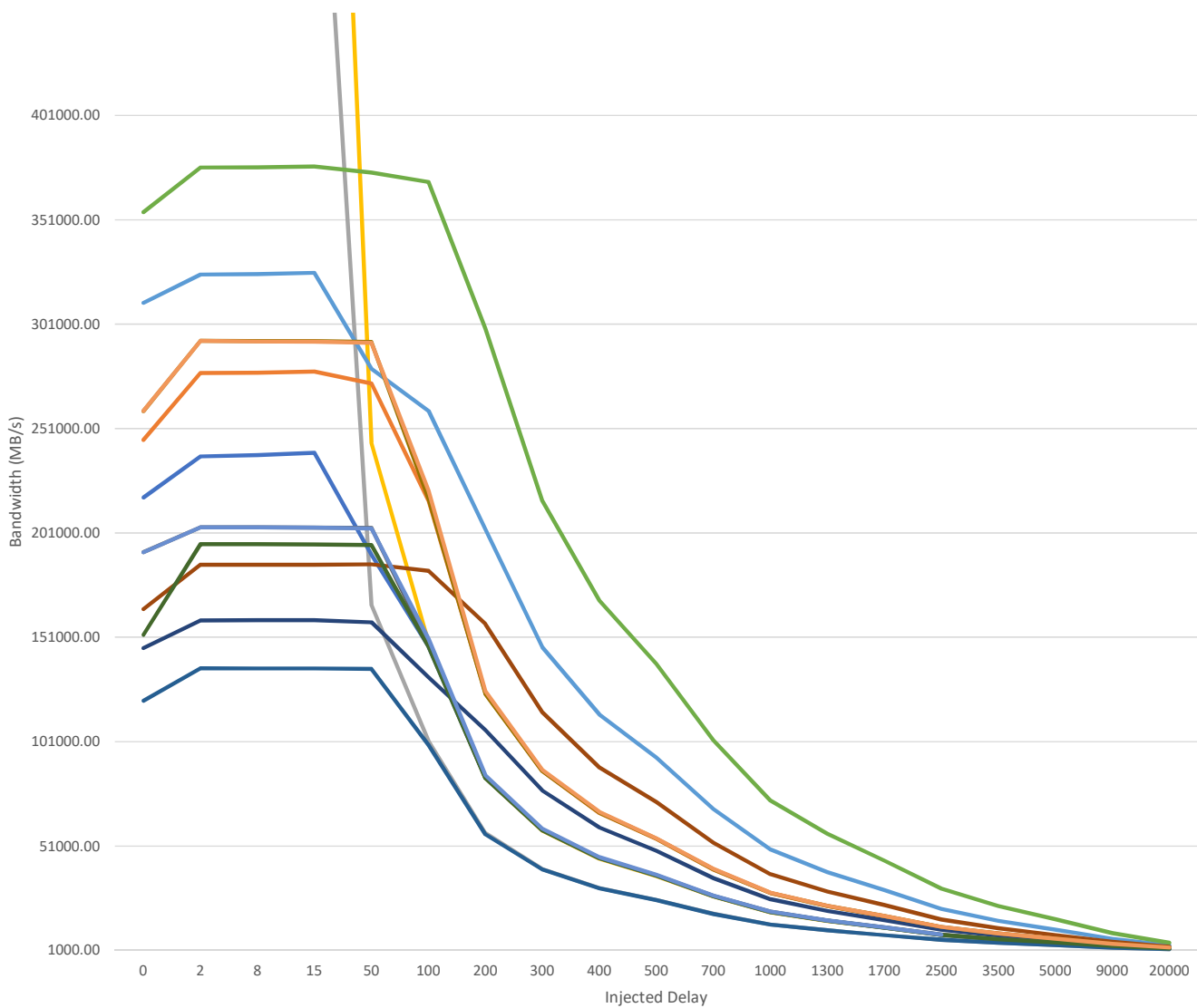


MLC NVM Test (Bandwidth versus Delay)



Node 0 Read Node 1 Read Node 0 W2 Node 1 W2 Node 0 W3 Node 1 W3 Node 0 W5 Node 1 W5 Node 0 W7 Node 1 W7 Node 0 W8 Node 1 W8 Node 0 W10 Node 1 W10