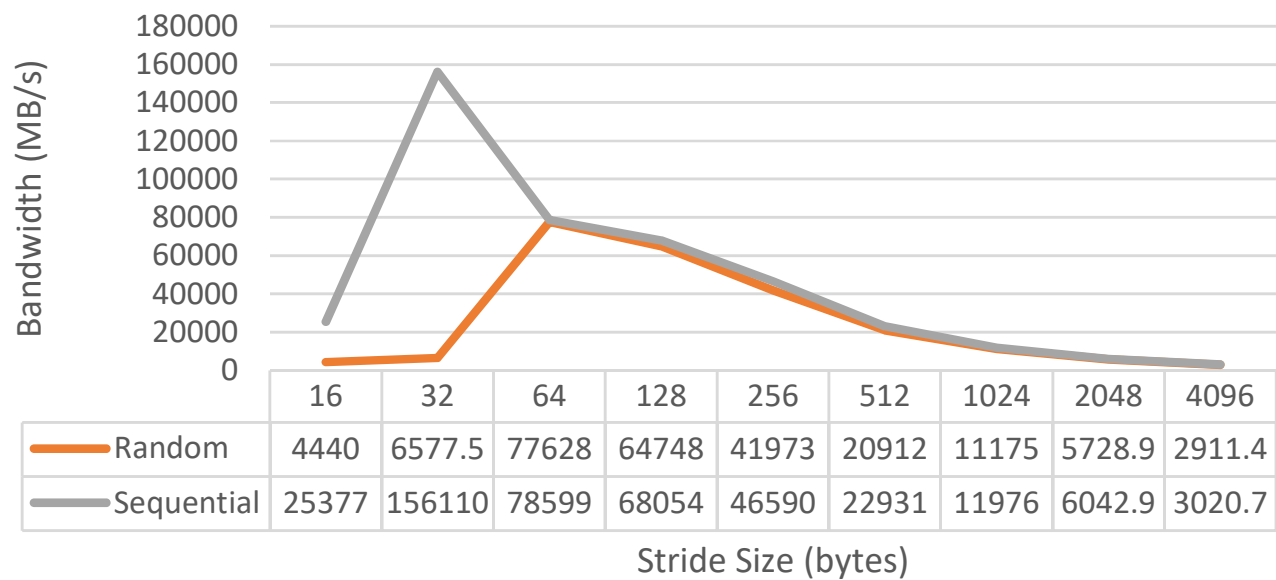


DRAM Baseline (Pure NT Write Pattern)



Random Sequential