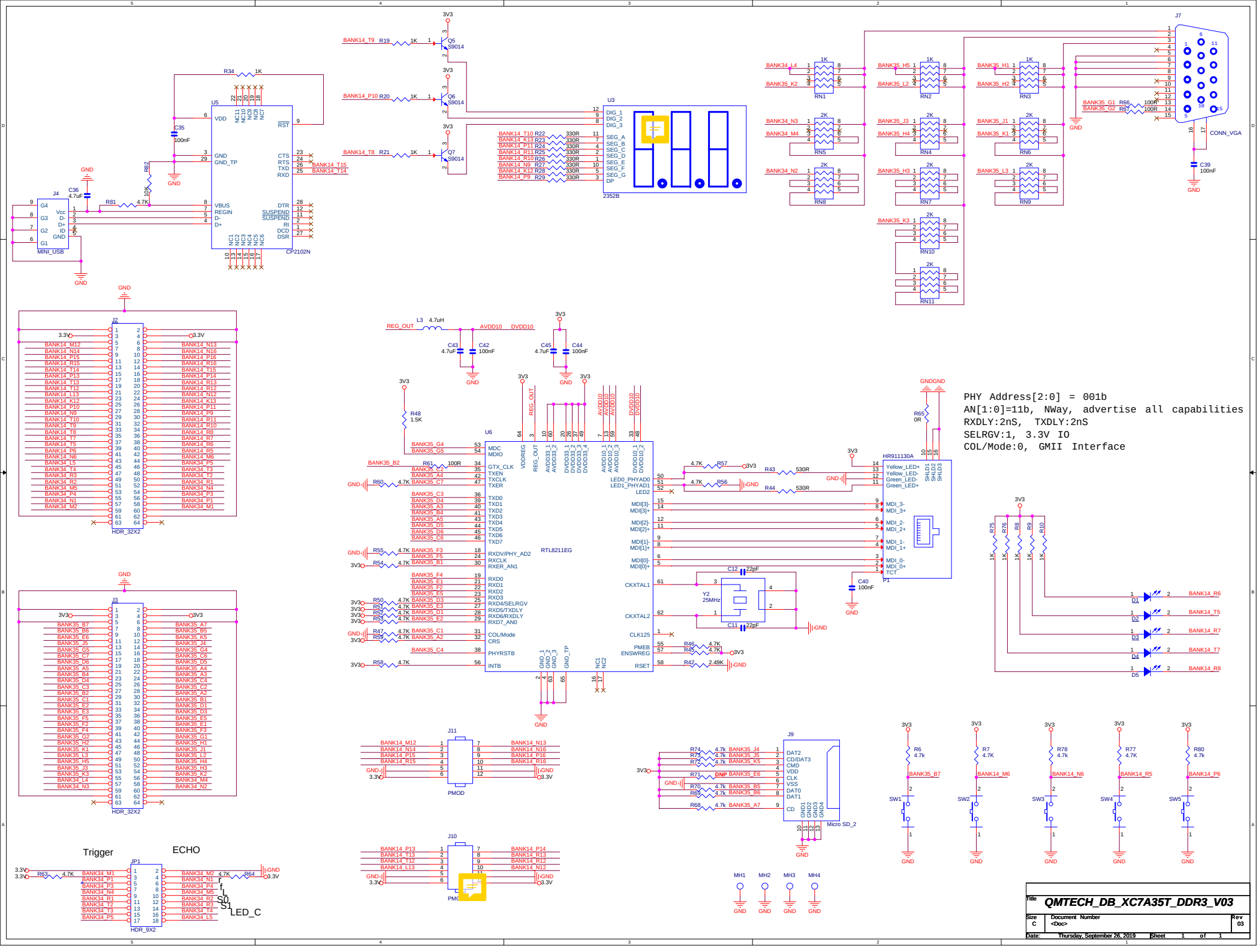
PHY Address[2:0] = 001b
AN[1:0]=11b, NWay, advertise all capabilities
RXDLY:2nS, TXDLY:2nS
SELRGV:1, 3.3V IO
COL/Mode:0, GMII Interface
Trigger
ECHO
LED_C
Title
QMTECH_DB_XC7A35T_DDR3_V03
Size
C
Document Number
<Doc>
Rev
03
Date:
Thursday, September 26, 2019
Sheet
1
of
1