

Felix Sebastian Nitz  
Technische Hochschule Köln

Sheet: /  
File: SOM.kicad\_sch

Title: CCSom

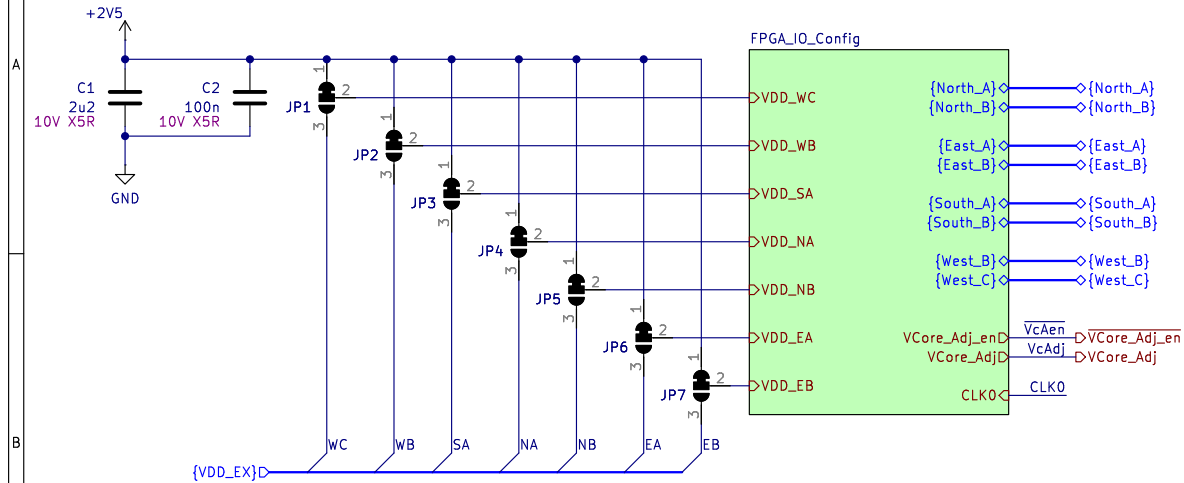
Size: A4 Date: 2024-12-20

KiCad E.D.A. 8.0.6

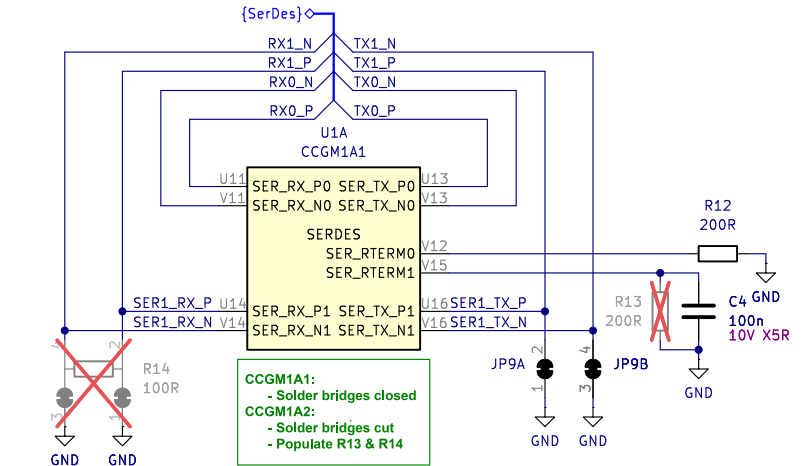
Rev: 1.0

Id: 1/7

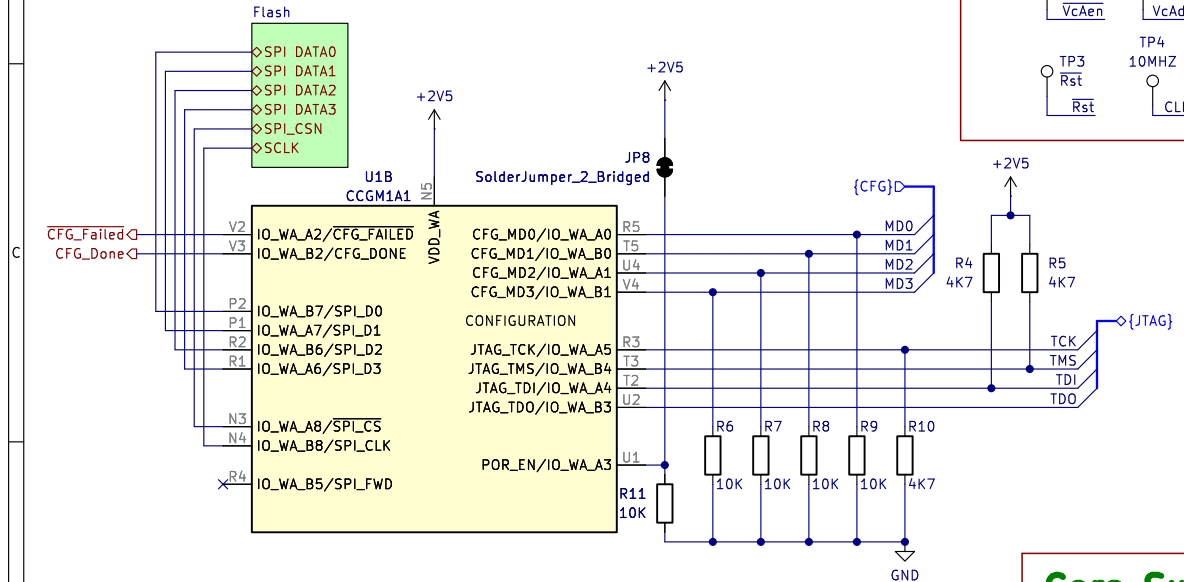
## IO Bank configuration



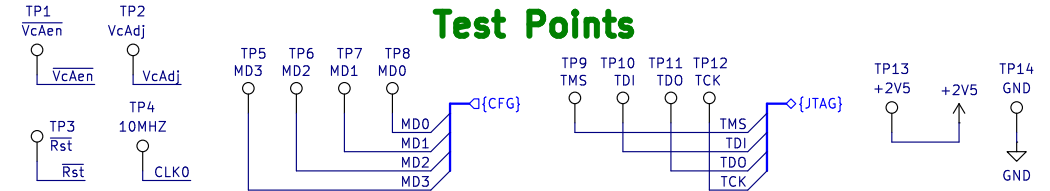
## SERDES Configuration



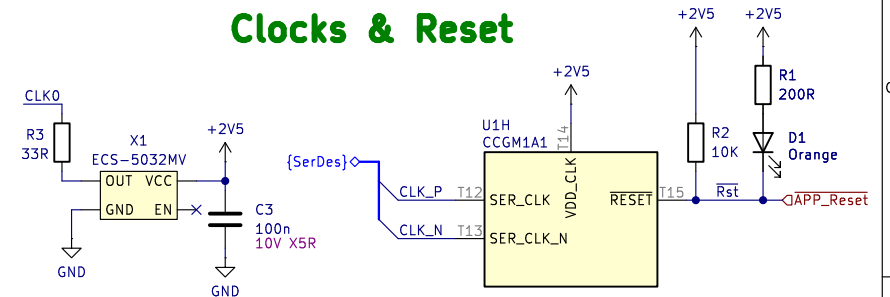
## Configuration Bank



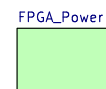
## Test Points



## Clocks & Reset



## Core Supply



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Technische Hochschule K8ln

Sheet: /FPGA\_Top\_Module/  
File: FPGA\_TOP.kicad\_sch

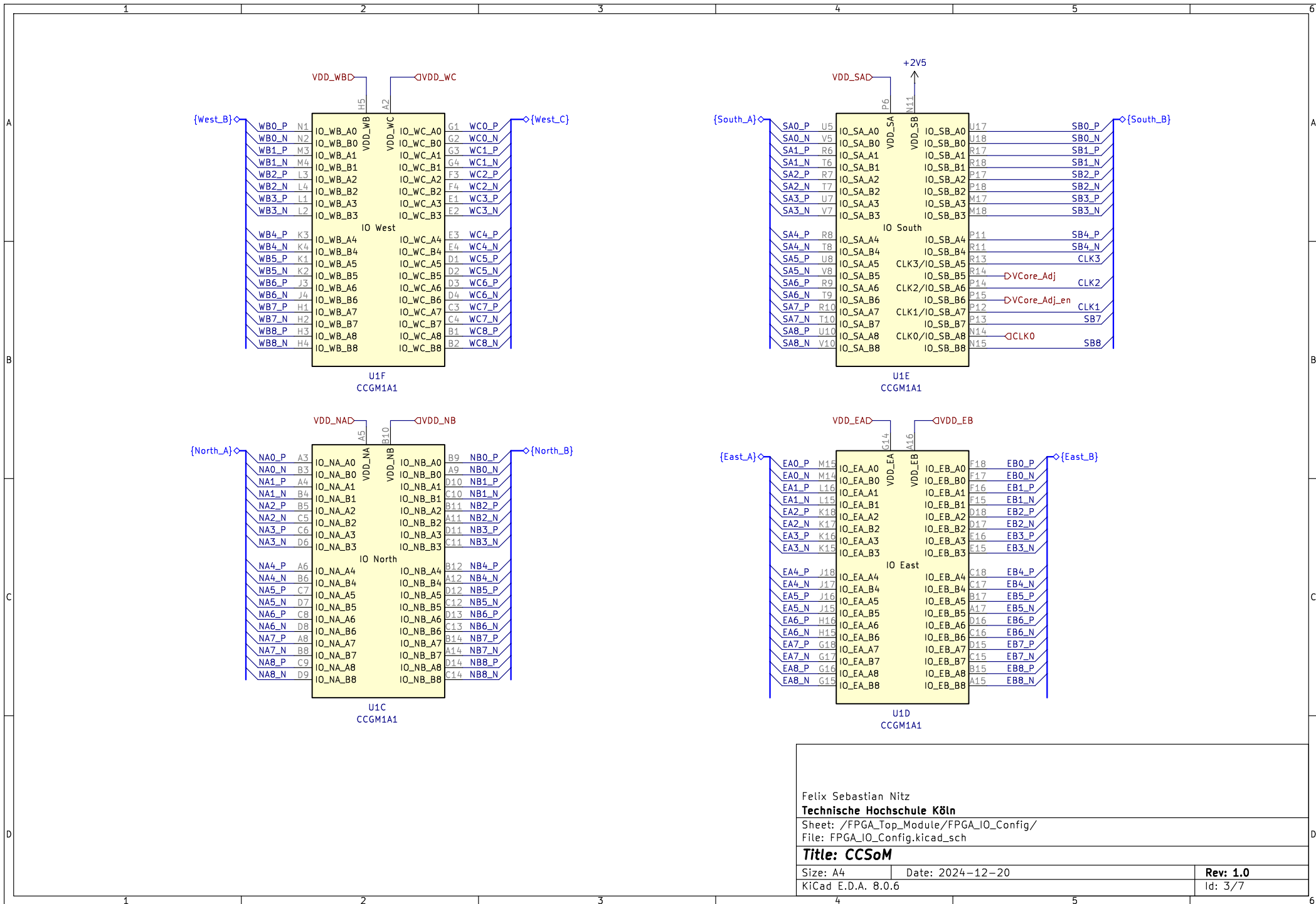
Title: CCSom

Size: A4 Date: 2024-12-20

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Sheet: /FPGA\_Top\_Module/FPGA\_IO\_Config/

File: FPGA\_IO\_Config.kicad\_sch

Title: CCSom

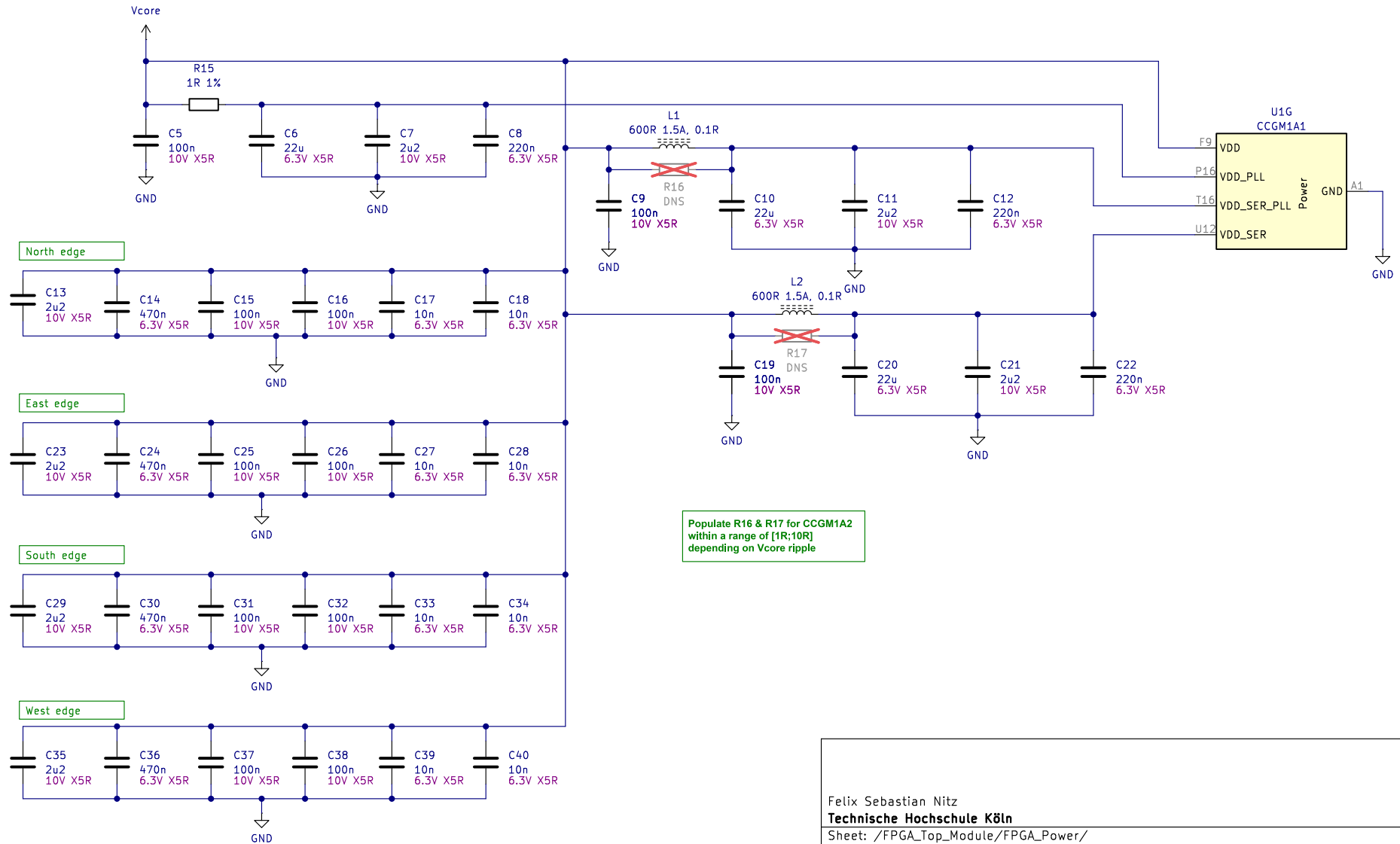
Size: A4

Date: 2024-12-20

Rev: 1.0

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Sheet: /FPGA\_Top\_Module/FPGA\_Power/

File: FPGA\_Power.kicad\_sch

**Title: CCSom**

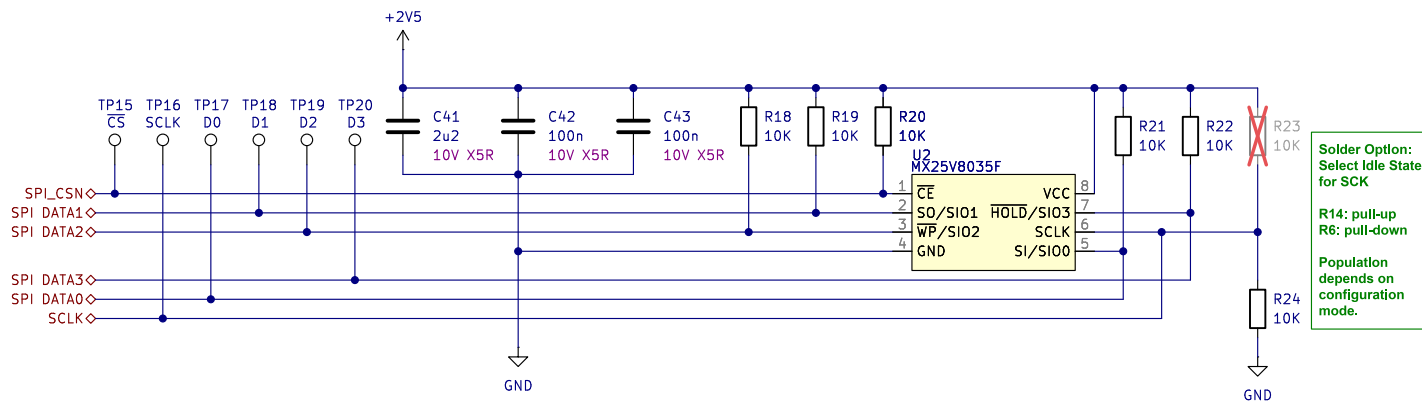
Size: A4

Date: 2024-12-20

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Sheet: /FPGA\_Top\_Module/Flash/  
 File: Flash.kicad\_sch

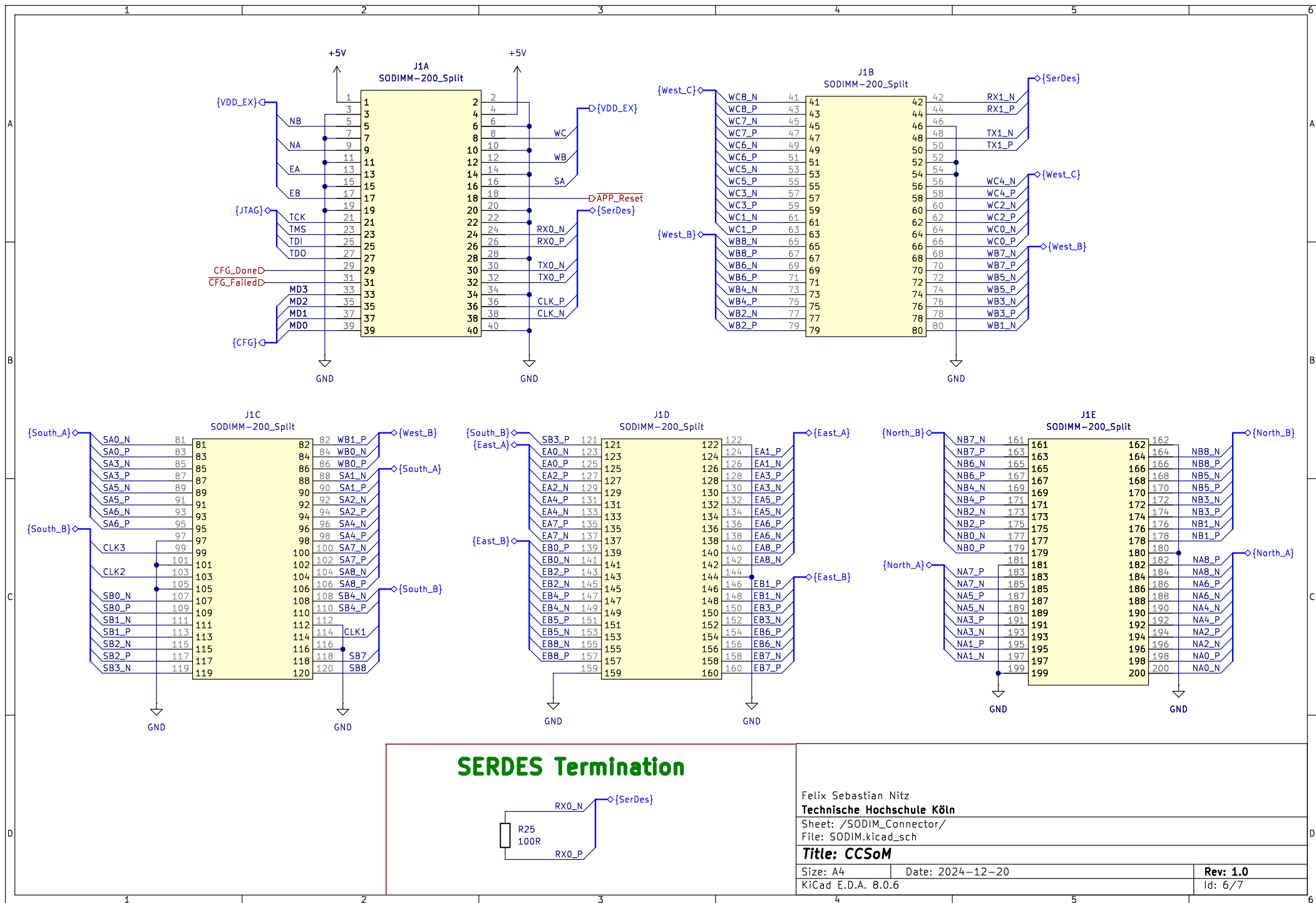
**Title: CCSOM**

Size: A4 Date: 2024-12-20

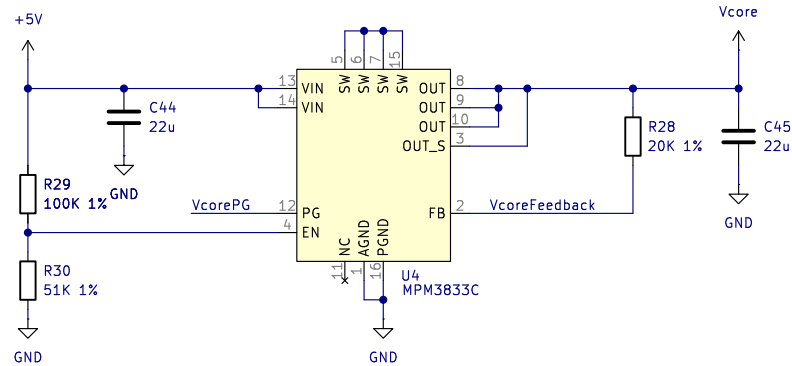
KiCad E.D.A. 8.0.6

**Rev: 1.0**

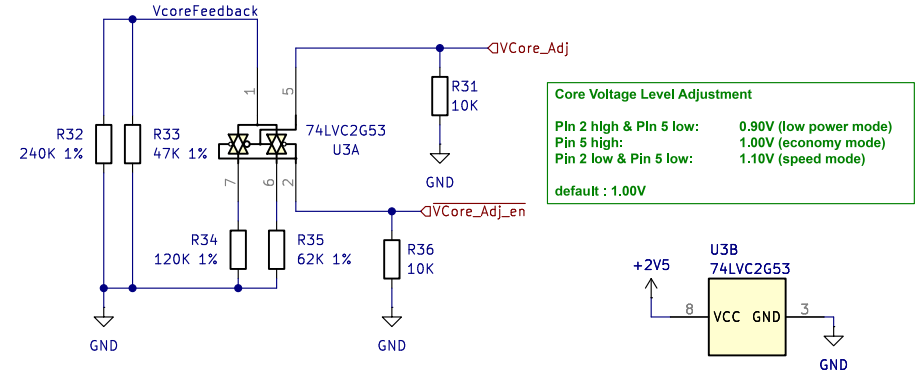
Id: 5/7



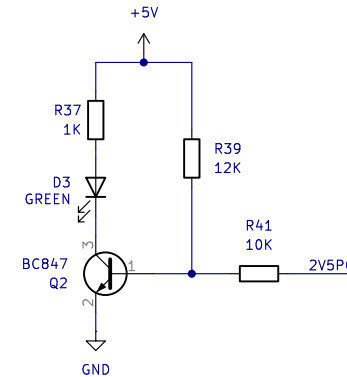
## Vcore Supply Voltage 1V / 3A



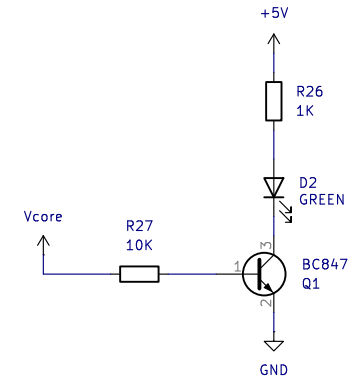
## VCore Adjustment



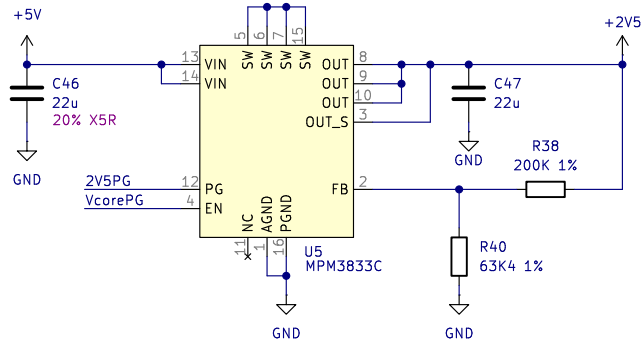
## Power Good LED



## Vcore LED



## IO Supply Voltage 2.5V / 3A



## Test Points



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Sheet: /DCDC\_Regulator/  
File: DCDC.kicad\_sch

**Title: CCSOM**

Size: A4 Date: 2024-12-20  
KiCad E.D.A. 8.0.6

**Rev: 1.0**  
Id: 7/7