

Compiler Design

Introduction to Compilers

Et Logical Analysis Phase

Importance

Computer understands binary or machine or low level language.
But language like C or C++ we write like english so
how does computer understand?

We are interacting with system or phone. This small
unit understanding our actions.

How we are able to interact with computer?

With help of OS → Interface between hardware and
Software

OS - Software → set of programs to do a task
↳ Collection of programs that can perform
a task.

Utility software / System software

↳ We don't have much involvement, we just
given initiate it & rest computer does it.

Ex: OS, compiler, interpreter, assembler, linker, loader

App Application Software - user has to give input.

Set of instructions to perform a given task or
Particular task → program

↳ Task is smaller

Software → Task is larger.

Compiler - converts high level language into low level
language

Because computer understands low level or binary &
we understand high level language.

Given one language \rightarrow [Translator] \rightarrow It produce another language

Such a translator is our compiler.

- Compiler is translator which translates high level language program into machine level language, program or assembly level language program.

HLP \rightarrow [Compiler] \rightarrow MLP/ALP

```
#include <stdio.h>
```

```
int main() {
```

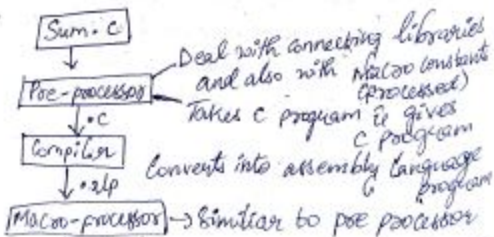
```
    int a=10, b=20;
```

```
    printf("Sum of %d & %d is %d", a, b, a+b);
```

```
    return 0;
```

```
}
```

wrote program as Sum.c which is high level language program



All object file into executable file

Assembler takes alp & converts it into .obj or object program
object file almost equal to binary file

Linker converts into executable file which is ready to execute

Loader loads exe file into memory & initiate execution of program

Command prompt

\$ cc sum.c → It produce .out file
by default it is a.out

\$ ls

\$ a.out → This means file is correctly processed

\$./a.out ⇒ Then sum of 10 & 20 is 30 is
output shown

→ Here all preprocessor, compiler, macroprocessor,
assembler, linker, loader processed to produce out file.

Compiler → System Software

What is cc?

What is Compiler?

Individual unit of program - tokens

Divide entire program into token

Using Regular expression divide programs into tokens.

Syntactically correct or not → Grammar

Combine tokens by using production & build parse tree

Machine to build parse tree is parser.

From parse tree convert into intermediate code (once
parse tree generated then syntactically correct)

Converted into intermediate code & optimized to
generate assembly level language program.

tokens

↓
Regular expression → Unit I

↓
Parse tree → Unit II & III

↓
Intermediate code - IV

↓
optimize

↓
Generate ALP } V

Another system gluano is Interpreter which takes HLP as input and gives directly output



Interpreter takes HLP directly as input and executes line by line. Interpreter HLP into low level language and executes and gives output.

Compiler compiles program at once.

↳ irrespective of errors it goes till end of program and prints entire program. If there are certain errors it lists out all errors.

Compiler

- ① Compiles whole program and identifies as many as errors possible.
- ② Compiler takes huge space as it needs to ^{store} all variables used in program.
- ③ Once program is compiled it doesn't require original file to execute.

Ex: C, Java Compiler

Interpreter

- ① It stops at first error until end user rectifies doesn't move
- ② Interpreter takes (occupies) less space
- ③ ~~also~~ It requires original file to execute

Ex: JVM, browser
Includes interpreter
(Chrome, Internet Explorer etc)

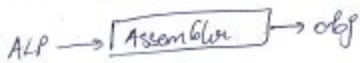
Macroprocessor (System Software)

↳ It takes ALP as input & gives ALP as output

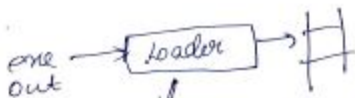
The input contains some macros these macros are expanded in output



Assembler (System Software)



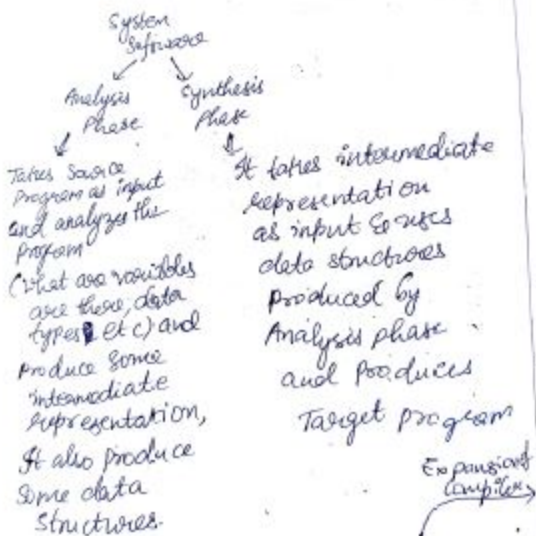
Diff object programs given to linker & it links them and gives executable file as output
(.exe or .out file)
a.out file



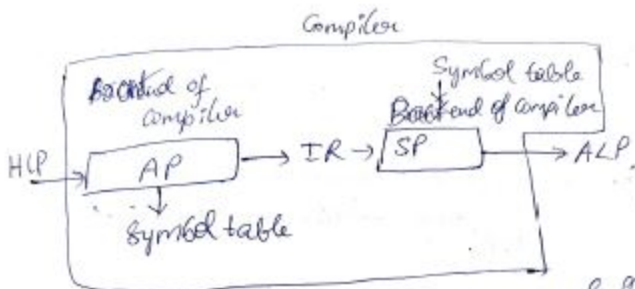
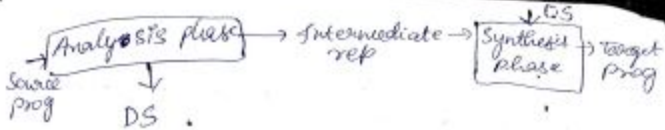
↓
Takes executable file and keeps it in memory & starts executing

Two phases in System Software while designing

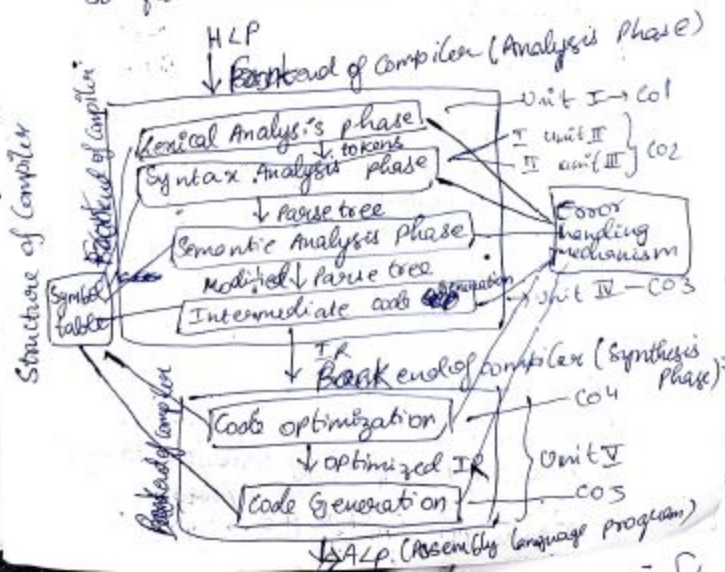
- Phase: A phase is a logically cohesive operation that takes one input representation and produces another output representation.



- DS is symbol table



So any software can be completed in single flow
 So front end & backend are further divided.



Lexical

entire source program is divided into individual units known as tokens. output is token.

So token taken by Syntax Analysis phase & combine tokens by means of some rules & produce output called parse tree.

Syntax → look for syntax of rule doesn't see meaning, checks only form correct or not.

```
void swap(char a, char b);
```

```
int a = 10, b = 20;
```

```
swap(10, 20);
```

```
int a = 10
```

```
float b = 5.5;
```

```
float c = a + b;
```

Syntax wise correct

but semantic (meaning

while error) bcz

adding float & integer

we can't do.

But in C we can

do bcz compiler we

do coercion (internal

typecasting) → done by Semantic Analysis

Depends upon datatype & Phase

* compiler

sees whether

function call

& function

definition

same name

and same

no. of characters

so correct

Syntax but

meaning wrong

Semantic wrong

bcz we passed

integer during

call but check

during definition

So output of Semantic Analysis phase is modified parse tree.

Intermediate Code Generation \rightarrow converts high level language statements into some intermediate representation close to target program.

$$C = a + b * c - d / e;$$

If my ALP should contain at most one operation

So as C has 4 operation we want it to be close to ALP

$$\text{So } T_1 = b * c$$

$$T_2 = a + T_1$$

$$T_3 = d / e$$

$$T_4 = T_2 - T_3$$

$$C = T_4$$

} Apply precedence & associativity to be in form of IR so that can be easily converted into ALP or target.

Some redundancy exist in IR which are not useful, if eliminate that redundancy convert into efficient code (Either less space or less time but both accomplishing not possible) which is Optimized IR as output.

Code generation takes optimized IR & converts into ALP using registers.

ALP is directly linked with machine processor, so ALP can't do multiplication

For compiler to remember everything uses
symbol table or some data structure. So
Symbol table can interact with all phases.

Compiler when sees error does some repair
before moving next line → Error handling
mechanism

Interacted by all
phases

Pass → Collection of phases which takes
one representation as input & produces another
representation as output.

all
Depends upon designer ability we can design phases
in either one pass, two pass etc

Single pass compiler → combine all 6 phases
into single pass

Two pass compiler → combine all 4 phases
into one pass &
2 phases into one pass

Multi-pass compiler → combine lexical analysis
and Syntax Analysis phase into one pass

Replace
expression

↑
int a, b, c;
↑ ↑ ↑
token token token
(KE) helpful to divide
entire line into

, → token
; → token
int → token

individual tokens ⇒ Combined using rules
Grammar

Semantic, Intermediate Code Generation \rightarrow one pass
Code optimization, Code Generation \rightarrow one pass

If different phases then lexical Analysis phase produces file containing tokens as output is given to Syntax Analysis phase which produces parse tree.

So when diff passes, intermediate files are produced due to which compiler gets heavy.

First compiler \rightarrow Find out

\hookrightarrow How much time it took to design it?

Find out various types of compilers available?

Speciality of C compiler?

C \rightarrow Procedural language

C++ \rightarrow C + Object oriented language

Java \rightarrow Completely object oriented programming language

In order to design phases \rightarrow automatic tools are present otherwise huge amount of code to be written

These automatic tools are called scanners bcz used to scan & divide into individual tokens.

One such scanner is lex tool.

\downarrow
Produces lexical Analysis phase

YACC Tool \rightarrow Produce another Syntax Analysis

Yet Another Compiler Compiler (But for what lang we need ^{phase} ~~for~~ ^{specific})

Phases of Compiler:

~~Lex~~
HLP → Lexical Analysis Phase → Tokens

```
int main() {
```

```
    int a=10, b=20;
```

```
    printf("%d
```

Scan program from beginning & form into some
called lexemes

Initially scans 1 lexeme as i

Next n lexeme as in

Next t lexeme as int

first
token

lexeme recognized first
& then what type of
lexeme

Space not considered
as lexeme

main → Another token

(→ one token

) → one token

{ → one token

int → one token

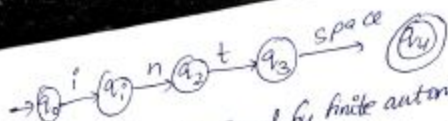
a → one token

= → one token

10 → one token

Entire program
divided into
series of
tokens.

Mechanism used to divide into tokens is automata
(finite automata)



int recognized by finite automata

String recognized by FA \rightarrow Regular expression

Recognizing token rule RE (Regular expression) using finite automata & RE can recognize various types of tokens.

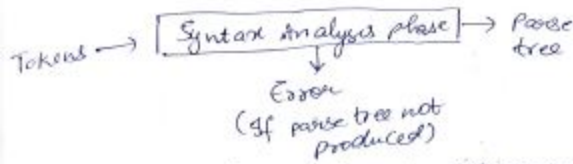
int keyword	} Identifiers	} Diff type of tokens (Types of tokens)
a } variable name		
main } function name		
printf }		
=, ; \rightarrow Symbols	}	
%, (,), + \rightarrow operator		
10, 20 \rightarrow Constants		

Tokens can be represented as pair

(Token type, Token value)

\downarrow
Depends upon type of token
Keyword can take 1 to 32
Identifier \rightarrow location where identifier placed in value \nwarrow symbol table

② Syntax Analysis phase (only looks at form not meaning)



$d = a + b * c ; \rightarrow$ Statement correct

form of Statement or instruction correct or not
 \rightarrow Syntax Analysis phases

form correct or not depends upon some rules

$\text{while (cond) ;} \rightarrow$ Run infinite times
 \rightarrow no error as Syntactically correct
but meaning wrong

$\text{while (cond) \{}$

~~Statement~~

Statement 1

\vdots

Statement n;

$\}$

CFG used to define rules to validate each & every instruction of program.
Based on productions given in CFG

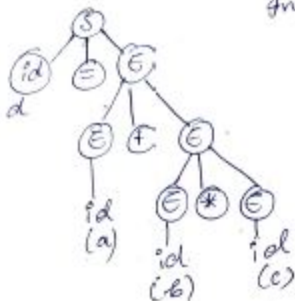
If statement $d = a + b * c ;$ written appropriately then we can produce parse tree

$S \rightarrow id = E$

$E \rightarrow E * E \mid E + E$

$E \rightarrow id$

leaf nodes \rightarrow terminals
internal \rightarrow non terminal



(BODMAS rule followed)

$d = a + b * c$ As statement not correct so can't produce parse tree

Semantic Analysis phase (It does look meaning of statement)

Parse tree \rightarrow Semantic Analysis Phase \rightarrow Modified Parse tree

- ① $C = a + 10;$ It should be of same data type but here diff data type
 $\hookrightarrow a \rightarrow \text{float}$

Compiler internally makes 10 as 10.0 and doesn't throw errors

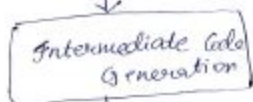
Modification done by compiler \hookrightarrow produces modified parse tree

② swap(10, 20);

void swap(char a, char b);

} compiler internally
converts 10 & 20
into character
(ASCII values)

Parse Tree



Intermediate representation

It should be simple statement and close to Assembly language program

Gap b/w high level & assembly language

very high
(conversion \rightarrow diff directly)

- Postfix Notation

All statements can be
converted in some form
of postfix notation

- Syntax Tree



Infix exp
operator must
be placed b/w
operands
But should know
BODMAS rule
or precedence
to evaluate

(Not worried
about
Precedence)

Prefix exp
+ a b

Postfix exp
a b c

Already
taken care

• From parse tree
Obtain syntax tree

- Three Address Statements

$$d = a + b * c$$

At most
one
operation

$$\left\{ \begin{array}{l} T_1 = b * c \\ T_2 = a + T_1 \\ d = T_2 \end{array} \right.$$

Three address
62 max of
3 operands
in each
statement

=> from now on deal with
three address statement

IR \rightarrow Code optimization \rightarrow optimized IR

Remove redundant statements
without compromising meaning
is optimization (code improvement)

$\left. \begin{array}{l} T_2 = a + T_1 \\ d = T_2 \end{array} \right\} \rightarrow \left. \begin{array}{l} T_1 = b * c \\ d = a + T_1 \end{array} \right\}$ Done by
Code optimization
as ICG can't
do this thing as
don't have

local optimization
(focused only on blocks)

loop optimization
(focus on block in loops)

Global optimization
 \downarrow
Depends upon more than one block

~~Code~~

IR \rightarrow Code Generation \rightarrow ALP

ALP \rightarrow low level language (Tightly bound with hardware)

Know about hardware, registers is very much important

More time \uparrow R_1, R_2 are two registers available freely

These may take more memory cycles \downarrow

{	MOV R_1, b	}	Direct mode Dealing with memory location
	MUL R_1, c		
	MOV R_2, a		

3

{	ADD R_2, R_1	}	Both register So register mode

Takes only 1 memory cycle \rightarrow Less time

How effectively use registers & it should know hardware structure to produce ALP

Error handling mechanism \rightarrow Routines to deal with errors & throw errors

Phases of Compiler - An Example

Amount = Principle + rate * term / 100

\rightarrow High level language statement

\downarrow
Lexical Analyzer (LAr)

\downarrow tokens $id_1, =, id_2, +, id_3, *, id_4, /, least$

Syntax Analyzer (SAr)

Semantic Analyzer
Se Ar

modified
PT

ICG

Cor

CG

ALP

first combines
 $id_3, *, id_4$
then /

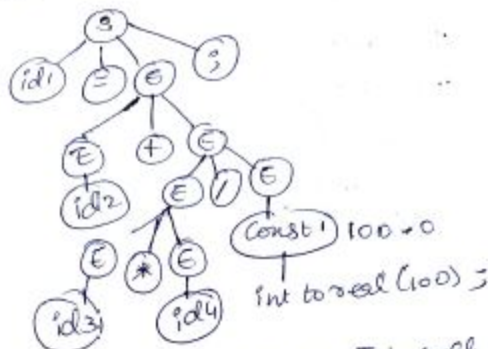


Parse tree

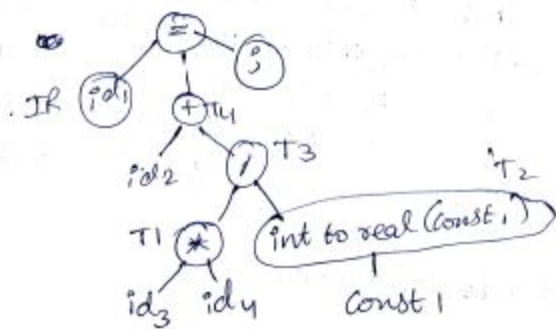
Amount, principle,
rate, term and real
and 100 is constant
So diff data types

So Se Ar adds
one function to
const that is
int to real which
will take 100 as
parameter to
convert to 100.0

Modified Parse tree



- ④ Syntax Tree \rightarrow Eliminates E in such a way that operators become internal nodes & operands become leaf nodes. whose depth is very high that operation performed first. It is simplified ~~tree~~ parse tree.



④ Three Address Statements

$$T_1 = id_3 * id_4$$

$$T_2 = \text{int to real (const 1)}$$

$$T_3 = T_1 / T_2$$

$$T_4 = id_2 + T_3$$

$$id_1 = T_4 ;$$

⑤

$$T_1 = id_3 * id_4$$

$$T_3 = T_1 / 100.0$$

$$id_1 = id_2 + T_3$$

Empty Register
 R_1, R_2

⑥ MOV R_1, id_3 $R_1 \leftarrow id_3$ 3L

MUL R_1, id_4 $R_1 = R_1 * id_4$ 3L

DIV $R_1, \#100.0$ $R_1 \leftarrow R_1 / 100.0$ 4L

MOV R_2, id_2 R_2, id_2 3L

ADD R_2, R_1 $R_2 \leftarrow R_2 + R_1$ 1L

STORE R_2, id_1 $id_1 \leftarrow R_2$ 3L

Division
immediately
↓
Passes
directly
the
operand
into
instruction

$$d = b * b - 4 * a * c ;$$

LEX TOOL \rightarrow It is for scanner
It is given REG it will give us into
~~can~~ tokens

YACC tool \rightarrow Produce a parser
program
 \downarrow
Yet Another
Compiler Compiler

Different ways or The Science of a building
a Compiler.

There are 2 methods :

- ① Bootstrapping
- ② Cross Compiler

\rightarrow Building of a Compiler on its own action is
known as bootstrapping.

Cross Compiler - The Compiler that runs on
one machine & produce ^{an} output that can
run on another machine.

Build a compiler for particular language
what are needed

These are keywords
Define way constants, Symbols, etc that
programming language accepts

Define Syntax

Define tokens \rightarrow RE

Define rules \rightarrow CFG

Metal detector
↓
Chip

Automatic washing machine
↓
Chip
↓
Not a Computer

Automatic gear bike
↓
Not a Computer

Compiling on one machine but output on another machine

① Bootstrapping

YZ
X

X → The machine that runs
Program runs on that machine
/ Source language of
Compiler
Program

Y → Input lang
↓
lang in which
I want to design
Compiler

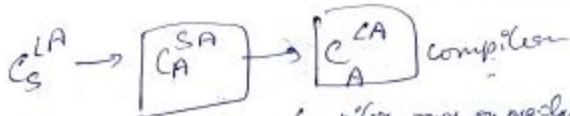
Z → The machine that runs / Target lang of
Compiler.

Internal hardware architecture of machine (we should have that knowledge)

Interested to develop a compiler for language L

Small set of features ✓ S C L
↓
with S features we can build
Compiler for L

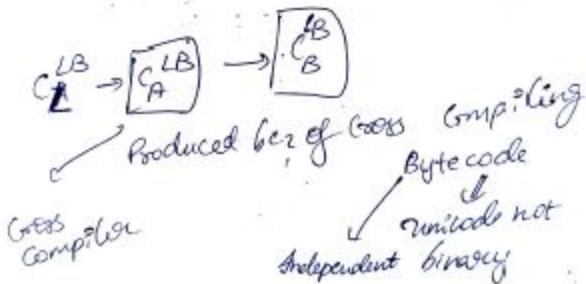
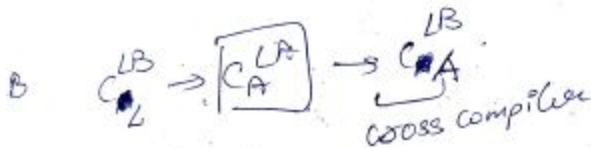
Using S we can write compiler program for machine A.
SA
CA



Bootstrapping

Compiler runs on machine A
 & accept any programming
 language & produce
 target that runs
 on machine A

Ex: Java C
 is written
 in Java
 itself



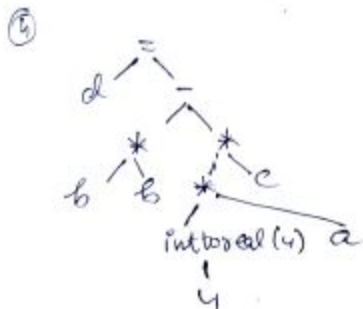
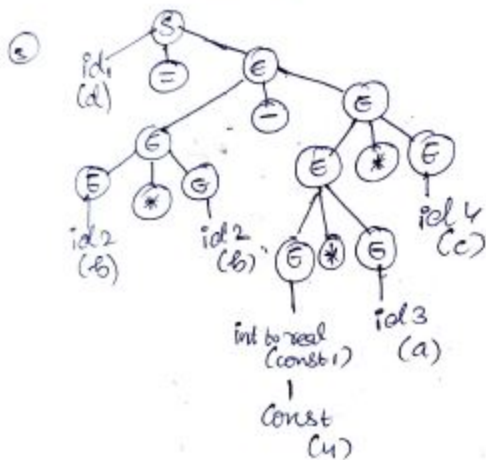
$$d = \frac{b \times b - 4 \times a \times c}{12} \quad (2)$$

$$d = \frac{b * b - 4 * a * c}{\frac{(1)}{(2)} \frac{(2)}{(1)}}$$

① $id_1, =, id_2, *, id_2, -, Const^1, *, id_3, *, id_4$

New token

New token
→ checks in symbol table
if same gives same identifier



$$T_1 = b * b$$

$$T_2 = \text{int to real}(u)$$

$$T_3 = T_2 * a$$

$$T_4 = T_3 * c$$

$$T_5 = T_1 - T_4$$

$$d = T_5$$

⑤

$$T_1 = b * b$$

$$T_3 = u.o * a$$

$$T_4 = T_3 * c$$

$$d = T_1 - T_4$$

⑥ R_1, R_2

MOV R_1, b

MUL R_1, R_1

MUL $R_2, \#4.0$

MUL R_2, a

MUL R_2, c

SUB R_1, R_2

STORE R_1, d

$u * a * b$

left associative

$u \wedge 2 \wedge 2$

Two same
Precedence
associativity

Right
associative

Register mode Reg Reg (first operand in Register,
Second operand in register)

Reg Memory location (Direct location
from memory location
fetch & use)

(Allocate
memory to
stack memory
location)

Stack location

direct value (Immediate value)

ML ML (Memory direct mode)

Lexical Analysis Phase

High level language → Lexical Analysis Phase → Tokens

How to design lexical Analysis phase?

Consider 3 aspects

Scan entire source program and character by character & divide into tokens → main work

1) A mechanism to describe tokens

How token appears at end of output

Each token has a different type of characteristic pattern known as Regular Expressions.

⇒ Tokens in form of RE

Pattern for ~~operator~~ operator, symbols etc

2) A mechanism to recognize the token

- Finite Automata

Deterministic as it can be implemented

3) It produce a mechanism to perform certain action

- produce output token recognized otherwise error

If output produce give it to parser

② Role of Lexical Analyser

Two roles:

→ ~~It~~ As a single phase produce all tokens (of src program)

→ It doesn't scan entire program at a time)

→ It acts as co routine/subroutine to produce a token & give it back to parser. (Gives one token at time)

(Scans when parser requests & give it to parser, again if parser requests scan remaining program & give it to parser)

③ Need of Lexical Analyser

from HL to produce parse tree → very difficult

If not individual tokens then difficult for parser to convert into parse tree

→ To simplify function of parser
(simplify task of parser (Syntax Analyzer))

$c = a + b;$
 $c = a + b;$
 $c = a + b;$

} Additional spaces
 skipped by lexical
 Analyzer

/* Sum = c developed by CS */
 /* Date (created : 9th Dec, 2012) */
 /* Updated on 12th Dec, 2023 */

Any preprocessing done in the task of
 Lexical Analyzer

(2) Preprocess the HLP (to give exactly
 required code)

Input given to lexical analyzer
 in form of file.

Preprocessing to be done to
 file is very much difficult
 to insert, delete between lines
 So use buffer to do
 preprocessing.

Buffer
 ↓
 Synchronize
 Processor
 & memory
 Fetches lot
 of instruction
 & stored
 in buffer
 In buffer we
 can delete
 add easily

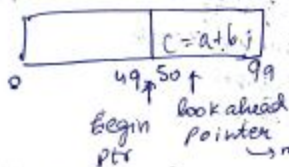
buffer

Copy instructions & fetches instruction from memory to buffer so for faster access

from file
Fetches into buffer first & then reads it
If comment there in buffer removes all comments. If there are spaces in input buffer they are removed.

If buffer \rightarrow 100 characters

Split into two parts

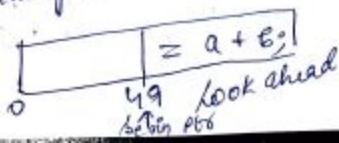


Whatever
file program
fetched into
second part
first
 \rightarrow move forward
 \rightarrow backward

String between
look ahead pointer

& begin ptr is a token

So look ahead first points at C & then
moves forward, space recognized as delimiter
& look ahead moves back & C is recognized
as token removed, space is squeezed &
remaining present



look ahead like circular queue after moving last
On move to first part.

Token → Smallest individual part of program
↳ collection of character that recognizes as
single unit
Always represented as string as it is
present in file

Token types

- ↳ Keywords
- ↳ Identifiers
- ↳ Constants/Literals
- ↳ Operators
- ↳ Symbols

Each token has
diff pattern

Each keyword has unique pattern

All unique pattern {
int → one pattern
float → another pattern
break
else
if
while

Pattern for integer → int

inta doesn't match anything
↳ So not keyword

Identifier \rightarrow Name given to programming constant
 \hookrightarrow require pattern to describe identifier

They are ~~rule~~ rules (Regular expression)

① Character or $-$ \rightarrow first character

② Remaining can be character or ~~or~~ digits

This pattern uniquely defines all names known as RE.

(A) Character const \rightarrow Enclosed in single quotes (' ') ^{Pattern}
(B) String const \rightarrow enclosed in double quotes (" ")

Operator \rightarrow 9 diff operators
 \hookrightarrow diff types, diff pattern

General term for pattern \rightarrow RE \rightarrow 3 operations
Concatenation
Kleene closure
Union

l
name for RE

Corresponding RE

l

[a..z A..Z]

d

[0..9]

} RE

Defined RE based on names

id

$l(l/d)^*$ \rightarrow Kleene closure
 \rightarrow RE

\hookrightarrow union

\hookrightarrow Concatenation

Note:
Square brackets
 \downarrow

Take only single character

3 operations
 \rightarrow one applied

int or float
 $dd^* + dd^* . d^* \rightarrow RE$
 int
 \downarrow
 dd^*

Diff types of token \rightarrow described in terms of RE

$+$
 $++$
 $-$
 $< =$
 $=$
 $!$
 $=$

RE for operators

int
 float
 break
 else
 if
 while

RE for keywords

Lexem \rightarrow Sequence or group of characters

Doing lexical analysis that scanning the string formed \rightarrow token

while returning it classify token.

int a, b

\downarrow
 scanned character by character

Stored whenever delimiter comes & it can be called as token
 & while returning it & classify
 & return.

Lexeme \rightarrow represented as pair

Pair (token type, token/attribute value)

\checkmark used by Lexical Analysis \hookrightarrow Parser uses it

token value/attribute \rightarrow used by many phases

\downarrow
first used by parser (Syntax Phase)

For keyword return as it is

For identifier token value is
location where it is placed in Symbol
~~table~~ table.

For const also where it is placed
in Symbol table that location
operator \rightarrow Each operator, a ~~value~~ ^{number} is
assigned that is taken as
value

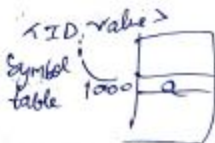
Symbol also ~~value~~ ^{number} is
assigned. So that is taken as value.

In Symbol table only number is assigned to
each symbol, so lexical analysis goes
for fetch value

② Recognize ~~parse~~ token
FA

\rightarrow FSM that consists of
finite set of states &
has finite set of transitions.

(M, L, Q, Σ , δ , q₀, F)



Special kind of
flowchart
consisting of
states as nodes

Describe token in form of Transition diagram

State transition
diagram

transition
as arrows
input symbols
on arrows



$L(A/a)^*$
↓
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- ③ what reaches 2 recognizes as identifier but we need to go back as

Moving backward size of delimiter with help

* The process of taking the look a head pointer moving backwards the size of delimiter in order to recognize exact token is called as Retraction.

When * is there in final state retraction required how much delimiter is there that much size go back.

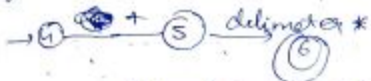
We need to return token

return (ID, install()) ~~to~~ identifier

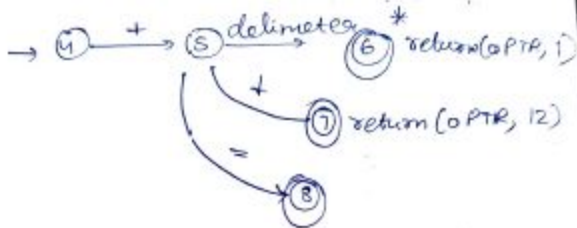
* Install procedure which finds the identifier in symbol table & if found get the address of identifier & return it, otherwise places the identifier

there in symbol table fetches otherwise place & fetch from symbol table

into symbol table & returns its address.

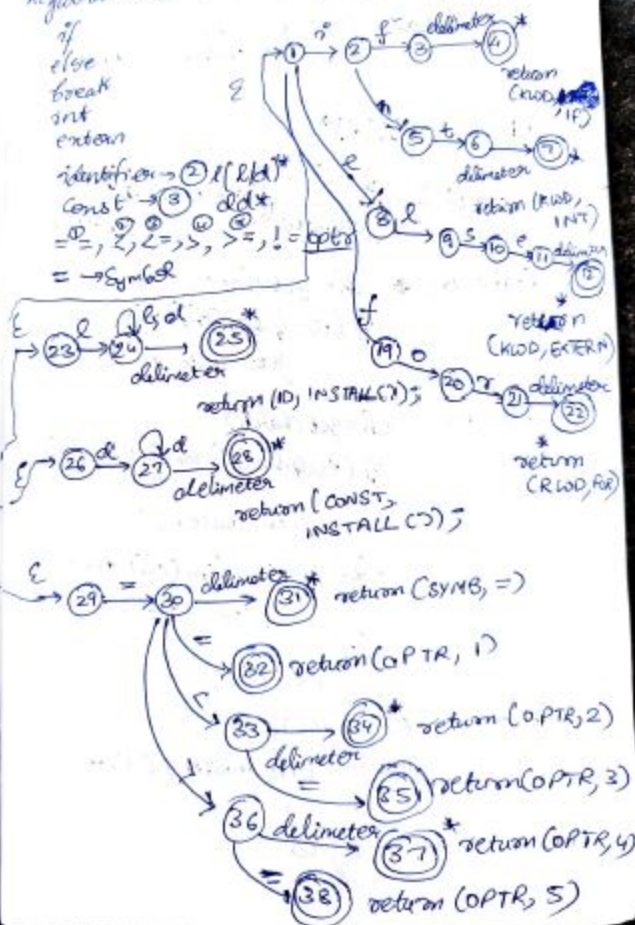


Retract & then return (OPTR, 1)



identifier \rightarrow ② $\{ \text{Id} \}^*$
 const \rightarrow ③ $\{ \text{Id} \}^*$
 = ①, ②, ③, ④, ⑤, ⑥, ⑦, ⑧, ⑨, ⑩, ⑪, ⑫, ⑬, ⑭, ⑮, ⑯, ⑰, ⑱, ⑲, ⑳, ㉑, ㉒, ㉓, ㉔, ㉕, ㉖, ㉗, ㉘, ㉙, ㉚, ㉛, ㉜, ㉝, ㉞, ㉟, ㊱, ㊲, ㊳, ㊴, ㊵, ㊶, ㊷, ㊸, ㊹, ㊺, ㊻, ㊼, ㊽, ㊾, ㊿, !
 = \rightarrow Symbol

scanning statement
as:



$E \rightarrow NFA \rightarrow DPA \rightarrow \text{minimize DPA}$
 leads to redundancies
 so we will minimize DPA

$ID \rightarrow (l|d)^*$



State 0: $ch = \text{getchar}()$
 if (isalpha(ch)) then
 goto state 1;

State 1: $ch = \text{getchar}()$
 if (isalpha(ch) or isdigit(ch)) then
 goto state 1;

else if (isdelim(ch)) then
 goto state 2;

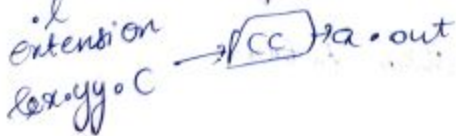
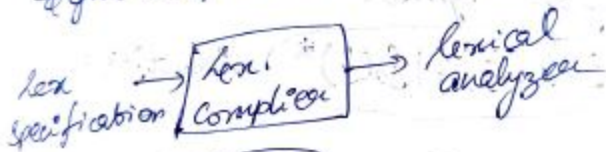
else
 error();

State 2: RETRACT();
 return (ID, INSTALL());

First Compiler $\rightarrow 20 \text{ yrs}$

Lex Tool

It is used to generate lexical analyzer.
For lex compiler give input lex specification
& give output lexical analyzer



Lex specification → Declaration section → Auxiliary declaration
 Auxiliary Transition rules → Regular expression definition or declaration
 function → Diff function used by Transition rules

Auxiliary declaration → Just like declaration which going to use in C

Reg. exp declaration

% [a..zA..Z]

↓
stands for only one symbol

% 5 int a = 10;
% 3

↓
Auxiliary declaration

% [0..9]

↓
Only one symbol

% { { { ({ { { | { { { } } } } } } } } * → identifier

Transition rules are enclosed in double % symbols. Inside we can give pattern followed by action.

% %
Pattern Action.
% %

Whatever defined put in curly braces
what not defined we can give in
[]

% %

```
Global { printf("%s is on it",  
yytext); }
```

Global variable
declared by
lex compiler

% %

Auxiliary functions

↳ Simple C functions

↳ Whatever functions written in
this copied to lex.yy.c

Transition rules

% %

```
Global { printf("welcome");  
printfun(yytext); }
```

% %

~~Global { printf("welcome");
printfun(yytext); }~~

% %

Auxiliary function

```
void printfun(char *str)
```

```
{ printf("%s is identifier", str);  
}
```

Calling lexCompiler
int main()

{

yy lex();

}

→ If pattern matches only
printed & function
called

Declared
Transition
Auxiliary

↓
Specification.