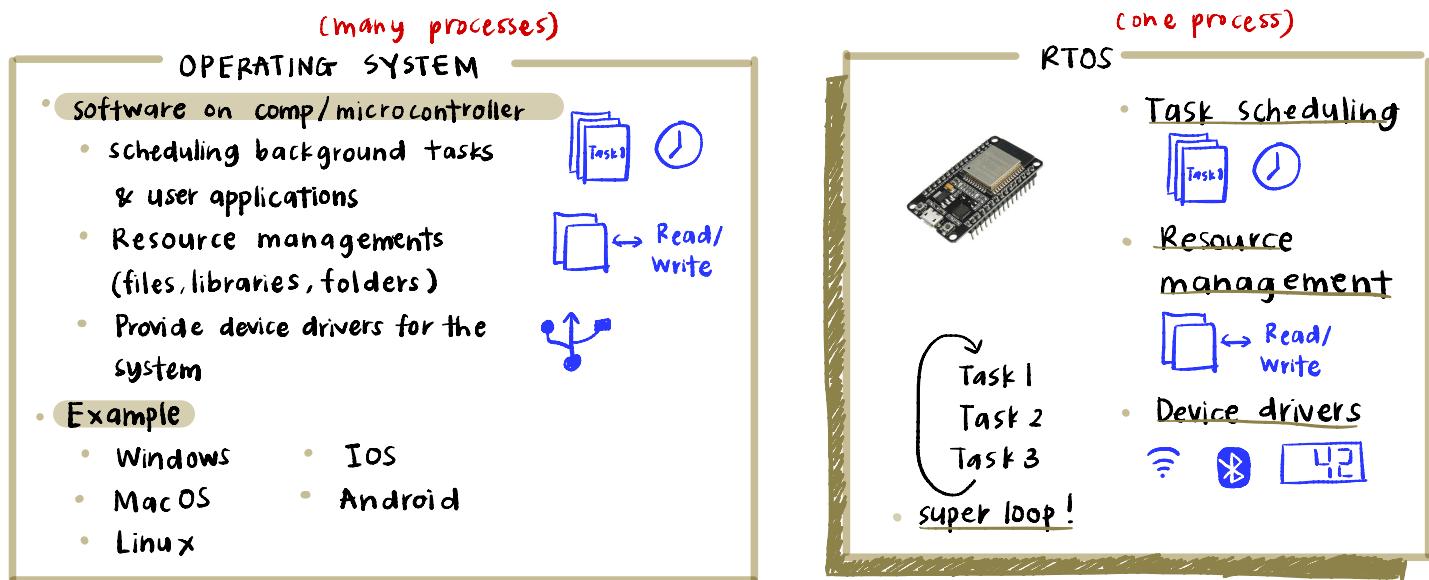
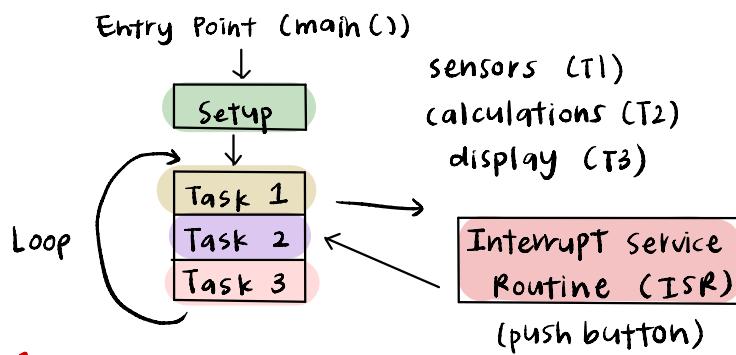


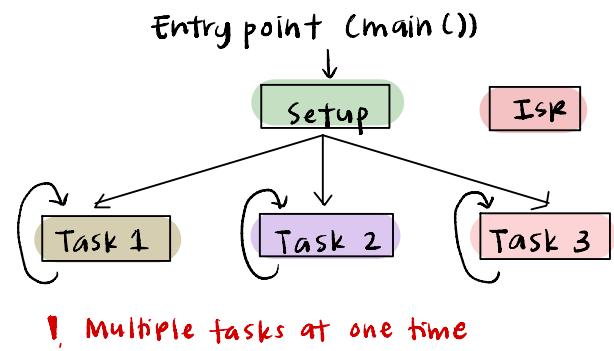
REAL-TIME OPERATING SYSTEM



SUPER LOOP



RTOS



Atmega 328p

- 16 MHz
- 32 kB flash
- 2 kB RAM



STM32L476RG

- 80 MHz
- 1 MB flash
- 128 kB RAM



ESP32

- 240 MHz (dual core)
- 4 MB flash
- 520 kB RAM

Super
Loop

RTOS

ESP 32



- ① Several tasks concurrently
- ② Handling user input
- ③ Reading / Writing SD card
- ④ Controlling hardware

What - is - that

- Interrupt Service Routine
- FPGA

TERMINOLOGY

Task : set of program instructions loaded in memory
Thread : unit of CPU utilization with its own program counter and stack
Process : instance of a computer program