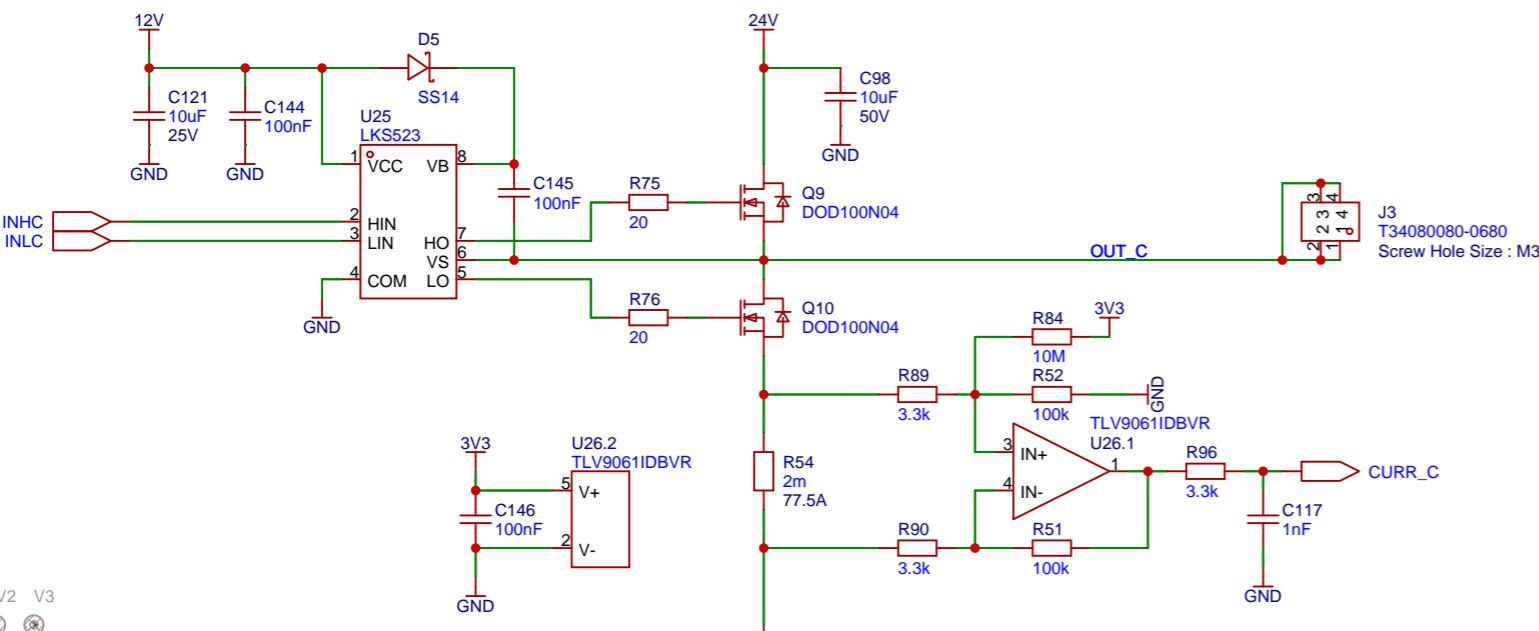
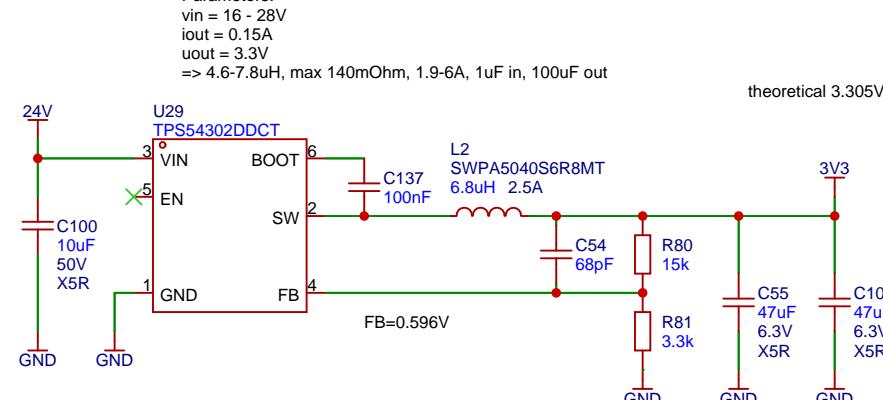
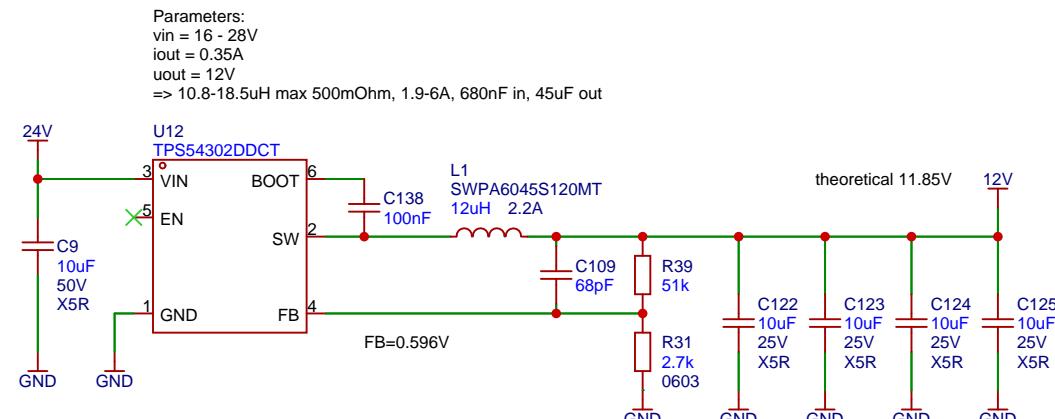
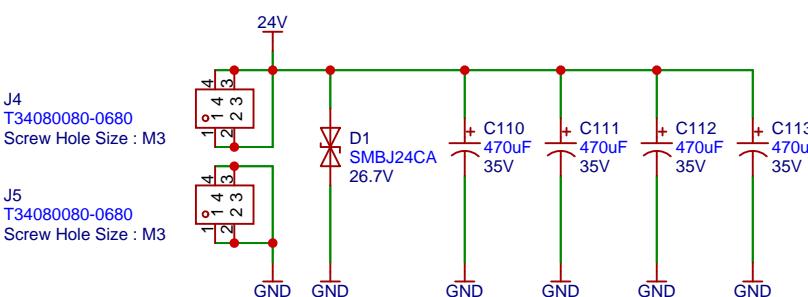


Errata:
 10MOhm (R83) must be 100kOhm
 100kOhm on negative side (R44) must be 47kOhm
 => this lets measure negative freewheeling currents
 when ARR=0 (timer update, all 3 low side FET ON) take
 ADC measurement and get full 3 phase current data in one
 shot.

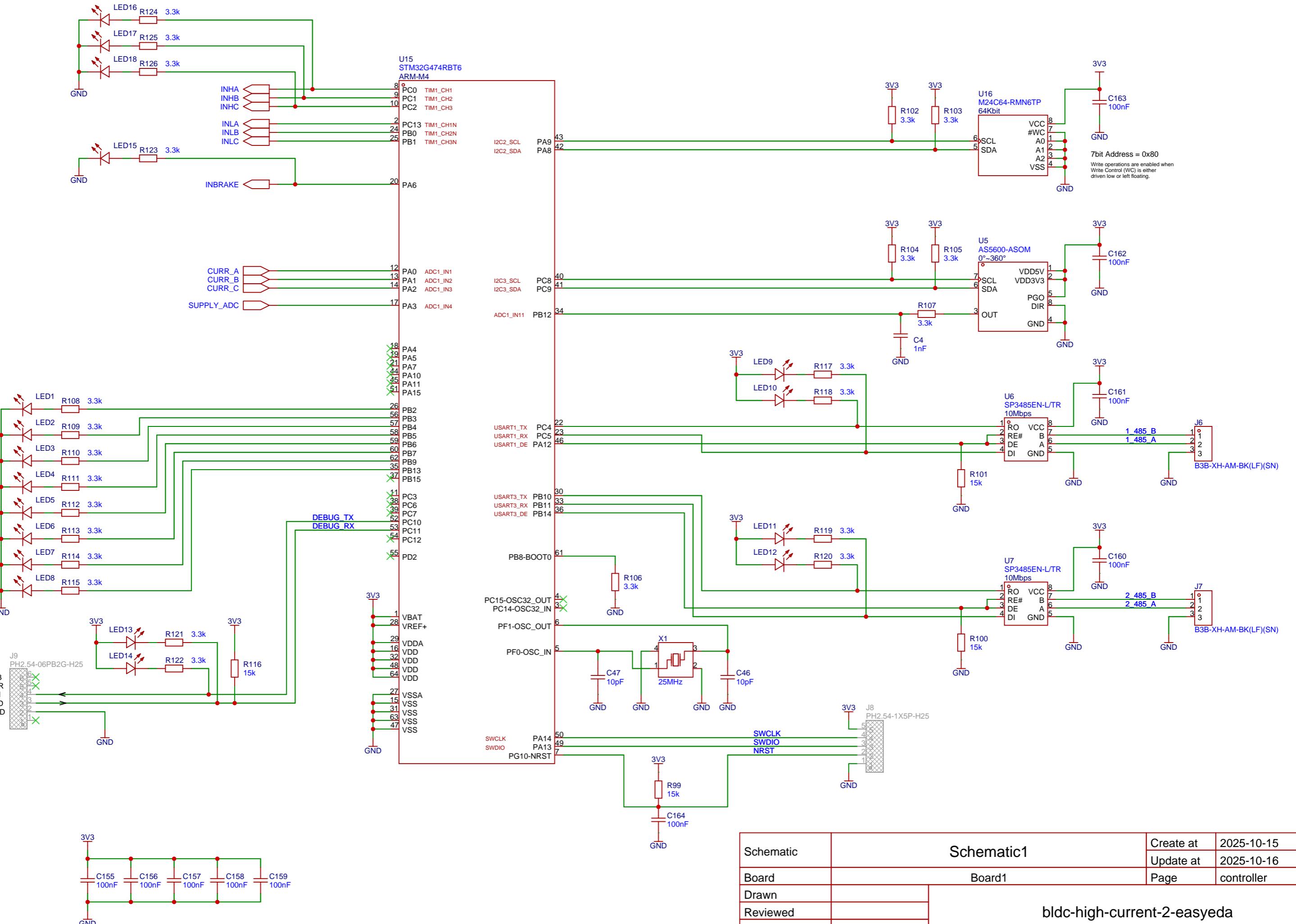


Schematic	Schematic1			Create at	2025-10-15
Board	Board1			Update at	2025-10-24
Drawn				Page	power stage
Reviewed					
				Version	Size
				Page 1 Total 3	
bldc-high-current-2-easyeda			EasyEDA.com	V1.0	A4

24V
26V max
D1 breaks down at 26.7V DC-DC max out at 28V



Schematic	Schematic1			Create at	2025-10-15
Board	Board1			Update at	2025-10-16
Drawn				Page	supply
Reviewed				bldc-high-current-2-easyeda	
	Version	Size	Page 2 Total 3		
		V1.0	A4	EasyEDA.com	



Schematic	Schematic1			Create at	2025-10-15
Board	Board1			Update at	2025-10-16
Drawn				Page	controller
Reviewed					
	Version	Size	Page 3 Total 3		
EasyEDA		V1.0	A4	EasyEDA.com	

bldc-high-current-2-easyeda