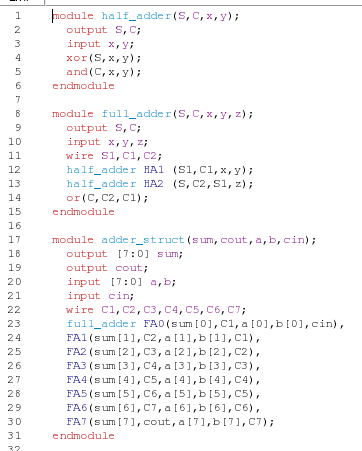
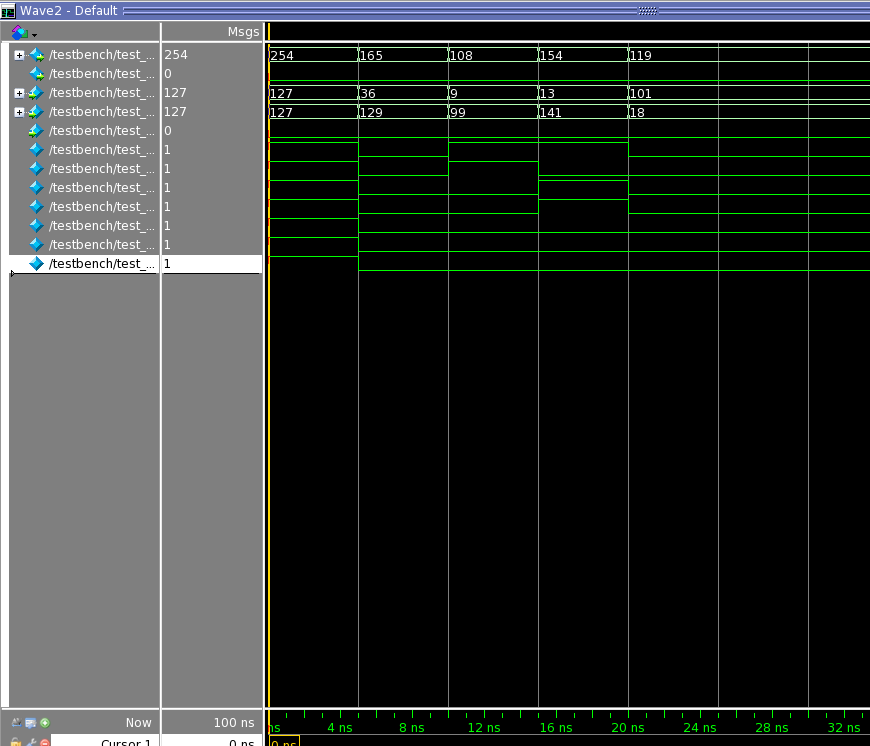
硬體描述語言作業一

B023040018 傅正安

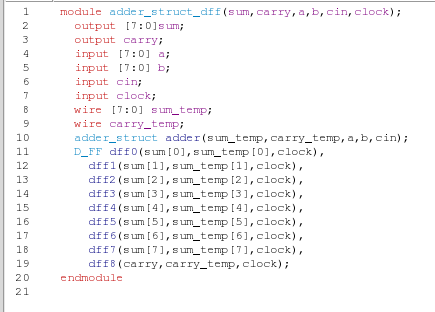
Structure-Level modeling

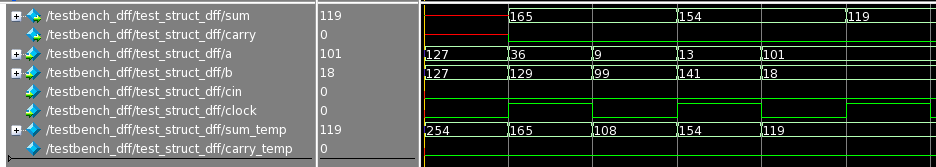


產生的波形圖

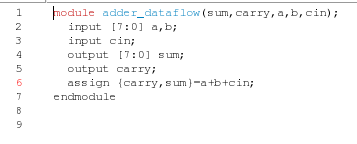


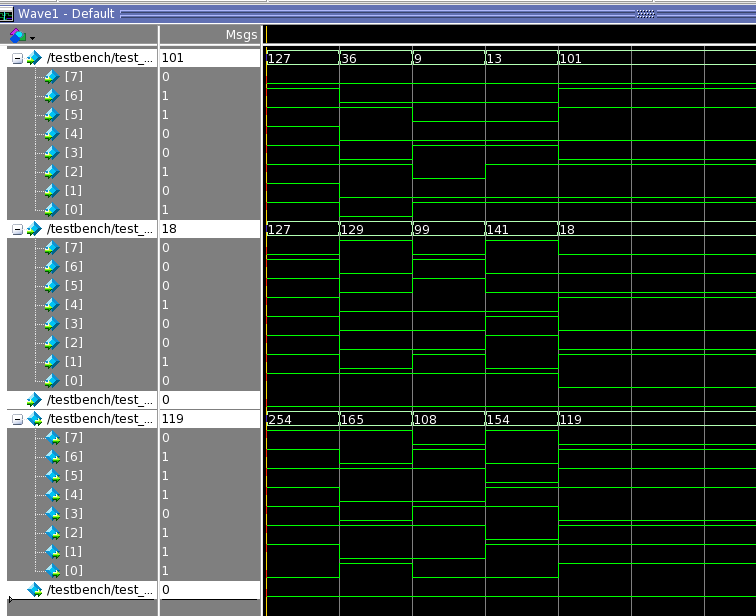
加上D-Flip-Flop

把結果先存到flip flop 裡面，等到clock 為正時產生結果

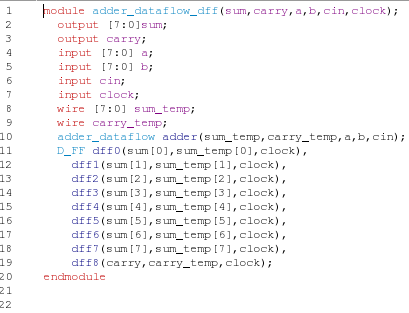


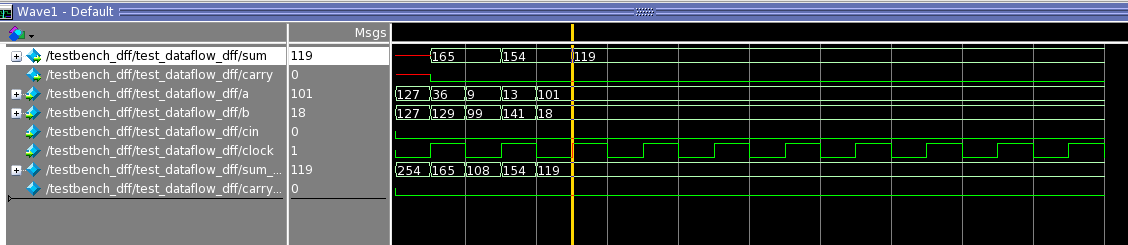
Data flow



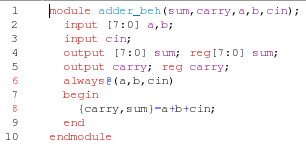


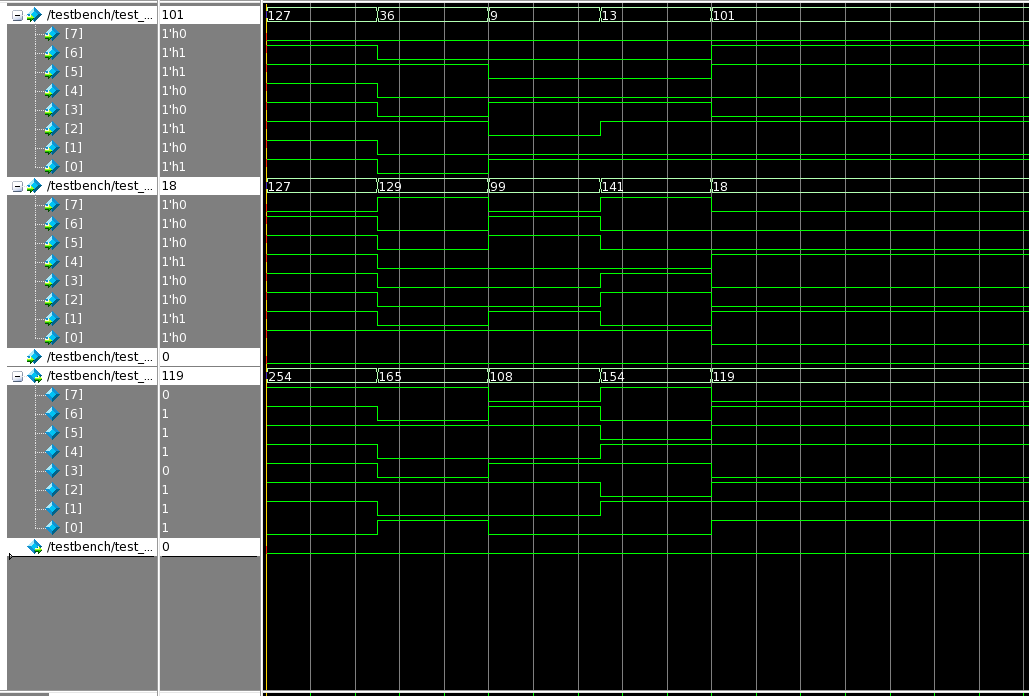
配上DFF



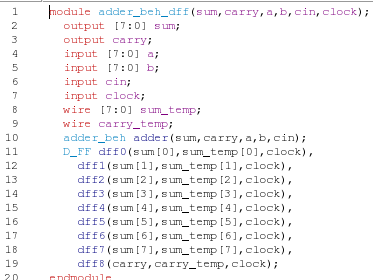


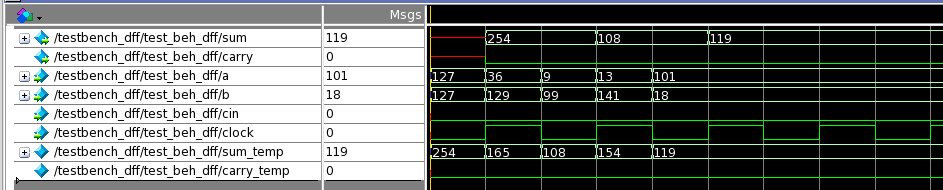
Behavioral modeling



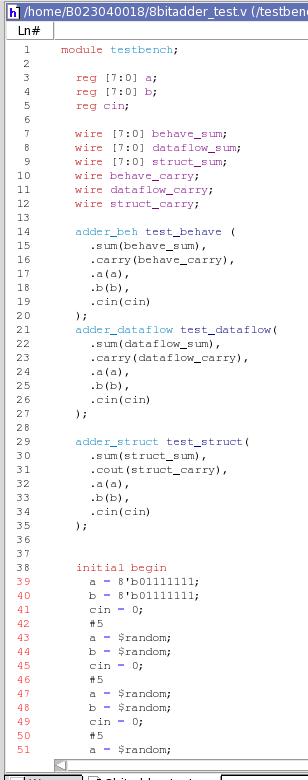


配上DFF





附上Testbench



DFF的testbench

