

A64FX®

PMU Events Errata

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Revision History

Change Date	Edition	Description of Change
11/30/2020	1.0	First Release

Introduction

This document describes the errata information for A64FX PMU Events.

PMU Events Errata

0x0017, L2D_CACHE_REFILL

0x0059, L2D_CACHE_REFILL_PRF

0x0300, L2D_CACHE_REFILL_DM

0x0309, L2_MISS_COUNT

These events count more than they occur actually when the time distance between the demand request and the prefetch request is close.

They can be corrected as follows.

A64FX PMU Event	Correction method
L2D_CACHE_REFILL (corrected)	L2D_CACHE_REFILL - L2D_SWAP_DM - L2D_CACHE_MIBMCH_PRF
L2D_CACHE_REFILL_DM (corrected)	L2D_CACHE_REFILL_DM - L2D_SWAP_DM
L2D_CACHE_REFILL_PRF (corrected)	L2D_CACHE_REFILL_PRF - L2D_CACHE_MIBMCH_PRF
L2_MISS_COUNT (corrected)	L2_MISS_COUNT - L2D_CACHE_SWAP_LOCAL - L2_PIPE_COMP_PF_L2MIB_MCH