

# FUJITSU- MONAKA®

## PMU Events Errata

---

Copyright© 2025 Fujitsu Limited, 4-1-1 Kamikodanaka, Nakahara-ku, Kawasaki, 211-8588, Japan. All rights reserved.

This product and related documentation are protected by copyright and distributed under licenses restricting their use, copying, distribution, and decompilation. No part of this product or related documentation may be reproduced in any form by any means without prior written authorization of Fujitsu Limited and its licensors, if any.

The product(s) described in this book may be protected by one or more U.S. patents, foreign patents, or pending applications.

## TRADEMARKS

Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

Fujitsu and the Fujitsu logo are trademarks of Fujitsu Limited.

This publication is provided “as is” without warranty of any kind, either express or implied, including, but not limited to, the implied warranties of merchantability, fitness for a particular purpose, or noninfringement.

This publication could include technical inaccuracies or typographical errors. Changes are periodically added to the information herein; these changes will be incorporated in new editions of the publication. Fujitsu Limited may make improvements and/or changes in the product(s) and/or the program(s) described in this publication at any time.

---

# Revision History

---

Change Date	Edition	Description of Change
6/13/2025	1.0	First Release

# Introduction

---

This document describes the errata information for FUJITSU-MONAKA PMU Events.

# PMU Events Errata

---

**0x0037, LL\_CACHE\_MISS\_RD**

**0x400B, L3D\_CACHE\_LMISS\_RD**

**0x0396, L2D\_CACHE\_REFILL\_L3D\_MISS**

**0x039A, L2D\_CACHE\_REFILL\_L3D\_MISS\_PRF**

**0x039B, L2D\_CACHE\_REFILL\_L3D\_MISS\_HWPRF**

**0x039C, L2D\_CACHE\_REFILL\_L3D\_HIT**

**0x03A0, L2D\_CACHE\_REFILL\_L3D\_HIT\_PRF**

**0x03A1, L2D\_CACHE\_REFILL\_L3D\_HIT\_HWPRF**

These events may count inaccurately.