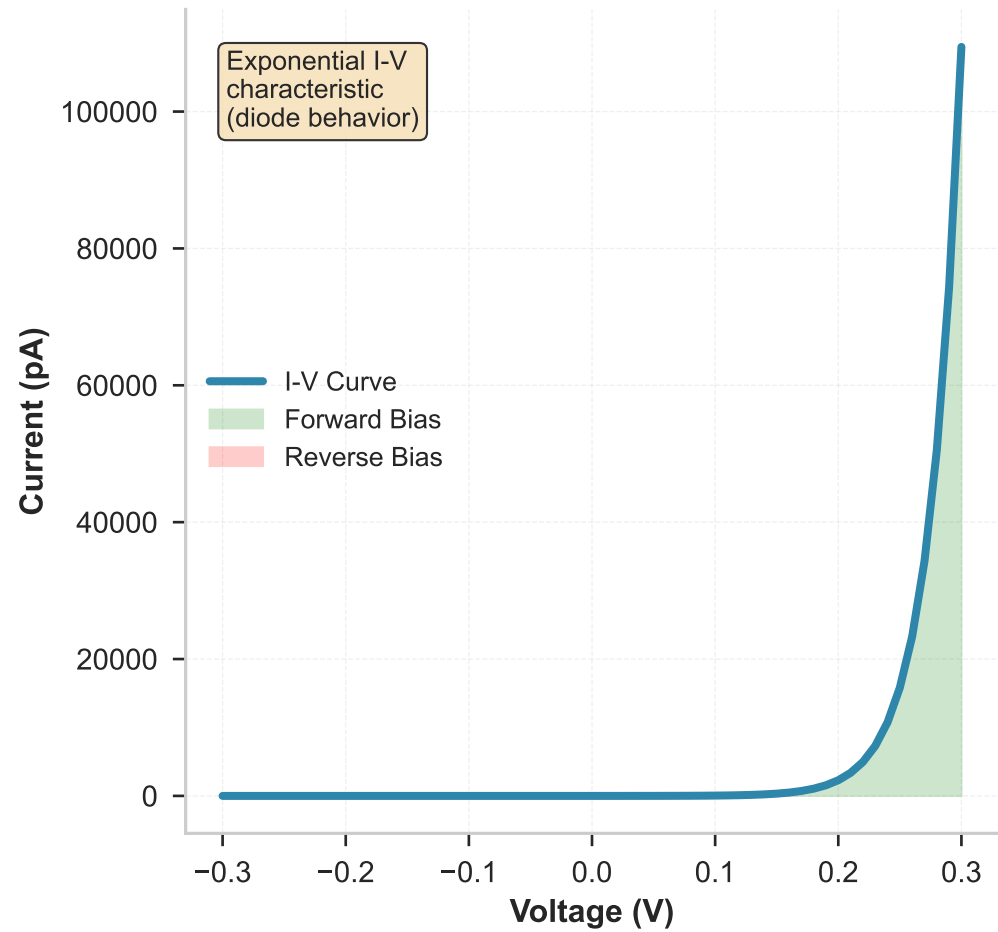
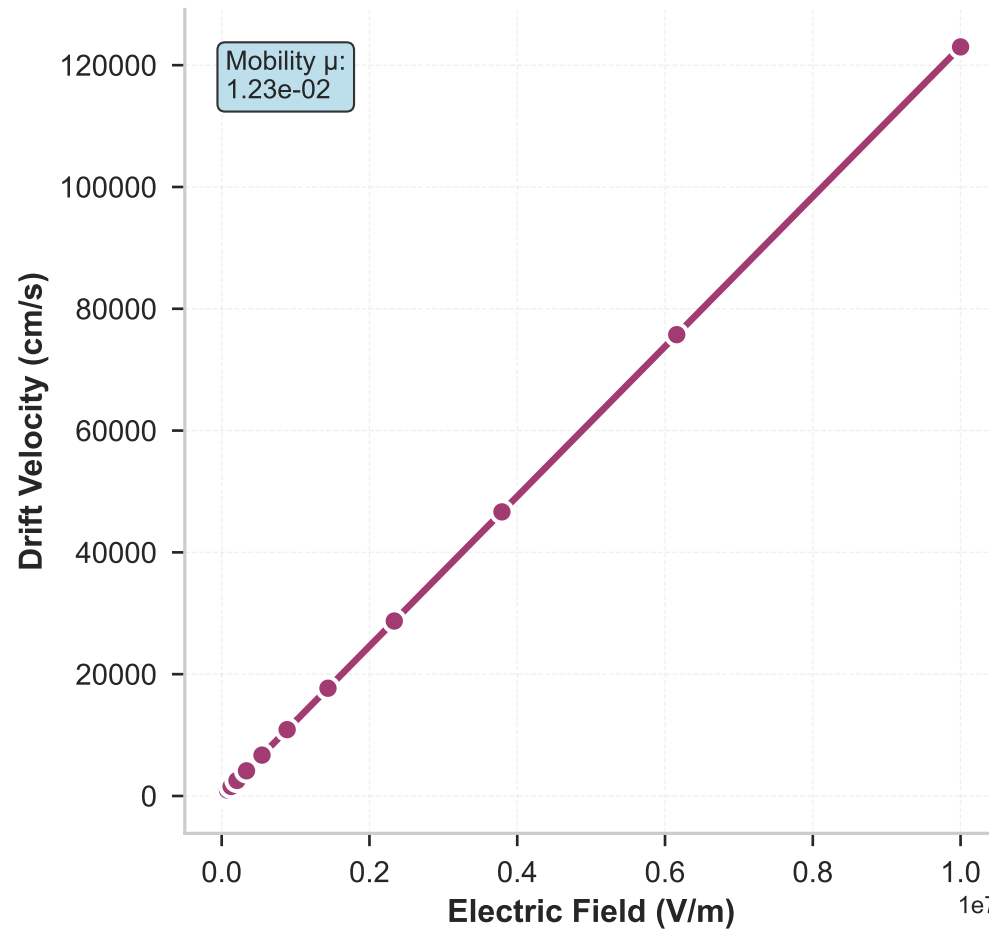


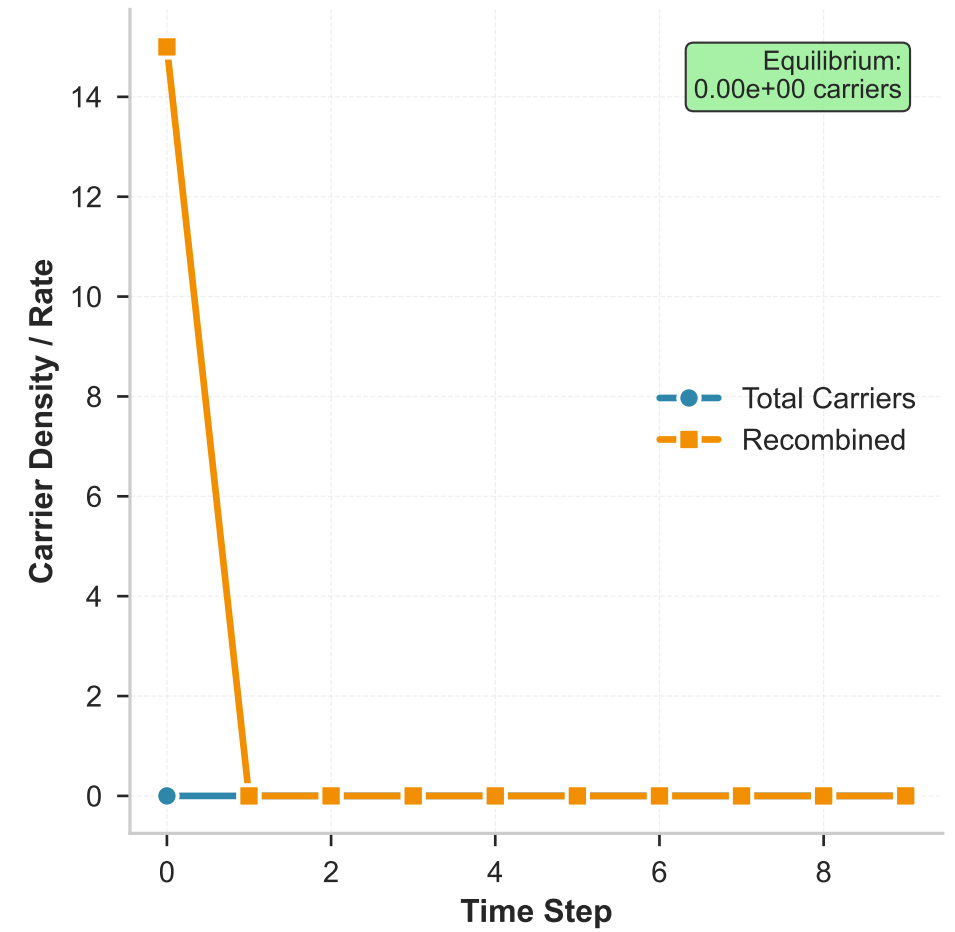
A. I-V Characteristic Curve
(Semiconductor Junction)



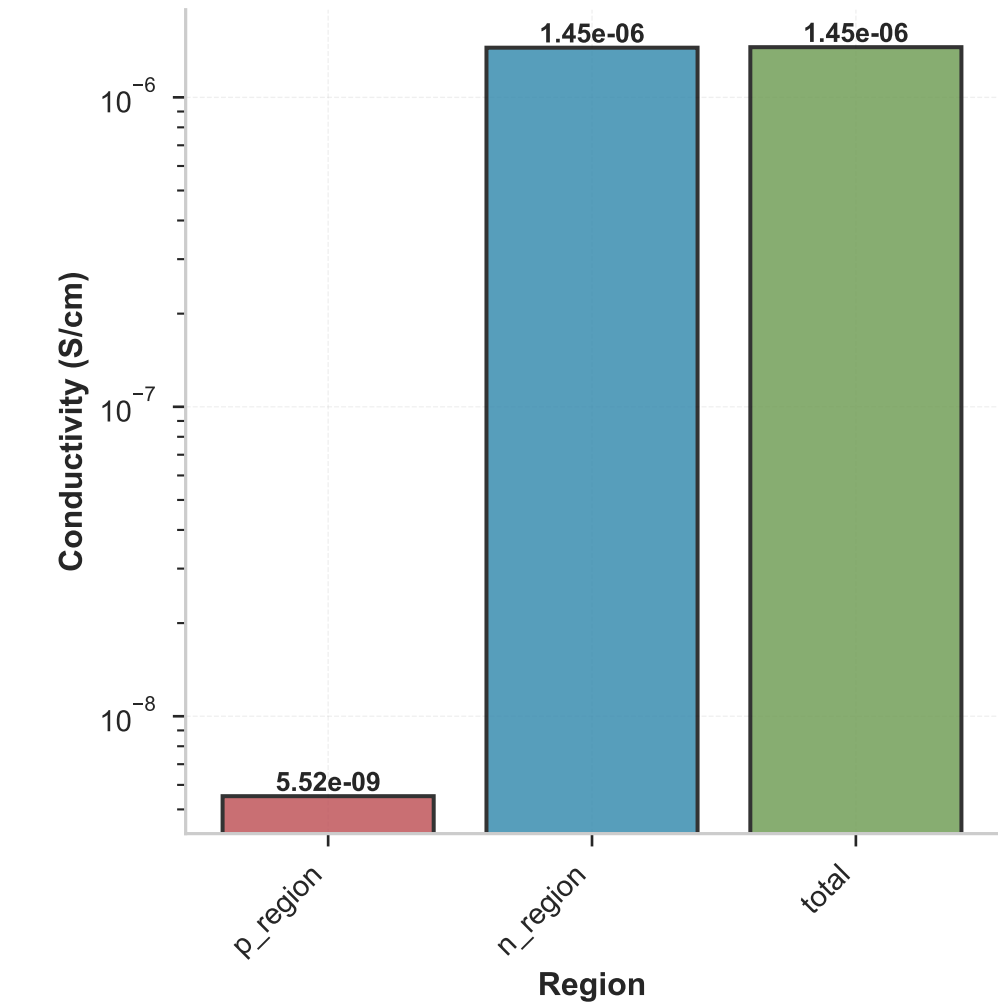
B. Carrier Mobility and Process Validation Results
Semiconductor Physics and Interconnect Dynamics



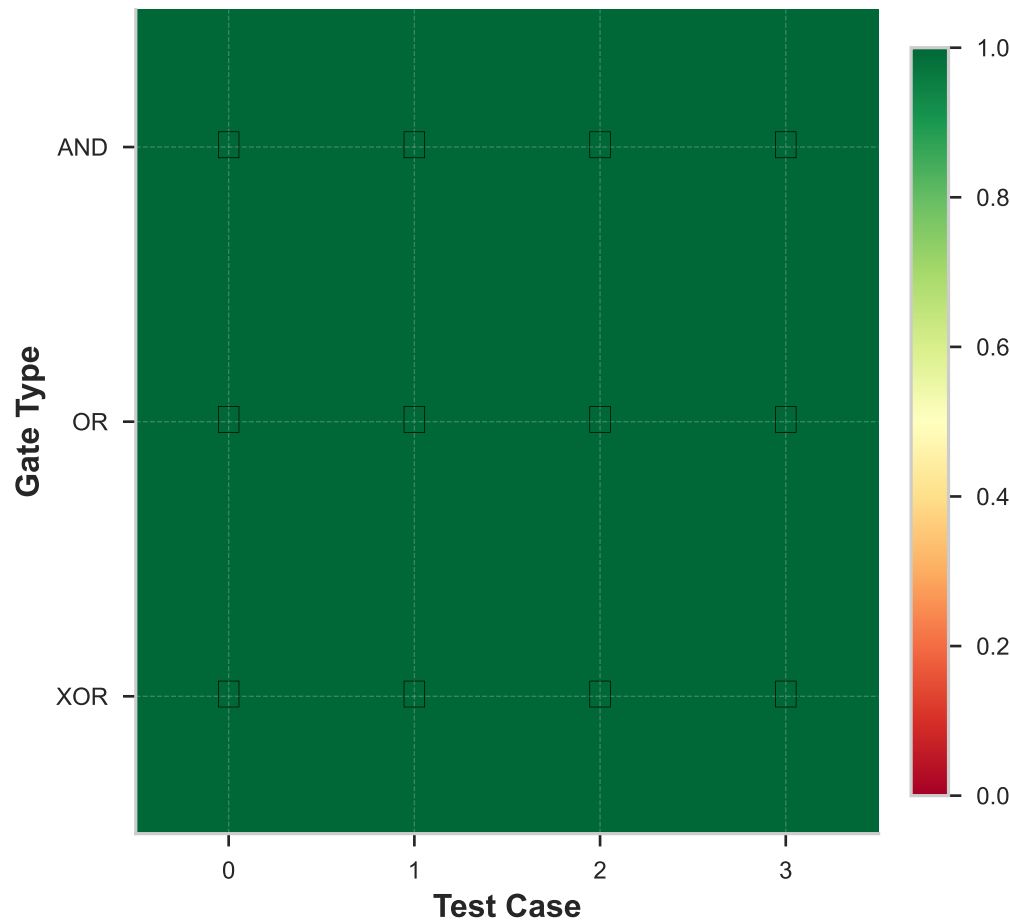
C. Carrier Recombination Dynamics



D. Regional Conductivity
(p-region, n-region, depletion)



E. Logic Gate Verification
(Truth Table)



F. Gear Interconnects
(Frequency Multiplication)

