**Design Rules Verification Report**Filename: W:\GreenfieldTechSolutions\Projects\Ontwerpen\Project\_234\_BAS\PCB\BAS\_ DevKIT\DevKIT.PcbDoc

Warnings 0 Rule Violations 0 Waived Violations 4

## Warnings Total

Rule Violations   Clearance Constraint (Gap=0.1mm) (All), (OnLayer(Top Layer) or OnLayer(Bolton Layer))		
Clearance Constraint (Gap-0.475mm) (InNet(VPWR) or InNet(VIN) or InNet(PWR?), (AII)  Clearance Constraint (Gap-0.155mm) ((InDifferentialPairClass(AII Differential Pairs))), (AII)  Clearance Constraint (Gap-0.15mm) (InNet(NetC104_17) or InNet(TVR_OUT)), (AII)  O Clearance Constraint (Gap-0.15mm) (InNet(TVR_OUT)), (AII) Interential Pairs))), (OnLayer(Top Layer') or O Clearance Constraint (Gap-0.15mm) (InNet(TVR_OUT)), (AII)  O Clearance Constraint (Gap-0.15mm) (InNet(TVR_OUT)) or InNet(TVR_OUT), (AII)  O Un-Routed Net Constraint (AII)  Un-Routed Net Constraint (AII)  O Un-Route	Rule Violations  Classrate Constraint (Con O Impr) (All) (Onlawer/Ten Lawer/Ten Lawer/Deltam Law	0
Clearance Constraint (Gap-0.125mm) ((InDilferential PairClass(All Dilferential Pairs))),(All)		
Clearance Constraint (Gap=0.125mm) (AID, (AII)   O		
Clearance Constraint (Gap=0.15mm) (InNet(NetC104_1') or InNet('1V8_OUT')),(All)  Clearance Constraint (Gap=0.1mm) (All),(All)  Clearance Constraint (Gap=0.3mm) (InNet('1+2V') or InNet('+5VR')),(All)  Clearance Constraint (Gap=0.3mm) (InNet('+12V') or InNet('+5VR')),(All)  Short-Circuit Constraint (Allowed-No) (All),(All)  Un-Routed Net Constraint (Allowed-No) (All),(All)  0  Un-Routed Net Constraint (Allowed-No) (Allo, Allow shelved: No)  Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet('VIN'))  Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=0.254mm) (All)  Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=0.254mm) (All)  Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.254mm) (All)  Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.56mm) (InNet('VIN'))  0  Power Plane Connect Rule(Reile Connect) (Expansion=0.58mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)  Minimum Annutar Ring (Minimum=0.124mm) (All)  Hole To Hole Clearance (Gap=0.125mm) (All),(All)  0  Minimum Solder Mask Silver (Gap=0mm) (All),(All)  0  Silk To Solder Mask (Clearance=0mm) (BPad),(All)  0  Board Clearance Constraint (Gap=0mm) (All),(All)  0  Board Clearance Constraint (Gap=0mm) (All),(All)  0  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay) or OnLayer(Bottom Overlay'))  0  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair (USBST))  Matched Lengths(Tolerance=0.127mm) (I		
Clearance Constraint (Gap=0mm) (AID, (AID)		
Clearance Constraint (Gap=0.1mm) ((InDifferential Pairs'))), (OnLayer(Top Layer') or Occilearance Constraint (Gap=0.3mm) (InNet(*12V) or InNet(*5V) or InNet(*5VR)), (All)		
Clearance Constraint (Gap=0.3mm) (InNet(*12V) or InNet(*5VR)), (AII)		
Short-Circuit Constraint (Allowed=No) (All), (All)		
Un-Routed Net Constraint ((Ail))		
Modified Polygon (Allow modified: No), (Allow shelved: No)	· · · · · · · · · · · · · · · · · · ·	
Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet("VIN"))         0           Width Constraint (Min=0.125mm) (Max=2mm) (Preferred=0.254mm) (AII)         0           Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet("YPWR"))         0           Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.7mm) (InNet("YPWR"))         0           Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.7mm) (InNet("YPWR"))         0           Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)         0           Minimum Annular Ring (Minimum=0.124mm) (AII)         0           Hole Size Constraint (Min=0.025mm) (Max=20mm) (AII)         0           Hole To Hole Clearance (Gap=0.125mm) (AII), (AII)         0           Minimum Solder Mask (Clearance=0mm) (AII), (AII)         0           Silk To Solder Mask (Clearance=0mm) (AII), (AII)         0           Silk to Silk (Clearance=0mm) (AII), (AII)         0           Net Anlennae (Tolerance=0mm) (AII), (AII)         0           Board Clearance Constraint (Gap=0mm) (AII)         0           Board Clearance Constraint (Gap=0mm) (InDifferentialPair (Class(*HDMI'))         0           Matched Lengths (Tolerance=1.27mm) (InNetClass(*SDT))         0           Matched Lengths (Tolerance=1.27mm) (InNetClass(*SDASCL'))         0           Matched Lengths (Tolerance=0.127mm) (InNetClass(*SPI'))         <	*****	
Width Constraint (Min=0.125mm) (Max=20mm) (Preferred=0.254mm) (All)         0           Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=0.7mm) (InNet(*12V'))         0           Width Constraint (Min=0.55mm) (Max=2mm) (Preferred=0.7mm) (InNet(*VPWR'))         0           Power Plane Connect Rule(Relief Connect) (Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)         0           Minimum Annular Ring (Minimum=0.124mm) (All)         0           Hole Size Constraint (Min=0.025mm) (Max=20mm) (All)         0           Hole To Hole Clearance (Gap=0.125mm) (Max=20mm) (All), (All)         0           Minimum Solder Mask Sliver (Gap=0mm) (All), (All)         0           Silk To Solder Mask (Clearance=0mm) (IsPad), (All)         0           Silk (Clearance=0mm) (All), (All)         0           Net Antennae (Tolerance=0mm) (All), (All)         0           Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay*) or OnLayer(Bottom Overlay*))         0           Malched Lengths (Tolerance=1.27mm) (InDifferentialPair Class(HDMI*))         0           Malched Lengths(Tolerance=5mm) (InNetClass(SDS))         0           Malched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))         0           Malched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))         0           Malched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))         0           Malched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL')	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet("+12V"))  Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.7mm) (InNet("VPWR"))  Power Plane Connect Rule(Relief Connect) (Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)  Minimum Annular Ring (Minimum=0.124mm) (Ail)  Hole Size Constraint (Min=0.025mm) (Max=20mm) (Ail)  Hole To Hole Clearance (Gap=0.125mm) (All), (All)  Minimum Solder Mask Silver (Gap=0mm) (All), (All)  Silk To Solder Mask Silver (Gap=0mm) (All), (All)  Silk To Solder Mask (Clearance=0mm) (IsPad), (All)  O Net Anlennae (Tolerance=0mm) (All), (All)  D O Net Anlennae (Tolerance=0mm) (All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass("HDMI"))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair ("USBF1) Or InDifferentialPair ("USBF1_RX") Or  Matched Lengths(Tolerance=0.127mm) (InNetClass("SDASCL"))  Matched Lengths(Tolerance=0.127mm) (InNetClass("SDASCL"))  Matched Lengths(Tolerance=0.127mm) (InNetClass("USBF1") Or InDifferentialPair ("USBF2_RX") Or  Matched Lengths(Tolerance=0.127mm) (InNetClass("USBF2"))  Matched Lengths(Tolerance=0.127mm) (InNetClass("USBF2"))  Matched Lengths(Tolerance=0.127mm) (InNetClass("USBF2"))  Matched Lengths(Tolerance=0.127mm) (InNetClass("USBF2"))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ("USBF2") Or InDifferentialPair ("USBF2_RX") Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ("USBF2"))  Matched Length	· · · · · · · · · · · · · · · · · · ·	
Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.7mm) (InNet(VPWR'))  Power Plane C onnect Rule(Relief C onnect ) (Expansion=0.508mm) (C onductor Width=0.254mm) (Air Gap=0.254mm)  Minimum Annular Ring (Minimum=0.124mm) (AII)  Hole Size C onstraint (Min=0.025mm) (Max=20mm) (AII)  Hole To Hole C Learance (Gap=0.125mm) (AII),(AII)  Minimum Solder Mask Sliver (Gap=0mm) (AII),(AII)  Silk To Solder Mask (Clearance=0mm) (AII),(AII)  Silk to Silk (Clearance=0mm) (AII),(AII)  Net Antennae (Tolerance=0mm) (AII),(AII)  Board C Learance C constraint (Gap=0mm) (AII)  Board C Learance C constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair Class('HDMI'))  Matched Lengths(Tolerance=0mm) (InDifferentialPair (USBF1) Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0_RX') Or  Metched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0_RX') Or  Metched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0_RX') Or		
Power Plane C onnect Rule(Relief C onnect) (Expansion=0.508mm) (C onductor Width=0.254mm) (Air Gap=0.254mm) 0  Minimum Annular Ring (Minimum=0.124mm) (All) 0  Hole Size C onstraint (Min=0.025mm) (Max=20mm) (All) 0  Hole To Hole Clearance (Gap=0.125mm) (All),(All) 0  Minimum Solder Mask Siliver (Gap=0mm) (All),(All) 0  Silk To Solder Mask (Clearance=0mm) (IsPad),(All) 0  Silk to Silk (Clearance=0mm) (IsPad),(All) 0  Net Antennae (Tolerance=0mm) (All),(All) 0  Board Clearance C onstraint (Gap=0mm) (All) 0  Board Clearance C onstraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay')) 0  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair (USBF1) Or InDifferentialPair ('USBF1_RX') Or 0  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL')) 0  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2) Or InDifferentialPair ('USBF2,RX') Or 0  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2) Or InDifferentialPair ('USBF0,RX') Or 0  Height C onstraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All) 0		
Minimum Annular Ring (Minimum=0.124mm) (AII) Hole Size Constraint (Min=0.025mm) (Max=20mm) (AII) Hole To Hole Clearance (Gap=0.125mm) (AII),(AII)  Minimum Solder Mask Sliver (Gap=0mm) (AII),(AII)  Silk To Solder Mask Sliver (Gap=0mm) (I),(AII)  Silk To Solder Mask (Clearance=0mm) (I),(AII)  Silk to Silk (Clearance=0mm) (AII),(AII)  Net Antennae (Tolerance=0mm) (AII),(AII)  Deard Clearance Constraint (Gap=0mm) (AII)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair Class(HDMI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass(SD))  Matched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass(SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass(SDE))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair (USBF2) Or InDifferentialPair (USBF2,RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class("HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class("HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=12mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=12mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=12mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class("USB7))  Matched Lengths(Tolerance=0.127mm) (		
Hole Size Constraint (Min=0.025mm) (Max=20mm) (All) Hole To Hole Clearance (Gap=0.125mm) (All), (All)  Minimum Solder Mask Silver (Gap=0mm) (All), (All)  Silk To Solder Mask (Clearance=0mm) (IsPad), (All)  Silk to Silk (Clearance=0mm) (IsPad), (All)  Silk to Silk (Clearance=0mm) (All), (All)  Net Antennae (Tolerance=0mm) (All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair Class'(HDMI'))  Matched Lengths(Tolerance=1.27mm) (InNetClass'(SD'))  Matched Lengths(Tolerance=1.27mm) (InNetClass'(SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass'(SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass'(SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass'(SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass'(SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass'(SPI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2,RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2,RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2,RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF0,RX') Or  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0,RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Hole To Hole Clearance (Gap=0.125mm) (All),(All)  Minimum Solder Mask Sliver (Gap=0mm) (All),(All)  Silk To Solder Mask (Clearance=0mm) (IsPad),(All)  Silk to Silk (Clearance=0mm) (All),(All)  Net Antennae (Tolerance=0mm) (All),(All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay) or OnLayer(Bottom Overlay))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass(HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass(SD'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2) Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2) Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0))  Matched Lengths('UsBF0, Or InDifferentialPair ('USBF0, Or InD		
Minimum Solder Mask Sliver (Gap=0mm) (All), (All)  Silk To Solder Mask (Clearance=0mm) (IsPad), (All)  Silk to Silk (Clearance=0mm) (All), (All)  Net Antennae (Tolerance=0mm) (All), (All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLay er(Top Overlay') or OnLay er(Bottom Overlay'))  Matched Lengths (Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))  Matched Lengths (Tolerance=5mm) (InNetClass('SD'))  Matched Lengths (Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths (Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths (Tolerance=0.127mm) (InNetClass('ISC'))  Matched Lengths (Tolerance=0.127mm) (InNetClass('ISC'))  Matched Lengths (Tolerance=0.127mm) (InNetClass('ISC'))  Matched Lengths (Tolerance=12mm) (InDifferentialPair ('USBF2_RX') Or Only in the content of the properties of the properties of the content of the properties of the pro		
Silk To Solder Mask (Clearance=0mm) (IsPad),(All)  Silk to Silk (Clearance=0mm) (All),(All)  Net Antennae (Tolerance=0mm) (All),(All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass('SD'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Silk to Silk (Clearance=Omm) (AII), (AII)  Net Antennae (Tolerance=Omm) (AII)  Board Clearance Constraint (Gap=Omm) (AII)  Board Clearance Constraint (Gap=Omm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPair Class('HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass('SD'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class('USB3'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (AII)		
Net Antennae (Tolerance=0mm) (All)  Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass('SD'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair Class('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Board Clearance Constraint (Gap=0mm) (All)  Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass('SD'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0) Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)	· · · · · · · · · · · · · · · · · · ·	
Board Clearance Constraint (Gap=0mm) (OnLayer(Top Overlay') or OnLayer(Bottom Overlay'))  Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=5mm) (InNetClass('SD'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or  Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Matched Lengths(Tolerance=1.27mm) (InDifferentialPairClass('HDMI'))0Matched Lengths(Tolerance=5mm) (InNetClass('SD'))0Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or0Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))0Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))0Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))0Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or0Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))0Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))0Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or0Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)0		
Matched Lengths(Tolerance=5mm) (InNetClass('SD'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)       0		
Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF1') Or InDifferentialPair ('USBF1_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)       0		
Matched Lengths(Tolerance=0.127mm) (InNetClass('SDASCL'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)       0	-	
Matched Lengths(Tolerance=0.127mm) (InNetClass('I2C'))       0         Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))       0         Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))       0         Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or       0         Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)       0		
Matched Lengths(Tolerance=0.127mm) (InNetClass('SPI'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF2') Or InDifferentialPair ('USBF2_RX') Or  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		-
Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('HDMI'))  Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))  Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or  Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)  0		
Matched Lengths(Tolerance=12mm) (InDifferentialPair ('USBF0') Or InDifferentialPair ('USBF0_RX') Or Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)  0		
Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)		
Total 0		
	Total	0

Waived Violations	
Board Clearance Constraint (Gap=0mm) (All)	4
Total	4

## Board Clearance Constraint (Gap=0mm) (All)

Board Outline Clearance(Outline Edge): (Collision < 0.254mm) Between Board Edge And Pad CO301-H (152mm, 45mm) on Multi-Layer Waived by Jonathan Board Outline Clearance(Outline Edge): (Collision < 0.254mm) Between Board Edge And Pad CO302-H (152mm, 60mm) on Multi-Layer Waived by Jonathan Board Outline Clearance(Outline Edge): (Collision < 0.254mm) Between Board Edge And Pad CO303-H (152mm, 75mm) on Multi-Layer Waived by Jonathan Board Outline Clearance(Outline Edge): (Collision < 0.254mm) Between Board Edge And Pad CO501-H (152mm, 130mm) on Multi-Layer Waived by