Design Rules Verification Report

Filename : W:\GreenfieldTechSolutions\Projects\Ontwerpen\Project_234_BAS\PCB\BAS_

Warnings 0
Rule Violations 500

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.15mm) (InNet('NetC104_1')),(All)	0
Clearance Constraint (Gap=0.1mm) (All),(All)	1
Clearance Constraint (Gap=0mm) (All),(All)	0
Clearance Constraint (Gap=0.2mm) (InNet('+12V') or InNet('+5V')),(All)	0
Clearance Constraint (Gap=0.25mm) (InNet('USB0_5V') or InNet('USB1_5V') or InNet('USB2_5V') or InNet('USB3_5V')	185
Clearance Constraint (Gap=0.1mm) ((InDifferentialPairClass('All Differential Pairs'))),(All)	0
Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('+24V')),(All)	162
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	1
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.127mm) (Max=20mm) (Preferred=0.254mm) (All)	0
Minimum Annular Ring (Minimum=0.1mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=20mm) (All)	0
Hole To Hole Clearance (Gap=0.125mm) (All),(All)	17
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Silk To Solder Mask (Clearance=0mm) (IsPad),(All)	0
Silk to Silk (Clearance=0mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	112
Board Clearance Constraint (Gap=0mm) (All)	1
Board Clearance Constraint (Gap=0mm) (OnLayer('Top Overlay') or OnLayer('Bottom Overlay'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB2_RX1') or InDifferentialPair('USB2_TX1'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB1_RX1') or InDifferentialPair('USB1_TX1'))	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB0') or (InDifferentialPair('USB0_RX') and	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB3_RX1') or InDifferentialPair('USB3_TX1'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB6_RX1') or InDifferentialPair('USB6_TX1'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB5_RX1') or InDifferentialPair('USB5_TX1'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB4_RX1') or InDifferentialPair('USB4_TX1'))	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB7') or (InDifferentialPair('USB7_RX') and	0
Matched Lengths(Tolerance=1.27mm) (InNetClass('SPI'))	2
Matched Lengths(Tolerance=1.27mm) (InNetClass('SPI_IC'))	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB6') or (InDifferentialPair('USB6_RX') and	0
Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))	19
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB4') or (InDifferentialPair('USB4_RX') and	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB5') or (InDifferentialPair('USB5_RX') and	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB3') or (InDifferentialPair('USB3_RX') and	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB7_RX2') or InDifferentialPair('USB7_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB6_RX2') or InDifferentialPair('USB6_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB0_RX1') or InDifferentialPair('USB0_TX1'))	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB1') or (InDifferentialPair('USB1_RX') and	0
Matched Lengths(Tolerance=12.7mm) (InDifferentialPair('USB2') or (InDifferentialPair('USB2_RX') and	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB5_RX2') or InDifferentialPair('USB5_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB1_RX2') or InDifferentialPair('USB1_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB0_RX2') or InDifferentialPair('USB0_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB7_RX1') or InDifferentialPair('USB7_TX1'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB4_RX2') or InDifferentialPair('USB4_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB3_RX2') or InDifferentialPair('USB3_TX2'))	0
Matched Lengths(Tolerance=1.27mm) (InDifferentialPair('USB2_RX2') or InDifferentialPair('USB2_TX2'))	0
Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All)	0
Total	500

Clearance Constraint (Gap=0.1mm) (All),(All)
Clearance Constraint: (0.075mm < 0.1mm) Between Track (267.687mm,96.925mm)(280.937mm,96.925mm) on Top Layer And Via (280.2mm,97.225mm)

Clearance Constraint (Gap=0.25mm) (InNet('USB0_5V') or InNet('USB1_5V') or InNet('USB2_5V') or InNet('USB3_5V') o
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(185.2mm,104.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(185.2mm,107.75mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track (190.55mm,96.5mm)(190.55mm,104.5mm)
Clearance Constraint: (0.108mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.135mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Track (191.65mm,106mm)(192.175mm,106mm)
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Via (190.55mm,104.5mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO101-SH(190.8mm,106mm) on Multi-Layer And Via (190.55mm,107.5mm) from Top Layer to
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO102-24(188.925mm,122.25mm) on Bottom Layer And Pad CO102-25(188.925mm,121.75mm)
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO102-25(188.925mm,121.75mm) on Bottom Layer And Track
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO102-25(188.925mm,121.75mm) on Bottom Layer And Track
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO102-28(188.925mm,120.25mm) on Bottom Layer And Pad CO102-29(188.925mm,119.75mm)
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO102-8(187.075mm,121.75mm) on Bottom Layer And Track
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO102-8(187.075mm,121.75mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO102-8(187.075mm,121.75mm) on Bottom Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Via (186.05mm,119.75mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,119.25mm) on Multi-Layer And Via (186.05mm,119.75mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Track
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Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Via (186.05mm,122.25mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO102-SH(185.2mm,122.75mm) on Multi-Layer And Via (186.05mm,122.25mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track

Clearance Constraint (Gap=0.25mm) (InNet('USB0_5V') or InNet('USB1_5V') or InNet('USB2_5V') or InNet('USB3_5V') or
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Via (197.05mm,119.75mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,119.25mm) on Multi-Layer And Via (197.05mm,119.75mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Track
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Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Via (197.05mm,122.25mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO103-SH(196.2mm,122.75mm) on Multi-Layer And Via (197.05mm,122.25mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(196.2mm,104.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(196.2mm,107.75mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.108mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.135mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Via (201.55mm,104.5mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO104-SH(201.8mm,106mm) on Multi-Layer And Via (201.55mm,107.5mm) from Top Layer to
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO105-28(211.925mm,120.25mm) on Bottom Layer And Pad CO105-29(211.925mm,119.75mm)
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO105-4(210.075mm,119.75mm) on Bottom Layer And Pad CO105-5(210.075mm,120.25mm) on
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO105-5(210.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO105-5(210.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO105-5(210.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Via (209.05mm,119.75mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,119.25mm) on Multi-Layer And Via (209.05mm,119.75mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Track

Clearance Constraint (Gap=0.25mm) (InNet('USB0_5V') or InNet('USB1_5V') or InNet('USB2_5V') or InNet('USB3_5V') or
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Via (209.05mm,122.25mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO105-SH(208.2mm,122.75mm) on Multi-Layer And Via (209.05mm,122.25mm) from Top Layer to
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO106-4(221.075mm,119.75mm) on Bottom Layer And Pad CO106-5(221.075mm,120.25mm) on
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO106-5(221.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO106-5(221.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO106-5(221.075mm,120.25mm) on Bottom Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Track
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Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Via (220.05mm,119.75mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,119.25mm) on Multi-Layer And Via (220.05mm,119.75mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Track
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Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Via (220.05mm,122.25mm) from Layer 3 to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO106-SH(219.2mm,122.75mm) on Multi-Layer And Via (220.05mm,122.25mm) from Top Layer to
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO107-24(211.925mm,107.25mm) on Bottom Layer And Pad CO107-25(211.925mm,106.75mm)
Clearance Constraint: (0.2mm < 0.25mm) Between Pad CO107-25(211.925mm,106.75mm) on Bottom Layer And Track
Clearance Constraint: (0.21mm < 0.25mm) Between Pad CO107-25(211.925mm,106.75mm) on Bottom Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(208.2mm,104.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(208.2mm,107.75mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track (213.55mm,97.3mm)(213.55mm,104.5mm)
Clearance Constraint: (0.108mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.135mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Track

Clearance Constraint (Gap=0.25mm) (InNet('USB0_5V') or InNet('USB1_5V') or InNet('USB2_5V') or InNet('USB3_5V') or
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Via (213.55mm,104.5mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO107-SH(213.8mm,106mm) on Multi-Layer And Via (213.55mm,107.5mm) from Top Layer to
Clearance Constraint: (0.22mm < 0.25mm) Between Pad CO108-28(222.925mm,105.25mm) on Bottom Layer And Pad CO108-29(222.925mm,104.75mm)
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(219.2mm,104.25mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(219.2mm,107.75mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.163mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track (224.55mm,102mm)(224.55mm,104.5mm)
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.108mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.135mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.162mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Track
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Via (224.55mm,104.5mm) from Top Layer to
Clearance Constraint: (0.1mm < 0.25mm) Between Pad CO108-SH(224.8mm,106mm) on Multi-Layer And Via (224.55mm,107.5mm) from Top Layer to

Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('+24V')),(All)
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (248.125mm,100.1mm) (248.4mm,101.25mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (248.125mm,118.75mm) (248.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (248.125mm,118.75mm) (248.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (248.125mm,120.1mm) (248.4mm,121.25mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (248.125mm,98.75mm) (248.4mm,99.9mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.063mm,101.438mm) (251.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.063mm,101.438mm) (251.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (251.063mm,101.438mm) (251.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.063mm,117.963mm) (251.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.063mm,117.963mm) (251.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (251.063mm,117.963mm) (251.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.863mm,101.438mm) (252.138mm,102.038mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (251.863mm,117.962mm) (252.138mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (262.125mm,100.1mm) (262.4mm,101.25mm) on Bottom Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (262.125mm,100.1mm) (262.4mm,101.25mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (262.125mm,118.75mm) (262.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (262.125mm,118.75mm) (262.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (262.125mm,120.1mm) (262.4mm,121.25mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (262.125mm,98.75mm) (262.4mm,99.9mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.063mm,101.437mm) (265.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.063mm,101.437mm) (265.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (265.063mm,101.437mm) (265.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.063mm,117.963mm) (265.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.063mm,117.963mm) (265.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (265.063mm,117.963mm) (265.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.863mm,101.438mm) (266.138mm,102.038mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (265.863mm,117.962mm) (266.138mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (276.125mm,118.75mm) (276.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (276.125mm,118.75mm) (276.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (276.125mm,120.1mm) (276.4mm,121.25mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (276.125mm,98.75mm) (276.4mm,99.9mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.063mm,101.438mm) (279.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.063mm,101.438mm) (279.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (279.063mm,101.438mm) (279.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.063mm,117.963mm) (279.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.063mm,117.963mm) (279.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (279.063mm,117.963mm) (279.338mm,118.563mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.863mm,101.438mm) (280.138mm,102.038mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (279.863mm,117.962mm) (280.138mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (290.125mm,100.1mm) (290.4mm,101.25mm) on Bottom Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (290.125mm,100.1mm) (290.4mm,101.25mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (290.125mm,118.75mm) (290.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.275mm < 0.3mm) Between Area Fill (290.125mm,118.75mm) (290.4mm,119.9mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (290.125mm,120.1mm) (290.4mm,121.25mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (290.125mm,98.75mm) (290.4mm,99.9mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (293.063mm,101.437mm) (293.338mm,102.037mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (293.063mm,117.963mm) (293.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (293.063mm,117.963mm) (293.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.24mm < 0.3mm) Between Area Fill (293.063mm,117.963mm) (293.338mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (293.863mm,101.438mm) (294.138mm,102.038mm) on Bottom Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Area Fill (293.863mm,117.962mm) (294.138mm,118.562mm) on Top Layer And Pad
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-22(290.1mm,120.2mm) on Top Layer And Pad IC10-23(290.1mm,119.8mm) on Top Layer

Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('+24V')),(All)
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-22(290.1mm,120.2mm) on Top Layer And Track (289.5mm,119.8mm)(290.1mm,119.8mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC10-23(290.1mm,119.8mm) on Top Layer And Pad IC10-40(291.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-23(290.1mm,119.8mm) on Top Layer And Track (288.45mm,120.2mm)(290.1mm,120.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC10-24(290.1mm,119.35mm) on Top Layer And Pad IC10-40(291.44mm,120mm) on Top Layer
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC10-25(290.1mm,118.875mm) on Top Layer And Pad IC10-40(291.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-31(292.6mm,118.1mm) on Top Layer And Pad IC10-32(293mm,118.1mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-31(292.6mm,118.1mm) on Top Layer And Track (293mm,117.425mm)(293mm,118.1mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC10-32(293mm,118.1mm) on Top Layer And Pad IC10-39(293.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-32(293mm,118.1mm) on Top Layer And Track (292.6mm,118.1mm)(292.6mm,118.666mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-33(293.4mm,118.1mm) on Top Layer And Pad IC10-34(293.8mm,118.1mm) on Top Layer
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC10-33(293.4mm,118.1mm) on Top Layer And Pad IC10-39(293.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-33(293.4mm,118.1mm) on Top Layer And Track (293.8mm,116.8mm)(293.8mm,118.1mm) on
Clearance Constraint: (0.261mm < 0.3mm) Between Pad IC10-34(293.8mm,118.1mm) on Top Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC10-34(293.8mm,118.1mm) on Top Layer And Track (293.4mm,117.6mm)(293.4mm,118.1mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-22(276.1mm,99.8mm) on Bottom Layer And Pad IC11-23(276.1mm,100.2mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-23(276.1mm,100.2mm) on Bottom Layer And Track (274.841mm,99.8mm)(276.1mm,99.8mm)
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-31(278.6mm,101.9mm) on Bottom Layer And Pad IC11-32(279mm,101.9mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-31(278.6mm,101.9mm) on Bottom Layer And Track (279mm,101.9mm)(279mm,102.5mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC11-32(279mm,101.9mm) on Bottom Layer And Pad IC11-39(279.965mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-32(279mm,101.9mm) on Bottom Layer And Track (278.6mm,101.365mm)(278.6mm,101.9mm)
Clearance Constraint: (0.278mm < 0.3mm) Between Pad IC11-32(279mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-33(279.4mm,101.9mm) on Bottom Layer And Pad IC11-34(279.8mm,101.9mm) on Bottom
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC11-33(279.4mm,101.9mm) on Bottom Layer And Pad IC11-39(279.965mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-33(279.4mm,101.9mm) on Bottom Layer And Track (279.8mm,101.9mm)(279.8mm,103.5mm)
Clearance Constraint: (0.251mm < 0.3mm) Between Pad IC11-34(279.8mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC11-34(279.8mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-22(248.1mm,120.2mm) on Top Layer And Pad IC12-23(248.1mm,119.8mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-22(248.1mm,120.2mm) on Top Layer And Track (247.5mm,119.8mm)(248.1mm,119.8mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC12-23(248.1mm,119.8mm) on Top Layer And Pad IC12-40(249.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-23(248.1mm,119.8mm) on Top Layer And Track (246.45mm,120.2mm)(248.1mm,120.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC12-24(248.1mm,119.35mm) on Top Layer And Pad IC12-40(249.44mm,120mm) on Top Layer
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC12-25(248.1mm,118.875mm) on Top Layer And Pad IC12-40(249.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-31(250.6mm,118.1mm) on Top Layer And Pad IC12-32(251mm,118.1mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-31(250.6mm,118.1mm) on Top Layer And Track (251mm,117.425mm)(251mm,118.1mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC12-32(251mm,118.1mm) on Top Layer And Pad IC12-39(251.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-32(251mm,118.1mm) on Top Layer And Track (250.6mm,118.1mm)(250.6mm,118.666mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-33(251.4mm,118.1mm) on Top Layer And Pad IC12-34(251.8mm,118.1mm) on Top Layer
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC12-33(251.4mm,118.1mm) on Top Layer And Pad IC12-39(251.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-33(251.4mm,118.1mm) on Top Layer And Track (251.8mm,116.8mm)(251.8mm,118.1mm) on
Clearance Constraint: (0.261mm < 0.3mm) Between Pad IC12-34(251.8mm,118.1mm) on Top Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC12-34(251.8mm,118.1mm) on Top Layer And Track (251.4mm,117.6mm)(251.4mm,118.1mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-22(248.1mm,99.8mm) on Bottom Layer And Pad IC13-23(248.1mm,100.2mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-22(248.1mm,99.8mm) on Bottom Layer And Track (247.5mm,100.2mm)(248.1mm,100.2mm)
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-23(248.1mm,100.2mm) on Bottom Layer And Track (246.841mm,99.8mm)(248.1mm,99.8mm)
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-31(250.6mm,101.9mm) on Bottom Layer And Track (251mm,101.9mm)(251mm,102.5mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC13-32(251mm,101.9mm) on Bottom Layer And Pad IC13-39(251.965mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-33(251.4mm,101.9mm) on Bottom Layer And Pad IC13-34(251.8mm,101.9mm) on Bottom
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC13-33(251.4mm,101.9mm) on Bottom Layer And Pad IC13-39(251.965mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-33(251.4mm,101.9mm) on Bottom Layer And Track (251.8mm,101.9mm)(251.8mm,103.5mm)
Clearance Constraint: (0.251mm < 0.3mm) Between Pad IC13-34(251.8mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC13-34(251.8mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.23mm < 0.3mm) Between Pad IC14-1(313.405mm,123.5mm) on Top Layer And Track

Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('+24V')),(All)
Clearance Constraint: (0.23mm < 0.3mm) Between Pad IC14-1(313.405mm,123.5mm) on Top Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-22(290.1mm,99.8mm) on Bottom Layer And Pad IC6-23(290.1mm,100.2mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-22(290.1mm,99.8mm) on Bottom Layer And Track (289.5mm,100.2mm)(290.1mm,100.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC6-23(290.1mm,100.2mm) on Bottom Layer And Pad IC6-40(291.44mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-23(290.1mm,100.2mm) on Bottom Layer And Track (288.841mm,99.8mm)(290.1mm,99.8mm)
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC6-24(290.1mm,100.65mm) on Bottom Layer And Pad IC6-40(291.44mm,100mm) on Bottom
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC6-25(290.1mm,101.125mm) on Bottom Layer And Pad IC6-40(291.44mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-31(292.6mm,101.9mm) on Bottom Layer And Pad IC6-32(293mm,101.9mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-31(292.6mm,101.9mm) on Bottom Layer And Track (293mm,101.9mm)(293mm,102.5mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-32(293mm,101.9mm) on Bottom Layer And Track (292.6mm,101.365mm)(292.6mm,101.9mm)
Clearance Constraint: (0.278mm < 0.3mm) Between Pad IC6-32(293mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC6-33(293.4mm,101.9mm) on Bottom Layer And Track (293.8mm,101.9mm)(293.8mm,103.5mm)
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-22(276.1mm,120.2mm) on Top Layer And Pad IC7-23(276.1mm,119.8mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-22(276.1mm,120.2mm) on Top Layer And Track (275.5mm,119.8mm)(276.1mm,119.8mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC7-23(276.1mm,119.8mm) on Top Layer And Pad IC7-40(277.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-23(276.1mm,119.8mm) on Top Layer And Track (274.45mm,120.2mm)(276.1mm,120.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC7-24(276.1mm,119.35mm) on Top Layer And Pad IC7-40(277.44mm,120mm) on Top Layer
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC7-25(276.1mm,118.875mm) on Top Layer And Pad IC7-40(277.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-31(278.6mm,118.1mm) on Top Layer And Pad IC7-32(279mm,118.1mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-31(278.6mm,118.1mm) on Top Layer And Track (279mm,117.425mm)(279mm,118.1mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC7-32(279mm,118.1mm) on Top Layer And Pad IC7-39(279.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-32(279mm,118.1mm) on Top Layer And Track (278.6mm,118.1mm)(278.6mm,118.666mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-33(279.4mm,118.1mm) on Top Layer And Pad IC7-34(279.8mm,118.1mm) on Top Layer
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC7-33(279.4mm,118.1mm) on Top Layer And Pad IC7-39(279.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-33(279.4mm,118.1mm) on Top Layer And Track (279.8mm,116.8mm)(279.8mm,118.1mm) on
Clearance Constraint: (0.261mm < 0.3mm) Between Pad IC7-34(279.8mm,118.1mm) on Top Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC7-34(279.8mm,118.1mm) on Top Layer And Track (279.4mm,117.6mm)(279.4mm,118.1mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-22(262.1mm,120.2mm) on Top Layer And Pad IC8-23(262.1mm,119.8mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-22(262.1mm,120.2mm) on Top Layer And Track (261.5mm,119.8mm)(262.1mm,119.8mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC8-23(262.1mm,119.8mm) on Top Layer And Pad IC8-40(263.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-23(262.1mm,119.8mm) on Top Layer And Track (260.45mm,120.2mm)(262.1mm,120.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC8-24(262.1mm,119.35mm) on Top Layer And Pad IC8-40(263.44mm,120mm) on Top Layer
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC8-25(262.1mm,118.875mm) on Top Layer And Pad IC8-40(263.44mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-31(264.6mm,118.1mm) on Top Layer And Pad IC8-32(265mm,118.1mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-31(264.6mm,118.1mm) on Top Layer And Track (265mm,117.425mm)(265mm,118.1mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC8-32(265mm,118.1mm) on Top Layer And Pad IC8-39(265.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-32(265mm,118.1mm) on Top Layer And Track (264.6mm,118.1mm)(264.6mm,118.666mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-33(265.4mm,118.1mm) on Top Layer And Pad IC8-34(265.8mm,118.1mm) on Top Layer
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC8-33(265.4mm,118.1mm) on Top Layer And Pad IC8-39(265.965mm,120mm) on Top Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-33(265.4mm,118.1mm) on Top Layer And Track (265.8mm,116.8mm)(265.8mm,118.1mm) on
Clearance Constraint: (0.261mm < 0.3mm) Between Pad IC8-34(265.8mm,118.1mm) on Top Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC8-34(265.8mm,118.1mm) on Top Layer And Track (265.4mm,117.6mm)(265.4mm,118.1mm) on
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-22(262.1mm,99.8mm) on Bottom Layer And Pad IC9-23(262.1mm,100.2mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-22(262.1mm,99.8mm) on Bottom Layer And Track (261.5mm,100.2mm)(262.1mm,100.2mm) on
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC9-23(262.1mm,100.2mm) on Bottom Layer And Pad IC9-40(263.44mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-23(262.1mm,100.2mm) on Bottom Layer And Track (260.841mm,99.8mm)(262.1mm,99.8mm)
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC9-24(262.1mm,100.65mm) on Bottom Layer And Pad IC9-40(263.44mm,100mm) on Bottom
Clearance Constraint: (0.275mm < 0.3mm) Between Pad IC9-25(262.1mm,101.125mm) on Bottom Layer And Pad IC9-40(263.44mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-31(264.6mm,101.9mm) on Bottom Layer And Pad IC9-32(265mm,101.9mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-31(264.6mm,101.9mm) on Bottom Layer And Track (265mm,101.9mm)(265mm,102.5mm) on
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC9-32(265mm,101.9mm) on Bottom Layer And Pad IC9-39(265.965mm,100mm) on Bottom Layer
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-32(265mm,101.9mm) on Bottom Layer And Track (264.6mm,101.365mm)(264.6mm,101.9mm)

Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('+24V')),(All)
Clearance Constraint: (0.278mm < 0.3mm) Between Pad IC9-32(265mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-33(265.4mm,101.9mm) on Bottom Layer And Pad IC9-34(265.8mm,101.9mm) on Bottom Layer
Clearance Constraint: (0.24mm < 0.3mm) Between Pad IC9-33(265.4mm,101.9mm) on Bottom Layer And Pad IC9-39(265.965mm,100mm) on Bottom
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-33(265.4mm,101.9mm) on Bottom Layer And Track (265.8mm,101.9mm)(265.8mm,103.5mm)
Clearance Constraint: (0.251mm < 0.3mm) Between Pad IC9-34(265.8mm,101.9mm) on Bottom Layer And Track
Clearance Constraint: (0.2mm < 0.3mm) Between Pad IC9-34(265.8mm,101.9mm) on Bottom Layer And Track (265.4mm,101.9mm)(265.4mm,102.383mm)

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net GND Between Pad IC11-7(281.4mm,98.1mm) on Bottom Layer And Pad IC11-6(281.9mm,99mm) on Bottom Layer

Hole To Hole Clearance (Gap=0.125mm) (All),(All)
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (186.05mm,119.75mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (186.05mm,122.25mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (197.05mm,119.75mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (197.05mm,122.25mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (203.4mm,213.325mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (203.8mm,213.325mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (206.2mm,213.325mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (206.6mm,213.325mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (208.836mm,181.561mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (209.05mm,119.75mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (209.05mm,122.25mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (212.725mm,185.45mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (215mm,172.929mm) from Top Layer to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (220.05mm,119.75mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (220.05mm,122.25mm) from Layer 3 to Bottom Layer And Via
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (221.2mm,181.6mm) from Top Layer to Bottom Layer And Via (221.2mm,181.6mm)
Hole To Hole Clearance Constraint: (Collision < 0.125mm) Between Via (247.6mm,110mm) from Top Layer to Bottom Layer And Via (247.6mm,110mm)

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (186.5mm,215.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (186.5mm,216.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (186.5mm,217.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (186.5mm,218.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (187.5mm,215.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (187.5mm,216.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (187.5mm,217.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (187.5mm,218.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (188.5mm,215.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (188.5mm,216.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (188.5mm,217.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (188.5mm,218.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.172mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.172mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.5mm,215.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.5mm,216.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.5mm,217.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.5mm,218.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.879mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.879mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (189.879mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (190.586mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (190.586mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (190.586mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (190.586mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (191.293mm,177.172mm) from Top Layer to Bottom Layer
Net Antennae: Via (191.293mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (191.293mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (191.293mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (191.293mm,182.828mm) from Top Layer to Bottom Layer
Net Antennae: Via (192.707mm,177.172mm) from Top Layer to Bottom Layer
Net Antennae: Via (192.707mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (192.707mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (192.707mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (192.707mm,182.828mm) from Top Layer to Bottom Layer
Net Antennae: Via (192mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (192mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (192mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (192mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (193.414mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (193.414mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (193.414mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (193.414mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (194.121mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (194.121mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (194.121mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (194.828mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (194.828mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (202mm,203.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (202mm,204.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (202mm,205.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (202mm,206.5mm) from Top Layer to Bottom Layer

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (203mm,203.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (203mm,204.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (203mm,205.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (203mm,206.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (204mm,203.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (204mm,204.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (204mm,205.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (204mm,206.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (205mm,203.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (205mm,204.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (205mm,205.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (205mm,206.5mm) from Top Layer to Bottom Layer
Net Antennae: Via (212.172mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (212.172mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (212.879mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (212.879mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (212.879mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (213.586mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (213.586mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (213.586mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (213.586mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (214.293mm,177.172mm) from Top Layer to Bottom Layer
Net Antennae: Via (214.293mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (214.293mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (214.293mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (214.293mm,182.828mm) from Top Layer to Bottom Layer
Net Antennae: Via (215.707mm,177.172mm) from Top Layer to Bottom Layer
Net Antennae: Via (215.707mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (215.707mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (215.707mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (215.707mm,182.828mm) from Top Layer to Bottom Layer
Net Antennae: Via (215mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (215mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (215mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (215mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (216.414mm,177.879mm) from Top Layer to Bottom Layer
Net Antennae: Via (216.414mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (216.414mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (216.414mm,182.121mm) from Top Layer to Bottom Layer
Net Antennae: Via (217.121mm,178.586mm) from Top Layer to Bottom Layer
Net Antennae: Via (217.121mm,180mm) from Top Layer to Bottom Layer
Net Antennae: Via (217.121mm,181.414mm) from Top Layer to Bottom Layer
Net Antennae: Via (217.828mm,179.293mm) from Top Layer to Bottom Layer
Net Antennae: Via (217.828mm,180.707mm) from Top Layer to Bottom Layer
Net Antennae: Via (249.44mm,101.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (249.44mm,118.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (249.44mm,121.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (249.44mm,98.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (263.44mm,101.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (263.44mm,118.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (263.44mm,121.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (263.44mm,98.925mm) from Top Layer to Bottom Layer

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Via (277.44mm,101.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (277.44mm,118.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (277.44mm,121.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (277.44mm,98.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (291.44mm,101.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (291.44mm,118.925mm) from Top Layer to Bottom Layer
Net Antennae: Via (291.44mm,121.075mm) from Top Layer to Bottom Layer
Net Antennae: Via (291.44mm,98.925mm) from Top Layer to Bottom Layer

Board Clearance Constraint (Gap=0mm) (All)
Board Outline Clearance(Outline Edge): (0.175mm < 0.254mm) Between Board Edge And Pad C304-2(180.6mm,220.25mm) on Bottom Layer

Matched Lengths(Tolerance=1.27mm) (InNetClass('SPI'))
Matched Net Lengths: Between Net SPI_CE4_X And Net SPI_DI_X Length:169.176mm is not within 1.27mm tolerance of Length:171.21mm (0.763mm
Matched Net Lengths: Between Net SPI_DI_X And Net SPI_DO_X Length:169.176mm is not within 1.27mm tolerance of Length:171.21mm (0.763mm

Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3'))
Matched Net Lengths: Between Net USB0_XTX_N And Net USB0_XTX_P Actual Difference against USB0_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB1_XTX_N And Net USB1_XTX_P Actual Difference against USB1_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB2_XTX_N And Net USB2_XTX_P Actual Difference against USB2_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB3_XTX_N And Net USB3_XTX_P Actual Difference against USB3_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB4_XTX_N And Net USB4_XTX_P Actual Difference against USB4_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB5_XTX_N And Net USB5_XTX_P Actual Difference against USB5_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB6_XTX_N And Net USB6_XTX_P Actual Difference against USB6_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USB7_XTX_N And Net USB7_XTX_P Actual Difference against USB7_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF0_RX1_N And Net USBF0_RX1_P Actual Difference against USBF0_RX1_P is: 0.538mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF0_TX2_N And Net USBF0_TX2_P Actual Difference against USBF0_TX2_P is: 0.29mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF1_RX1_N And Net USBF1_RX1_P Actual Difference against USBF1_RX1_P is: 0.538mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF1_TX2_N And Net USBF1_TX2_P Actual Difference against USBF1_TX2_P is: 0.29mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF1_XTX_N And Net USBF1_XTX_P Actual Difference against USBF1_XTX_P is: 0.207mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF2_RX1_N And Net USBF2_RX1_P Actual Difference against USBF2_RX1_P is: 0.414mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF2_TX2_N And Net USBF2_TX2_P Actual Difference against USBF2_TX2_P is: 0.414mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF3_RX1_N And Net USBF3_RX1_P Actual Difference against USBF3_RX1_P is: 0.538mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBF3_TX2_N And Net USBF3_TX2_P Actual Difference against USBF3_TX2_P is: 0.29mm, Tolerance : 0.127mm.
Matched Net Lengths: Between Net USBG1_XRX_N And Net USBG1_XRX_P Actual Difference against USBG1_XRX_P is: 0.207mm, Tolerance :
Matched Net Lengths: Between Net USBG2_XRX_N And Net USBG2_XRX_P Actual Difference against USBG2_XRX_P is: 0.207mm, Tolerance :