Design Rules Verification ReportFilename: W:\GreenfieldTechSolutions\Projects\Ontwerpen\Project_234_BAS\PCB\BAS_Bas

Warnings 0 Rule Violations 0

| Warnings | |
|----------|---|
| Total | 0 |

| Rule Violations | |
|--|---|
| Clearance Constraint (Gap=0.15mm) (InNet(NetC104_1)),(All) | 0 |
| Clearance Constraint (Gap=0.105mm) (All),(All) | 0 |
| Clearance Constraint (Gap=0mm) (All),(All) | 0 |
| Clearance Constraint (Gap=0.1mm) ((InDifferentialPairClass('All Differential Pairs'))),(All) | |
| Clearance Constraint (Gap=0.3mm) (InNet('VPWR') or InNet('VIN') or InNet('PWR')),(All) | 0 |
| Clearance Constraint (Gap=0.2mm) (InNet('+12V') or InNet('+5V')),(All) | 0 |
| Short-Circuit Constraint (Allowed=No) (All),(All) | 0 |
| Un-Routed Net Constraint ((All)) | 0 |
| Modified Polygon (Allow modified: No), (Allow shelved: No) | 0 |
| Width Constraint (Min=0.1mm) (Max=20mm) (Preferred=0.254mm) (All) | 0 |
| Width Constraint (Min=0.5mm) (Max=2mm) (Preferred=0.7mm) (InNet('VPWR')) | 0 |
| Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet('+12V')) | 0 |
| Width Constraint (Min=0.75mm) (Max=2mm) (Preferred=1mm) (InNet('VIN')) | 0 |
| Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm) | 0 |
| Minimum Annular Ring (Minimum=0.1mm) (All) | 0 |
| Hole Size Constraint (Min=0.025mm) (Max=20mm) (All) | 0 |
| Hole To Hole Clearance (Gap=0.125mm) (All),(All) | 0 |
| Minimum Solder Mask Sliver (Gap=0mm) (All),(All) | 0 |
| Silk To Solder Mask (Clearance=0mm) (IsPad),(All) | 0 |
| Silk to Silk (Clearance=0mm) (AII),(AII) | 0 |
| Net Antennae (Tolerance=0mm) (All) | 0 |
| Board Clearance Constraint (Gap=0mm) (All) | 0 |
| Board Clearance Constraint (Gap=0mm) (OnLayer('Top Overlay') or OnLayer('Bottom Overlay')) | 0 |
| Matched Lengths(Tolerance=0.127mm) (InDifferentialPairClass('USB3')) | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB5') or InDifferentialPair('USB5_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB4') or InDifferentialPair('USB4_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB7') or InDifferentialPair('USB7_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB6') or InDifferentialPair('USB6_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB1') or InDifferentialPair('USB1_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB0') or InDifferentialPair('USB0_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB3') or InDifferentialPair('USB3_RX1') or | 0 |
| Matched Lengths(Tolerance=6mm) (InDifferentialPair('USB2') or InDifferentialPair('USB2_RX1') or | 0 |
| Height Constraint (Min=0mm) (Max=100mm) (Prefered=12.7mm) (All) | 0 |
| Total | 0 |