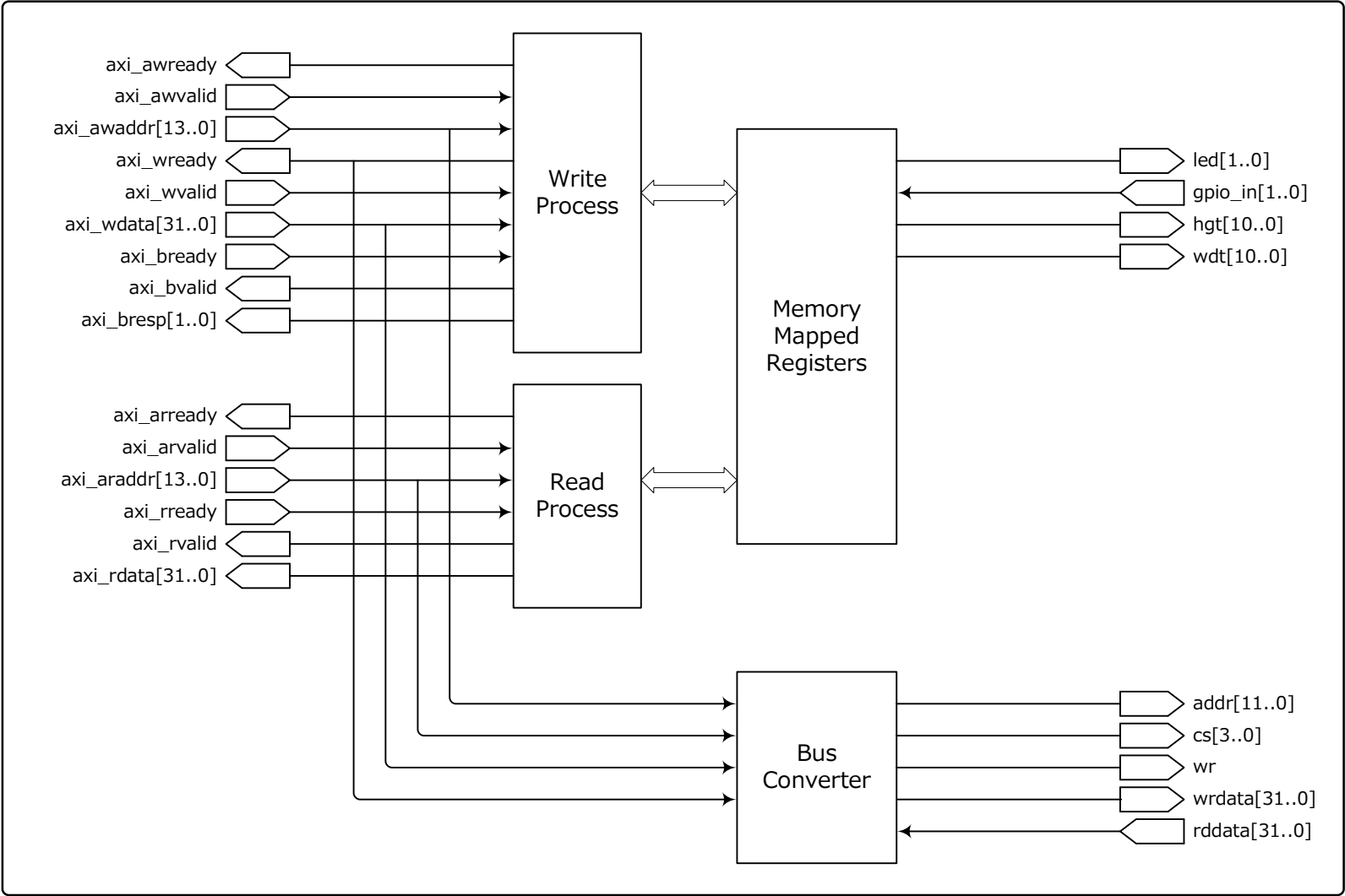


axi\_if

AXI Interface  
This module bridges AXI interface and internal interface. The internal interface is a simple SRAM-like interface.

Nov.24.2020

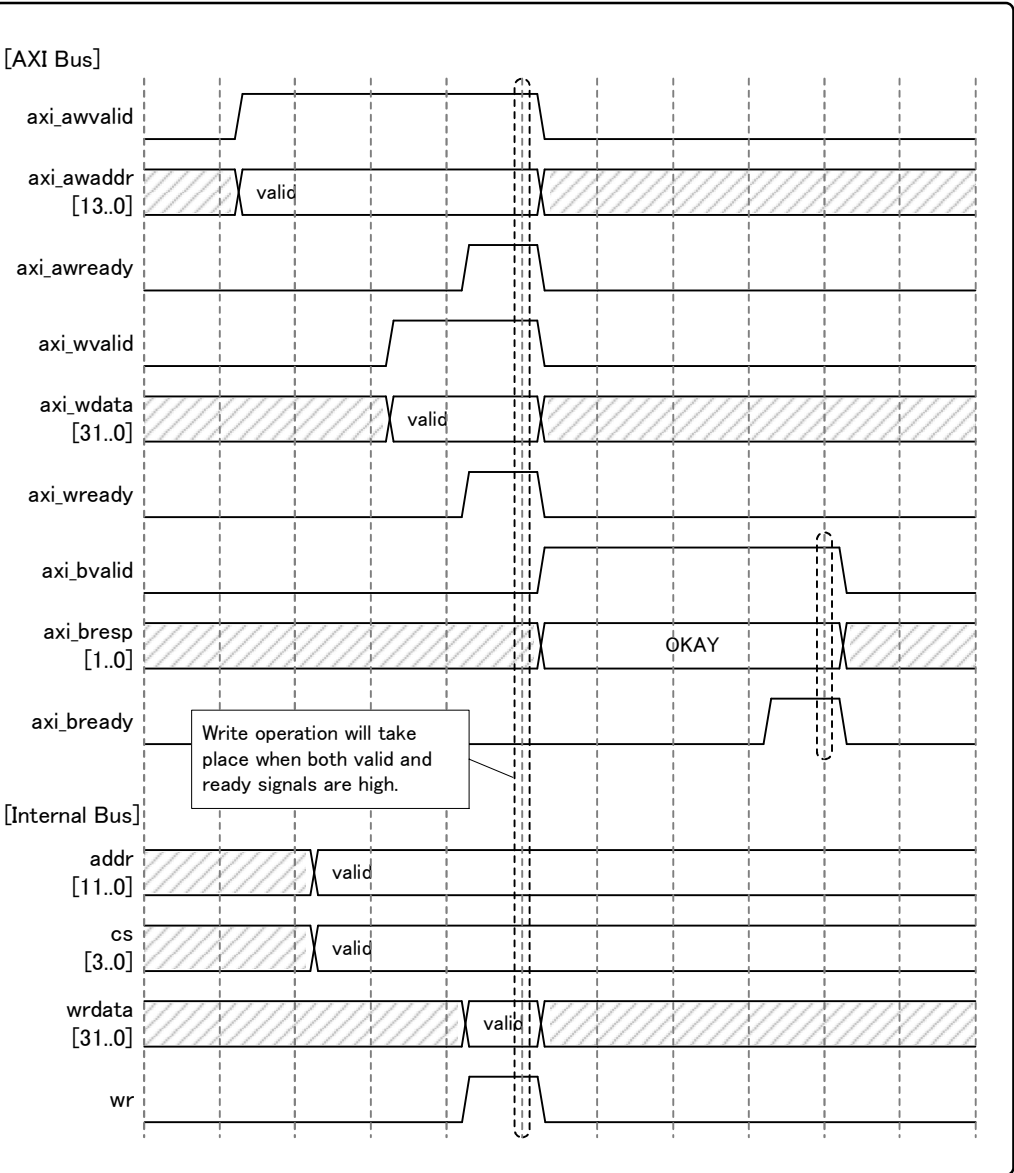
Block diagram



Memory Space Mapping

0000 - 0FFF	cs[0]
1000 - 1FFF	cs[1]
2000 - 2FFF	cs[2]
3000 - 3FFF	cs[3]

AXI Register Write



AXI Register Read

