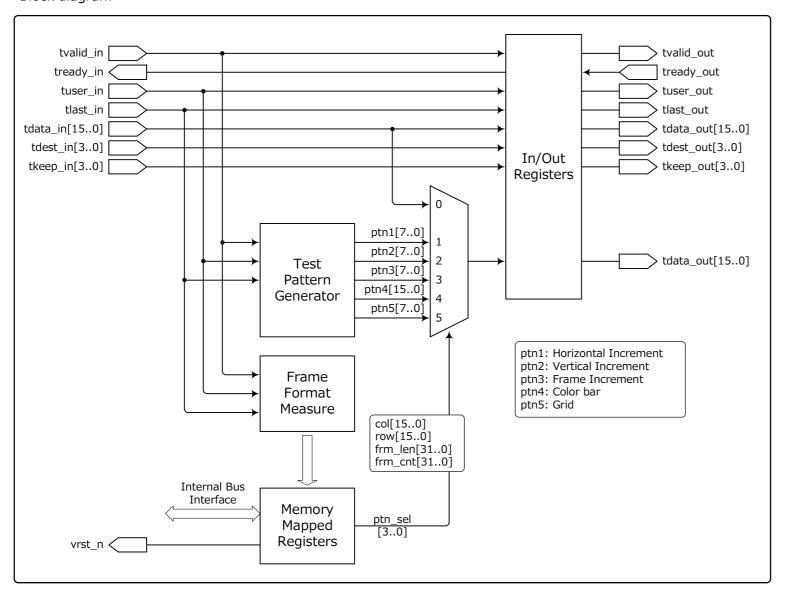
csi_if

CSI Interface

This module is an interface with Xilinx MIPI CSI-2 Receiver subsystem. Another functions include

- + frame format measurement
- + test pattern generation.

Block diagram



Nov.24.2020

Frame Format Measuremnt

