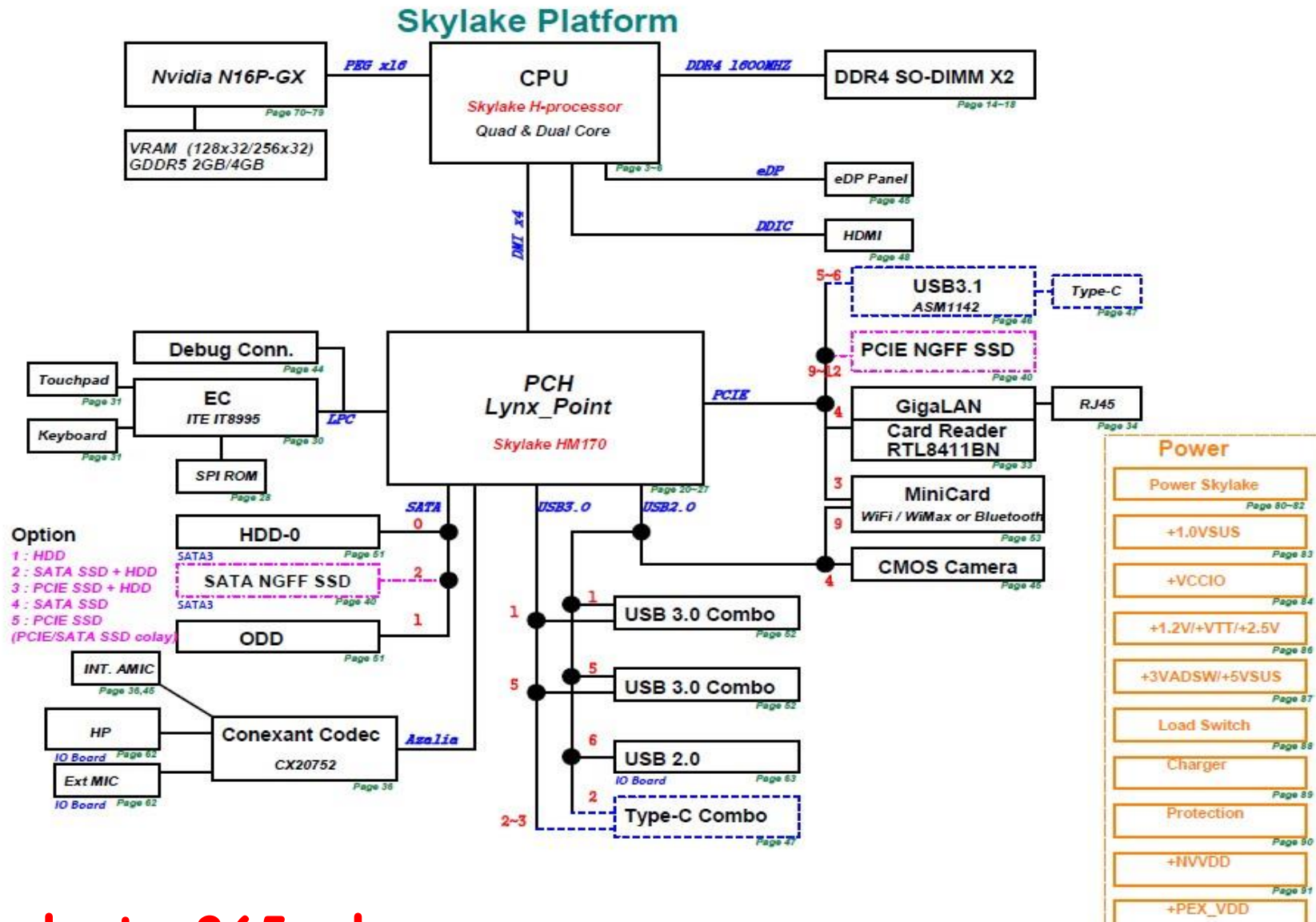
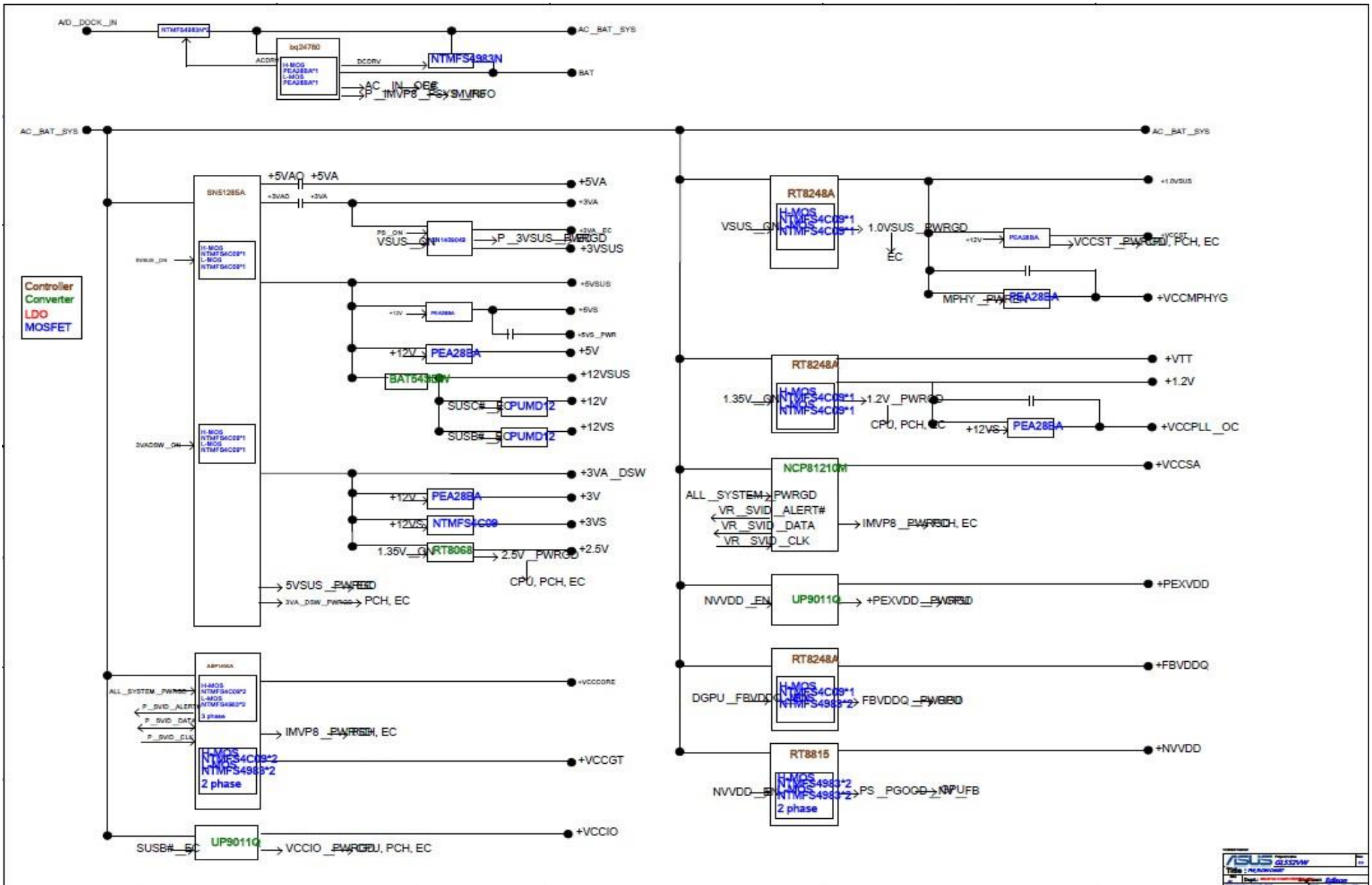


BLOCK DIAGRAM

GL552VW Block Diagram

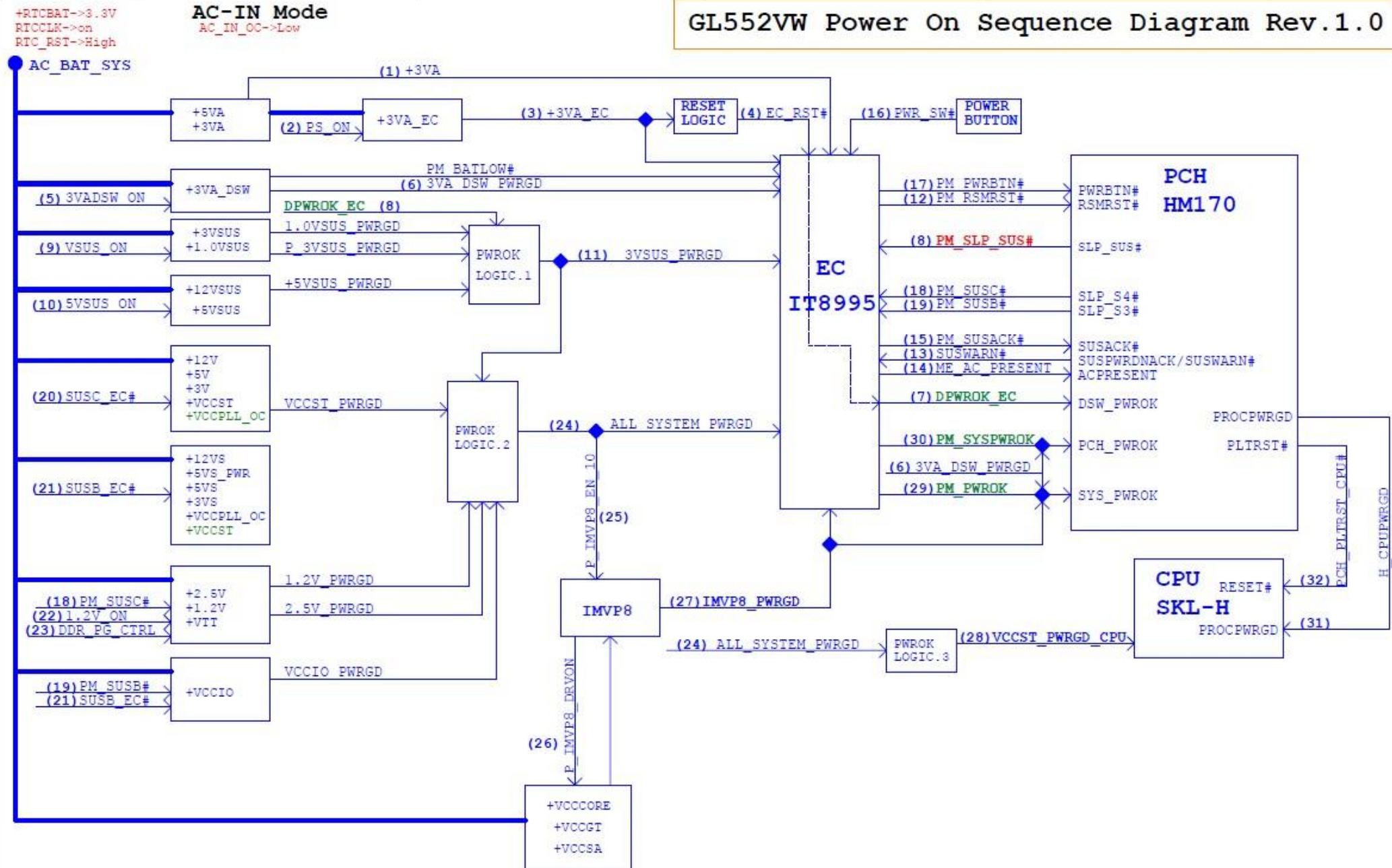


POWER FLOW



POWER ON SEQUENCE

GL552VW Power On Sequence Diagram Rev.1.0



AC_IN POWER ON SEQUENCE

AC-IN Mode

Power Signal

(1) +3VA/+5VA

(2) PS_ON

(3) +3VA_EC

(4) +EC_RST#

(5) +3VA_DSW

(6) 3VA_DSW_PWRGD

(7) DPWRK_EC

(8) PM_SLP_SUS#

(9) +3VSUS/7+1.0VSUS

(10) +12VSUS/+6VSUS

(11) 3VSUS_PWRGD

(12) PM_RSMRST#

(13) SUSWRN#

(14) ME_AC_PRESENT

(15) PCH_SUSACK#

(16) PWR_SW#

(17) PM_PWRBTN#

(18) PM_SUSC#

(19) PM_SUSB#

(20) +12V/+5V/+3V/+VCCST

(21) +12VS/+5VS/+5VS_PWR/+3VS/+VCCPLL_OC

+1.0V_VCCST, VCCPLL

+VCCSTG

+VCCPLL_OC

(22) +1.2V

(23) +2.5V

+VCCIO

DDR_VTT_CTRL

+VTT

(24) ALL_SYSTEM_PWRGD

(25) P_IMVP8_EN_10

(26) P_IMVP8_DRVON

+VCCCORE/+VCCGT/+VCCSA

(27) IMVP8_PWRGD

(28) VCCST_PWRGD_CPU

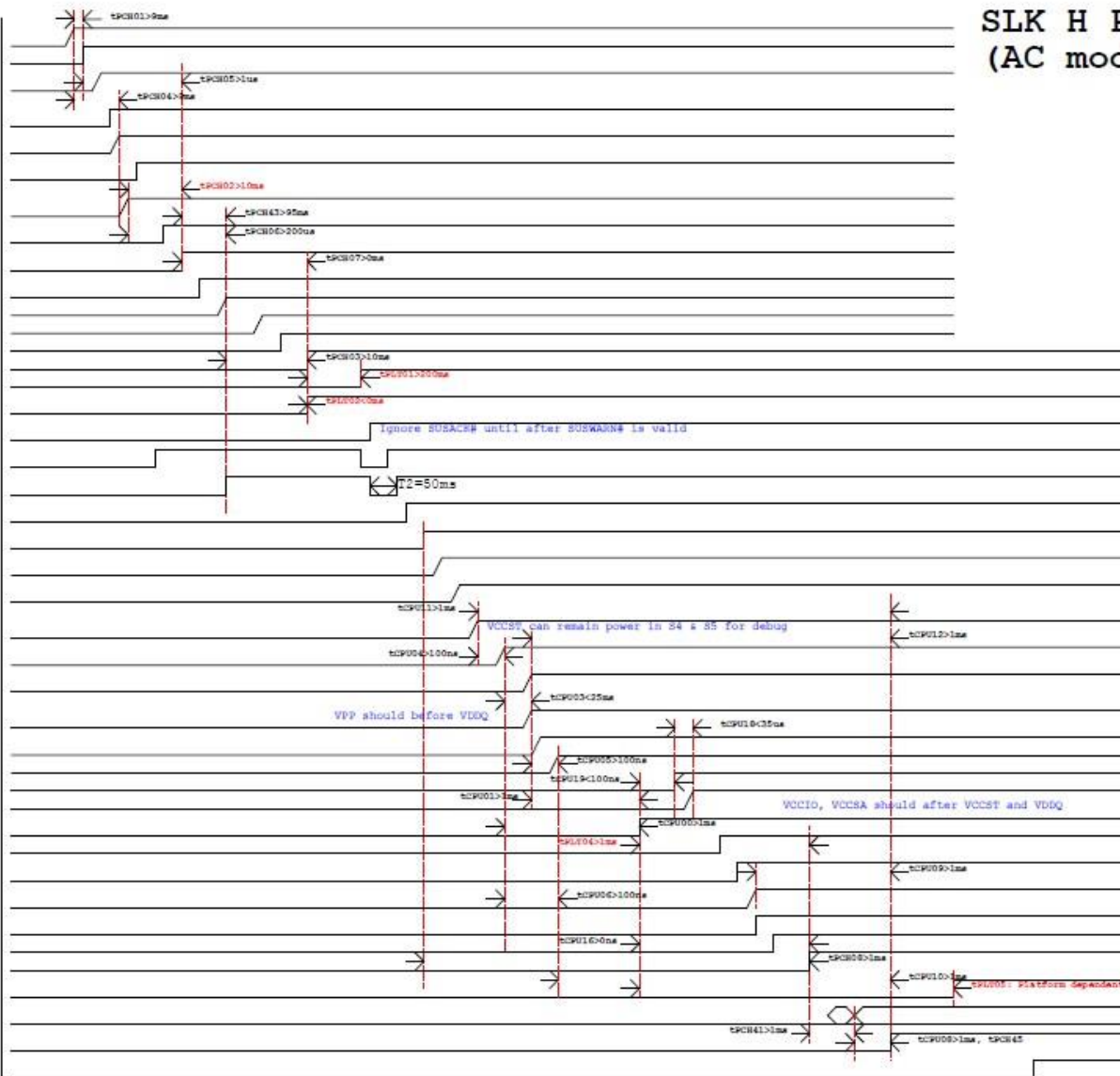
(29) PM_PWRK

(30) PM_SYSPWRK

CLK_PCH_BCLK

(31) H_CPU_PWRGD

(32) PCH_PLTRST_CPU#

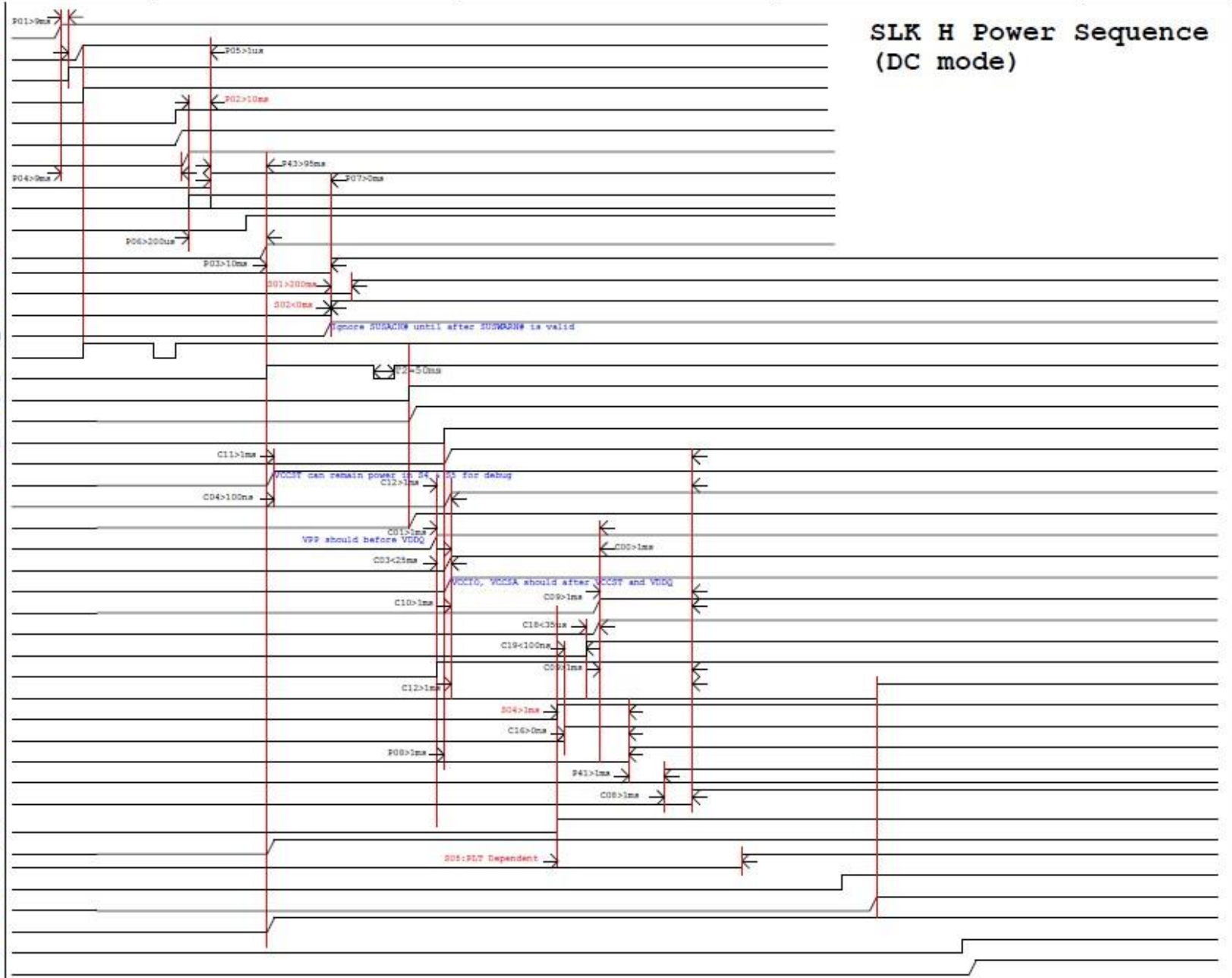


SLK H Power Sequence (AC mode)

DC_IN POWER ON SEQUENCE

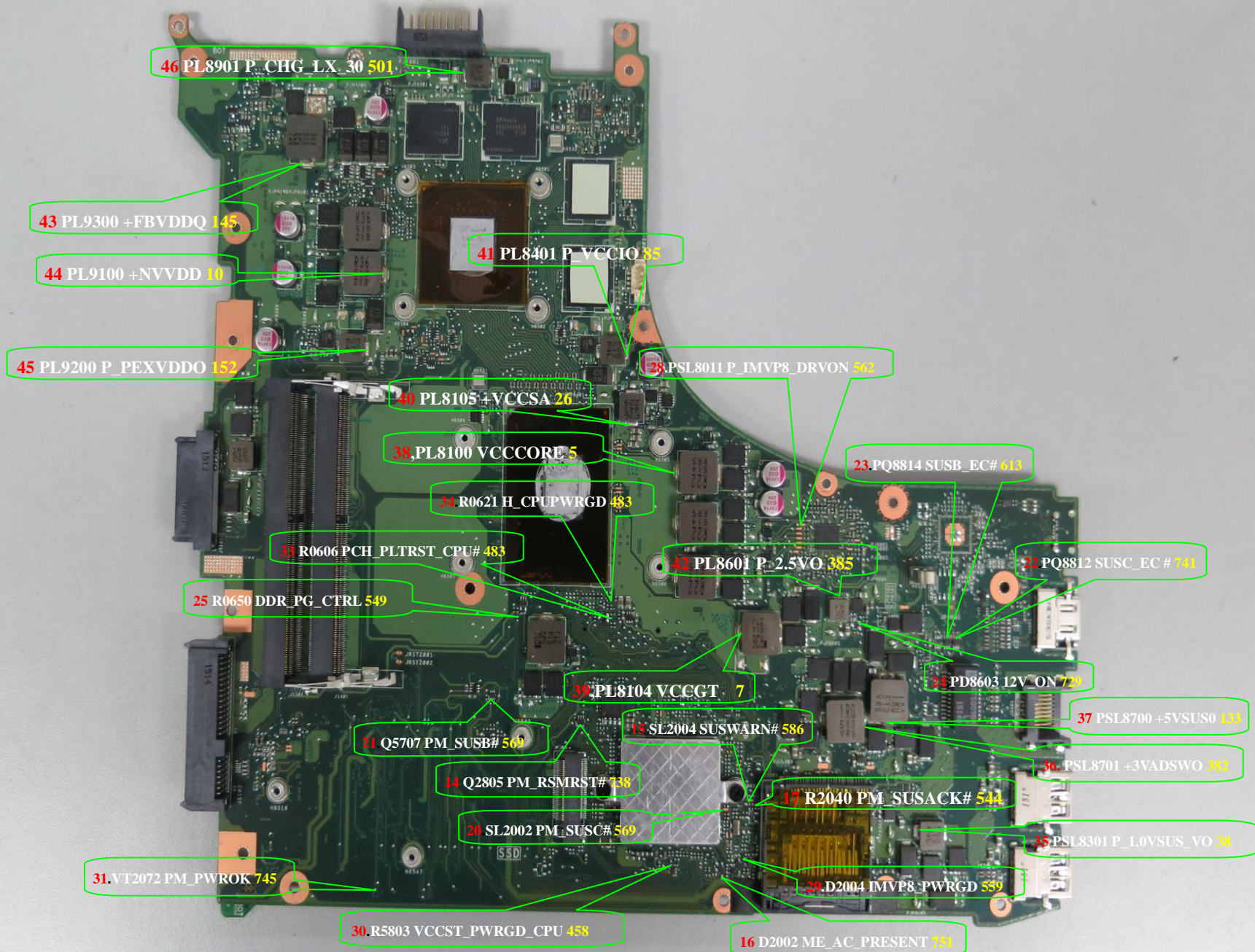
DC-IN Mode

C:CPU (+RTCBAT)+3VA_RTC
 P:PCH (AC_BAT_SYS)+3VA/+5VA
 S:PLT (+3VA_RTC)RTCRST#(PCH)
 Power (Power)AC_IN_OC#(EC)
 Signal (EC)PS_ON(+3VA_EC)
 (PS_ON)+3VA_EC(EC)
 (3VADSW_ON)+3VA_DSW(3VA_DSW_PWRGD)
 (EC)DPWRON_EC(PCH)
 (+3VA_DSW)PM_BATLOW#(PCH)
 (PCH)PM_SLP_SUS#(EC)
 (VSUS_ON)+1.0VSUS_VCCPRIM(1.0VSUS_PWRGD)
 (EC)PM_RSMRST#_PCH(PCH)
 (PCH)SUSWAKE#(EC)
 (EC)ME_AC_PRESENT_PCH(PCH)
 (EC)PCH_SUSACK#(PCH)
 (PWR_Switch)PWR_SW#(EC)
 (EC)PM_PWRBTN#(PCH)
 (EC)SUSC_EC#(Power)
 (SUSC_EC#)+12V/+5V/+3V
 (EC)SUSB_EC#(Power)
 (SUSB_EC#)+12VS/+5VS/+3VS
 (VSUS_ON)+1.0V_VCCST,VCCPLL(VCCST_PWRGD)
 (+VCCIO)+VCCSTG
 (1.2V_ON)+2.5V(2.5V_PWRGD)
 (1.2V_ON)+VDDQ_CPU(1.2V_PWRGD)
 (+12VS)+VCCPLL_OC
 (SUSB_EC#)+VCCIO(VCCIO_PWRGD)
 (ALL_SYSTEM_PWRGD)+VCCSA(IMVP8_PWRGD)
 (DDR_VTT_CTRL)+0.6V
 (CPU)DDR_VTT_CTRL(Power)
 (Power)1.2V_PWRGD(AND)
 (Power)IMVP8_PWRGD
 (AND)ALL_SYSTEM_PWRGD(CPU/PCH/EC/Power)
 (ALL_SYSTEM_PWRGD)VCCST_PWRGD_CPU(CPU)
 (EC)PM_PWRON_PCH(PCH)
 (PCH)CLK_PCH_BCLK(CPU)
 (PCH)H_CPUPWRGD(CPU)
 (ALL_SYSTEM_PWRGD)P_IMVP8_EN_10(Power)
 (CPU)P_SVID_DATA_X2(Power)
 (EC)PM_SYSFWROK_PCH(PCH)
 (PCH)PLT_RST#(CPU/EC/Device)
 (P_IMVP8_DRVON)+VCCCORE(IMVP8_PWRGD)
 (CPU)H_THERMTRIP#(PCH)
 (PCH)DDR4_DRAMRST#(Memory)
 +VCCGT



SLK H Power Sequence (DC mode)

Signal Measure Point-Bottom



Signal Measure Point-Top

