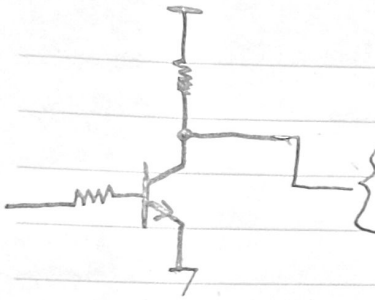


reaction times of not gate on 555 circuit

- High to low → 400ns
- low to high → 20µs

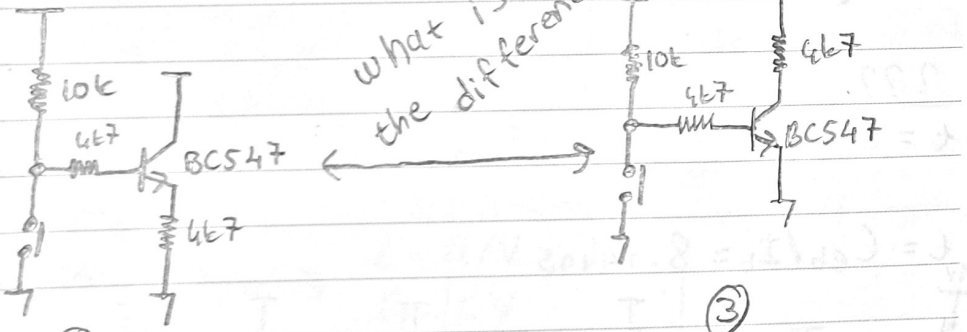
(output high to low or low to high)

with buttons and just not gate (the same)

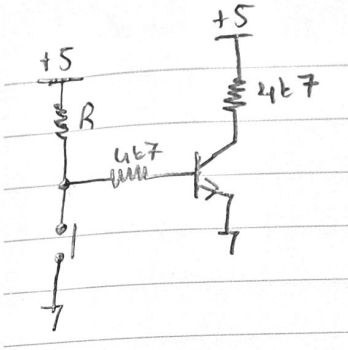
previous page circuits and photo numbers

④ high to low → 10ns

③ low to high → 1.1µs



② ③



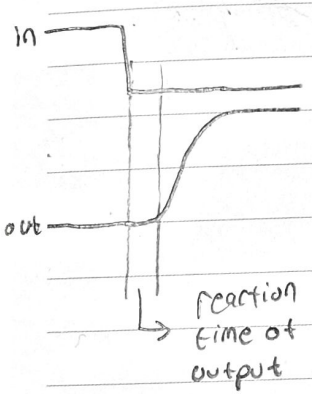
R = 100 → no difference

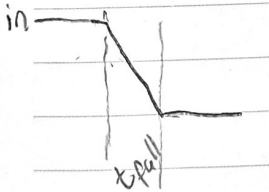
R = 82k → photo ⑤ no delay

R = 20k → nd

R = 30k

R	reaction time of output
10k	1.15-1.44µs
20k	.970-1.1µs
30k	670-740ns
40k	590-650ns sometimes 1.5µs
50k	550-950ns
60k	460-650ns
70k	440ns ....
80k	
220k	200-300ns





R = 10k → 20ns

R = 82k → 20ns

I pull down with button but, just curiosity

so it won't the delay of the transistor it was about circuit. this is why I didn't need to put capacitance after the not gate of rising edge detector.